

#### PowerDI123

### **Features**

- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- Patented Interlocking Clip Design for High Surge Current Capacity
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- An Automotive-Compliant Part is Available Under Separate Datasheet (<u>DFLS1200Q</u>)

#### **Mechanical Data**

- Package: PowerDI<sup>®</sup>123
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: Cathode Band
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (€3)
- Weight: 0.01 grams (Approximate)





Top View

### Ordering Information (Note 4)

Part Number	Paakaga	Packing		
Part Nullber	Package	Qty.	Carrier	
DFLS1200-7	PowerDI123	3000	Tape & Reel	

Notes:

1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

# **Marking Information**



F08 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: J = 2022) M = Month (ex: 9 = September)



Date Code Key

Year	2005		2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	S		J	К	L	М	Ν	0	Р	R	S	Т
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



# Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> VR	200	V
RMS Reverse Voltage	VR(RMS)	141	V
Average Forward Current	I <sub>F(AV)</sub>	1.0	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	IFSM	40	А

# **Thermal Characteristics**

Characteristic	Symbol	Тур	Мах	Unit
Thermal Resistance Junction to Ambient (Note 5)	Reja	132	—	°C/W
Thermal Resistance Junction to Soldering Point (Note 6)	Rejs	—	7	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-65 to	+175	°C

## Electrical Characteristics (@TA = +25°C, unless otherwise specified)

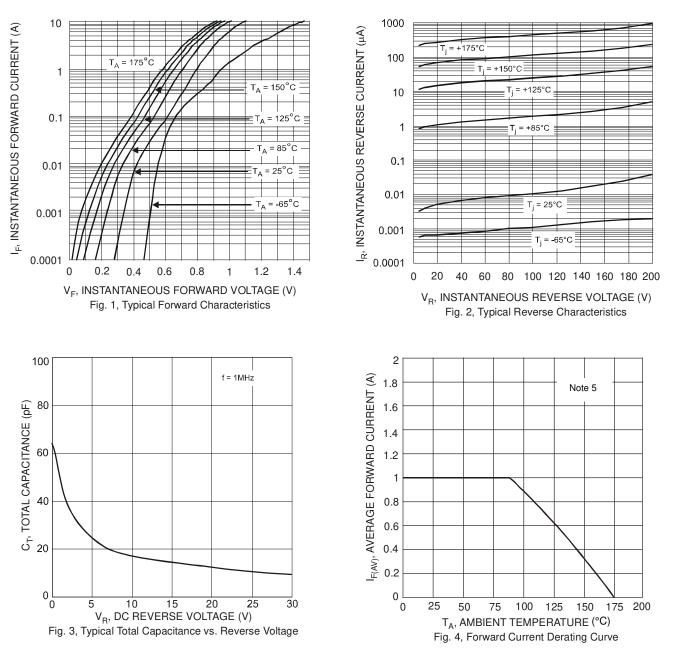
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 7)	V(BR)R	200	—		V	I <sub>R</sub> = 8μΑ
Forward Voltage	VF	_	_	0.85	V	IF = 1.0A
Leakage Current (Note 7)	IR	—	—	2	μA	VR = 200V, TA = +25°C
Total Capacitance	CT	_	23	_	pF	$V_R = 5V_{DC}, f = 1MHz$

Notes: 5. Part mounted on FR-4 board with 2 oz., minimum recommended copper pad layout, which can be found on our website at http://www.diodes.com/package-outlines.html. T<sub>A</sub> = +25°C.

6. Theoretical Reus calculated from the top center of the die straight down to the PCB/cathode tab solder junction.

7. Short duration pulse test used to minimize self-heating effect.

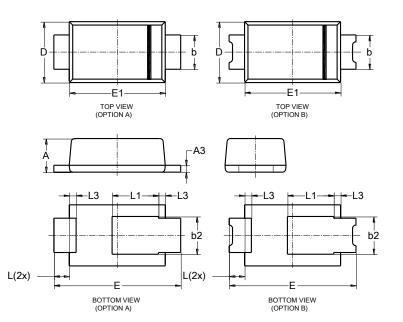






### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

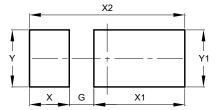


PowerDI123						
Dim	Min	Max	Тур			
Α	0.93	1.00	0.98			
A3	0.15	0.25	0.20			
b	0.85	1.25	1.00			
b2	1.025	1.125	1.10			
D	1.63	1.93	1.78			
E	3.50	3.90	3.70			
E1	2.60	3.00	2.80			
L	0.40	0.50	0.45			
L1	1.25	1.40	1.35			
L3	0.125	0.275	0.20			
All Dimensions in mm						

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI123



Dimensions	Value
Dimensions	(in mm)
G	0.65
X	1.05
X1	2.40
X2	4.10
Y	1.50
Y1	1.50

#### PowerDI123



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