

Data Sheet

Description

The CTNS-4606S is a 600 V, 60 A, fast recovery diode. The typical V_F of 1.15 V and the maximum t_{rr} of 150 ns ($I_F : I_{RP} = 1 : 1$) are realized by optimizing the trade-off relationship between V_F and t_{rr}. The low thermal resistance package achieves high performance in terms of heat dissipation.

Features

• V _{RSM}	600 V
• I _{F(AV)}	60 A
• V _F	
• $t_{rr1} (I_F = I_{RP})$	

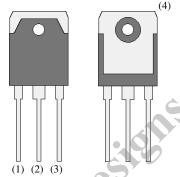
• Bare Lead Frame: Pb-free (RoHS Compliant)

Applications

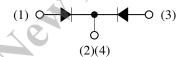
- PFC Crcuit (DCM and CRM)
- Ant Reconstruction of the second seco • Freewheel Diode (Offline Buck and Buck-boost Converter)

Package

TO3P-3L



Not to scale



- (1) Anode
- (2) Cathode
- (3) Anode
- (4) Cathode

CTNS-4606S

Absolute Maximum Ratings

Unless otherwise specified, $T_A = 25$ °C

Parameter	Symbol	Conditions	Rating	Unit
Peak Repetitive Reverse Voltage	V _{RSM}		600	V
Repetitive Reverse Voltage	V_{RM}		600	V
Average Forward Current	$I_{F(AV)}$	See Figure 1 and Figure 2	60	A
Surge Forward Current ⁽¹⁾	I_{FSM}	Half cycle sine wave, positive side, 10 ms, 1 shot	250	A
I ² t Limiting Value ⁽¹⁾	I^2t	$1 \text{ ms} \le t \le 10 \text{ ms}$	312.5	A^2s
Junction Temperature	T_{J}		-40 to 150	°C
Storage Temperature	T_{STG}		-40 to 150	°C

Electrical Characteristics

Unless otherwise specified, $T_A = 25$ °C

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage Drop ⁽¹⁾	V_{F}	$T_J = 25 ^{\circ}\text{C}, I_F = 30 \text{A}$	_	1.15	1.3	V
		$T_J = 100 ^{\circ}\text{C}, I_F = 30 \text{A}$	_	1.1	_	V
Reverse Leakage Current ⁽¹⁾	I_R	$V_R = V_{RM}$	_	_	50	μΑ
Reverse Leakage Current Under High Temperature ⁽¹⁾	$H \cdot I_R$	$V_R = V_{RM}$, $T_J = 150$ °C	_	_	20	mA
Reverse Recovery Time ⁽¹⁾	t _{rr1}	$I_F = I_{RP} = 500 \text{ mA}$ 90% recovery point, $T_J = 25 ^{\circ}\text{C}$	_	_	150	ns
	t _{m2}	$I_F = 500 \text{ mA},$ $I_{RP} = 1000 \text{ mA},$ 75% recovery point, $T_J = 25 \text{ °C}$	_		100	ns
Thermal Resistance (2)	$R_{\text{th(J-C)}}$				1.0	°C/W
Thermal Resistance (2)						

⁽¹⁾ The rating of one chip.

 $^{^{(2)}\,}R_{\text{th (J-C)}}$ is thermal resistance between junction and the case

Rating and Characteristic Curves

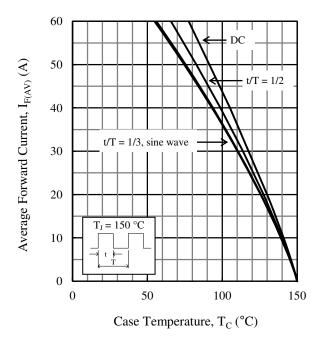


Figure 1. $I_{F(AV)}$ vs. T_C Typical Characteristics $(V_R = 0 \ V)$

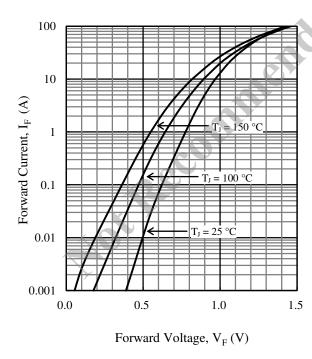


Figure 3. V_F vs. I_F Typical Characteristics

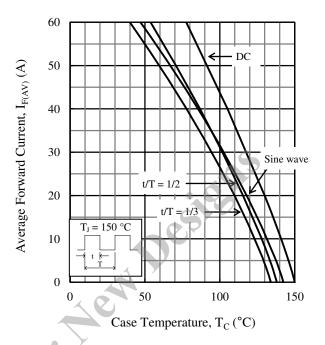


Figure 2. $I_{F(AV)}$ vs. T_C Typical Characteristics $(V_R = 600 \text{ V})$

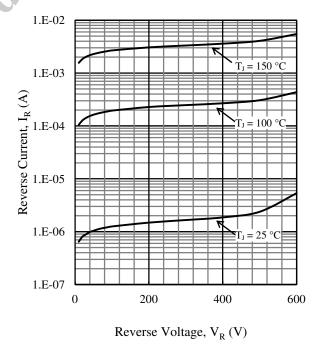
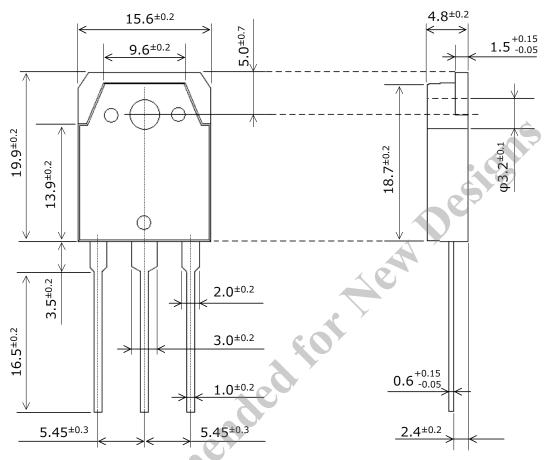


Figure 4. V_R vs. I_R Typical Characteristics

Physical Dimensions

• TO3P-3L



NOTES:

- Dimensions in millimeters
- Bare lead frame: Pb-free (RoHS compliant)
- When soldering the products, it is required to minimize the working time, within the following limits: Flow: 260 ± 5 °C / 10 ± 1 s, 2 times
 - Soldering Iron: 380 ± 10 °C / 3.5 ± 0.5 s, 1 time (Soldering should be at a distance of at least 1.5 mm from the body of the product.)
- Recommended screw torque for TO3P: 0.686 N·m to 0.882 N·m (7 kgf·cm to 9 kgf·cm)

Marking Diagram

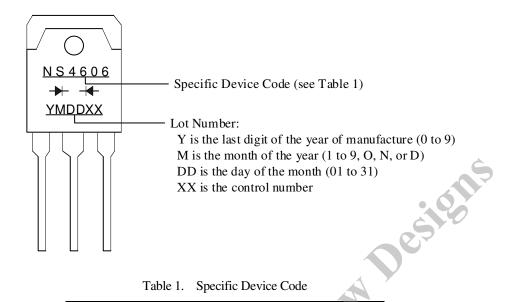


Table 1. Specific Device Code

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