

Nch 600V 59mohm(typ.) Power MOSFET

$V_{DSS}(@\text{Tj max.})^{*5}$	650V
R _{DS(on)} (Max.)	0.071Ω
I _{DP} *2	±165A
P _D	100W

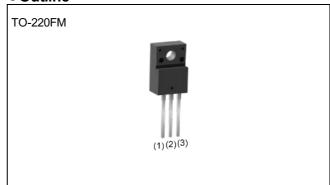
Features

- 1) Fast reverse recovery time (trr)
- 2) Low on-resistance
- 3) Fast switching speed
- 4) Drive circuits can be simple
- 5) Pb-free plating; RoHS compliant
- 6) Halogen free mold compound

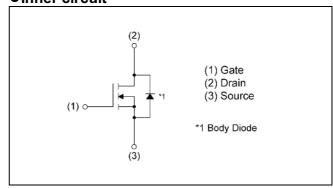
Application

Switching applications

Outline



•Inner circuit



Marking	R6055VNX

● **Absolute maximum ratings** (T_a = 25°C, unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain - Source voltage	V _{DSS}	600	V
Continuous drain current (T _c = 25°C)	I _D *1	±23	Α
Pulsed drain current	I _{DP} *2	±165	Α
Gate - Source voltage	V _{GSS}	±30	V
Avalanche current, single pulse	I _{AS} *3	4.6	А
Avalanche energy, single pulse	E _{AS} *3	233	mJ
MOSFET dv/dt	dv/dt*4	120	V/ns
Power dissipation (T _c = 25°C)	P _D	100	W
Junction temperature	T _j	150	°C
Operating junction and storage temperature range	T _{stg}	-55 to +150	°C

●Thermal resistance

Doromotor	Cymah al	Values			1.1
Parameter	Symbol	Min.	Тур.	Max.	Unit
Thermal resistance, junction - case	R _{thJC}	-	-	1.25	°C/W
Thermal resistance, junction - ambient	R _{thJA}	-	-	62.5	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	°C

● Electrical characteristics (T_a = 25°C)

Darameter	Cumb al	Conditions	Values			Unit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Offic
Drain - Source breakdown voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 1mA	600	-	1	V
Zero gate voltage drain current	I _{DSS}	V _{DS} = 600V, V _{GS} = 0V	-	-	100	μA
Gate - Source leakage current	I _{GSS}	$V_{GS} = \pm 30 V, V_{DS} = 0 V$	-	-	±100	nA
Gate threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 1.5 \text{mA}$	4.5	5.5	6.5	V
Static drain - source	D *5	V _{GS} = 15V, I _D = 16A	-	0.059	0.071	Ω
on - state resistance	R _{DS(on)} *5	V _{GS} = 10V, I _D = 16A	-	0.066	0.080	Ω
Gate resistance	R_{G}	f = 1MHz, open drain	-	1.1	-	Ω

● Electrical characteristics (T_a = 25°C)

Davanastan	Currente e l	Conditions	Values			Linit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 100V	-	3700	-	
Output capacitance	C _{oss}	f = 100kHz	-	135	-	
Effective output capacitance energy related	C _{o(er)} *6	V _{GS} = 0V	-	120	-	pF
Effective output capacitance time related	C _{o(tr)} *7	V _{DS} = 0V to 480V	-	750	1	
Turn - on delay time	t _{d(on)} *5	$V_{DD} \simeq 300V$, $V_{GS} = 15V$	-	35	-	
Rise time	t _r *5	I _D = 16A	-	35	-	
Turn - off delay time	t _{d(off)} *5	R _L ~ 18.8Ω	-	105	-	ns
Fall time	t _f *5	$R_G = 10\Omega$	-	26	-	

• Gate charge characteristics ($T_a = 25$ °C)

Darameter	Cumb al	Conditions	Values			Lloit
Parameter	Symbol Conditions -		Min.	Тур.	Max.	Unit
Total gate charge	Q_g^{*5}	V _{DD} ≈ 300V	-	80	1	
Gate - Source charge	Q _{gs} *5	I _D = 16A	-	29	-	nC
Gate - Drain charge	Q _{gd} *5	V _{GS} = 10V	-	37	-	
Gate plateau voltage	V _(plateau)	V _{DD} ≈ 300V, I _D = 16A	-	7.8	-	V

● Body diode electrical characteristics (Source-Drain) (T_a = 25°C)

Damanatan	0	O Est	Values			Lloit
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Source current	I _S *1	T = 25°C	-	-	23	Α
Pulsed source current	l _{SP} *2	T _C = 25°C	-	-	165	Α
Source-Drain voltage	V_{SD}^{*5}	V _{GS} = 0V, I _S = 16A	-	-	1.5	V
Reverse recovery time	t _{rr} *5	- V _{DD} ≃ 400V	-	112	-	ns
Reverse recovery charge	Q _{rr} *5	I _S = 16A	-	575	-	nC
Peak reverse recovery current	l _{rr} *5	di/dt = 100A/μs	-	10	-	А

^{*1} Limited only by maximum temperature allowed.

^{*2} Pw \leq 10µs, Duty cycle \leq 1%

^{*3} L \simeq 20mH, V_{DD} = 50V, R_G = 25 Ω , starting T_i = 25°C

^{*4} V_{DS} = 0 to 400V

^{*5} Pulsed

^{*6} Co(er) is a fixed capacitance that gives the same stored energy as Coss while V_{DS} is rising from 0 to 80% V_{DSS} .

^{*7} Co(tr) is a fixed capacitance that gives the same charging time as Coss while V_{DS} is rising from 0 to 80% V_{DSS} .

Fig.1 Power Dissipation Derating Curve

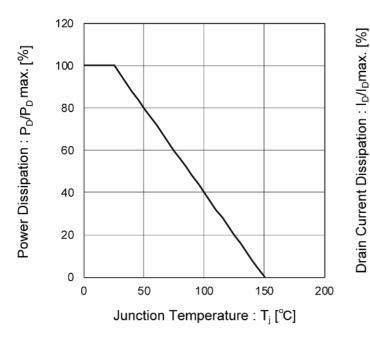


Fig.2 Drain Current Derating Curve

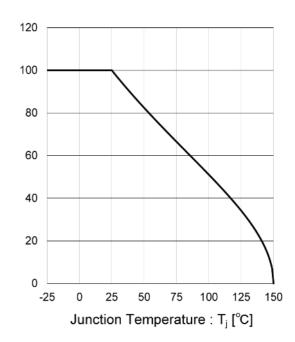


Fig.3 Normalized Transient Thermal Resistance vs. Pulse Width

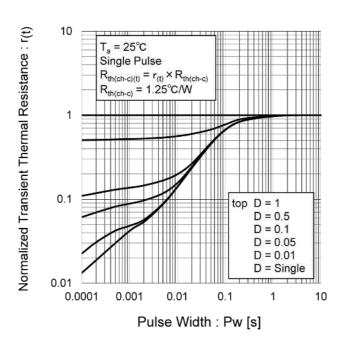


Fig.4 Maximum Safe Operating Area

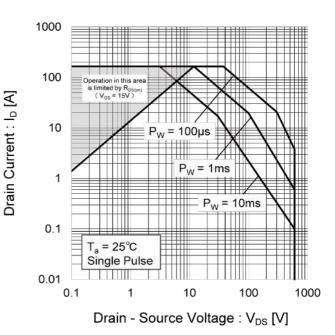


Fig.5 Avalanche Energy Derating Curve

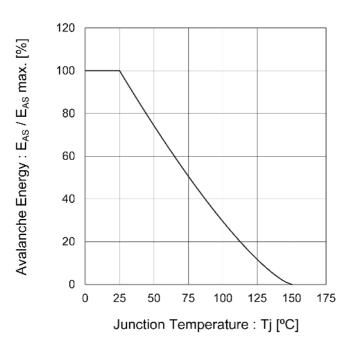


Fig.6 Normalized Breakdown Voltage vs. Junction Temperature

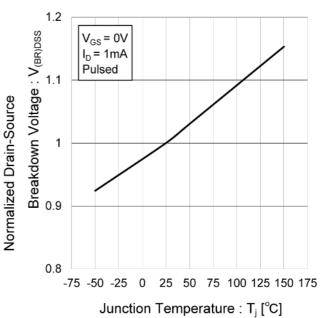


Fig.7 Output Characteristics(I)

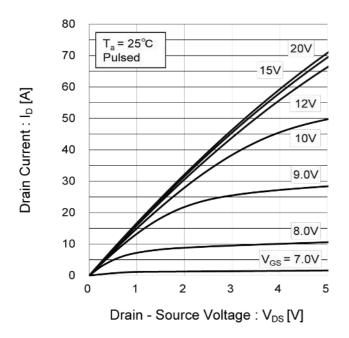


Fig.8 Output Characteristics(II)

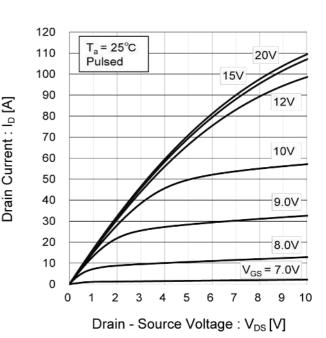


Fig.9 Gate Threshold Voltage vs. Drain current

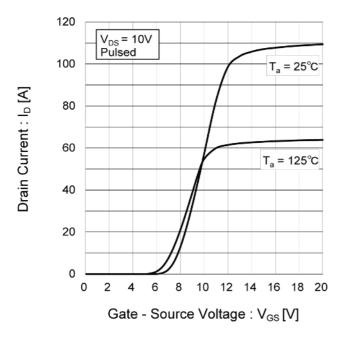


Fig.10 Normalized Gate Threshold .

Voltage vs Junction Temperature

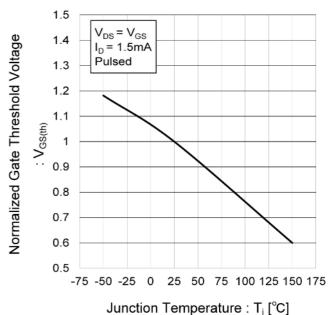


Fig.11 Static Drain - Source On - State Resistance vs. Gate Source Voltage

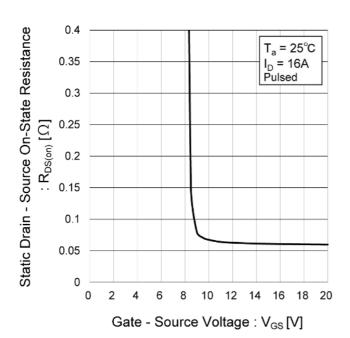


Fig.12 Normalized Static Drain - Source On - State Resistance vs. Junction Temperature

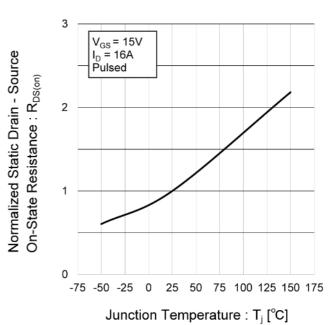


Fig.13 Static Drain - Source On - State Resistance vs. Drain Current

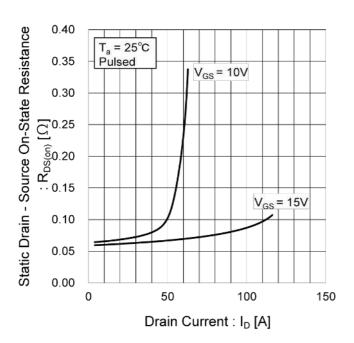


Fig.14 Capacitances

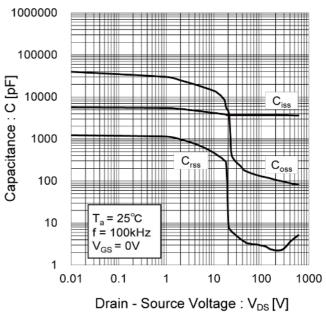


Fig.15 Coss Stored Energy

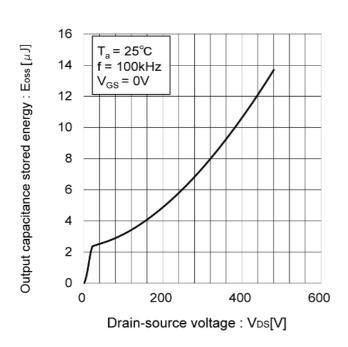
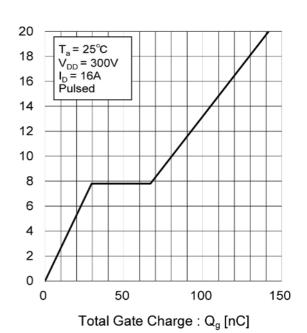
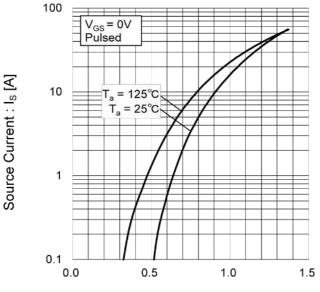


Fig.16 Gate Charge



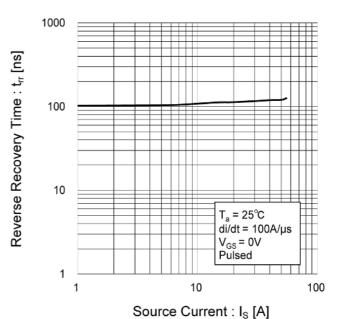
Gate - Source Voltage : V_{GS} [V]

Fig.17 Source Current vs. Source - Drain Voltage



Source - Drain Voltage : V_{SD} [V]

Fig.18 Reverse Recovery Time vs. Source Current



Measurement circuits

Fig.1-1 Switching Time Measurement Circuit

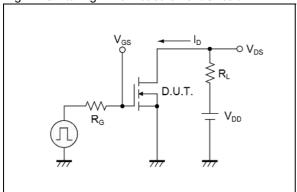


Fig.2-1 Gate Charge Measurement Circuit

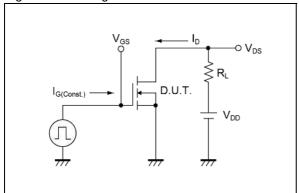


Fig.3-1 Avalanche Measurement Circuit

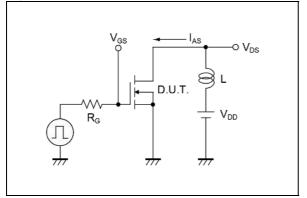


Fig.4-1 Diode Recovery Measurement Circuit

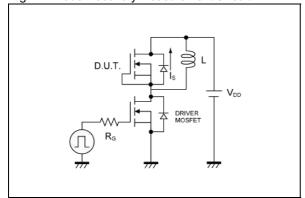


Fig.1-2 Switching Waveforms

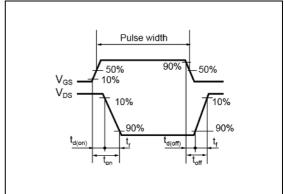


Fig.2-2 Gate Charge Waveform

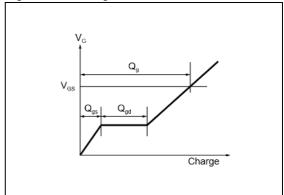


Fig.3-2 Avalanche Waveform

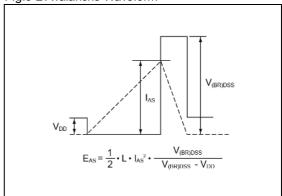
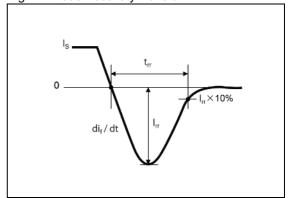
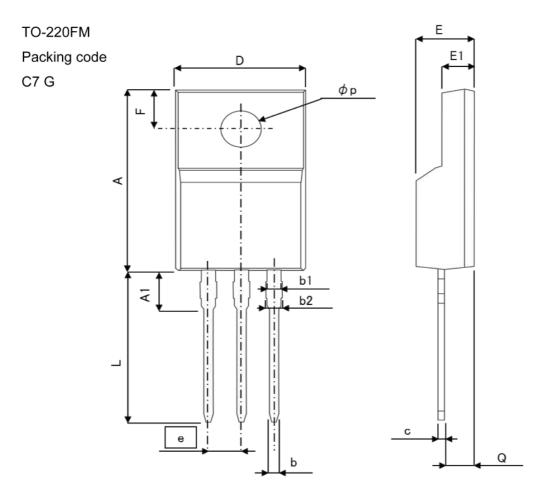


Fig.4-2 Diode Recovery Waveform



Dimensions



DIM	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
Α	15.67	16.27	0.617	0.641
A1	3.03	3.43	0.119	0.135
b	0.70	0.95	0.028	0.037
b1	1.00	1.40	0.039	0.055
b2	1.10	1.50	0.043	0.059
С	0.45	0.65	0.018	0.026
D	9.90	10.30	0.390	0.406
E	4.60	5.00	0.181	0.197
E1	2.44	2.74	0.096	0.108
е	2.	54	0.1	00
F	3.10	3.50	0.122	0.138
L	12.6	13.6	0.946	0.535
р	2.98	3.38	0.117	0.133
Q	2.25	3.25	0.089	0.128

Dimension in mm/inches



Notice

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JAPAN	USA	EU	CHINA
CLASSⅢ	CL ACCTI	CLASS II b	СГУССШ
CLASSIV	CLASSII	CLASSⅢ	CLASSⅢ

- 2. ROHM designs and manufactures its Products subject to strict quality control system. However, semiconductor products can fail or malfunction at a certain rate. Please be sure to implement, at your own responsibilities, adequate safety measures including but not limited to fail-safe design against the physical injury, damage to any property, which a failure or malfunction of our Products may cause. The following are examples of safety measures:
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 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (Exclude cases where no-clean type fluxes is used. However, recommend sufficiently about the residue.); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse, is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- Even under ROHM recommended storage condition, solderability of products out of recommended storage time period
 may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is
 exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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