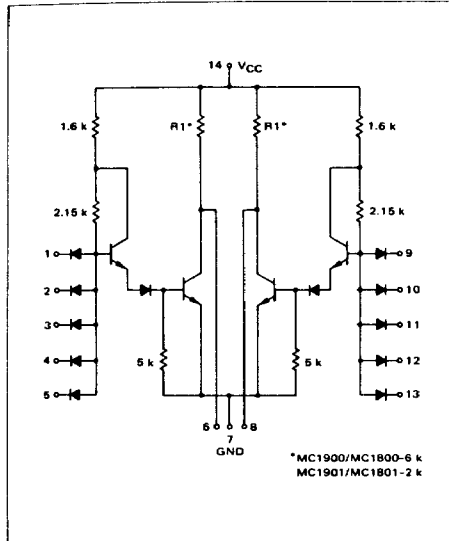


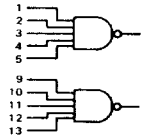
DUAL 5-INPUT "NAND" GATE

MDTL MC930/830 series

MC1900F · MC1800F, P
MC1901F · MC1801F, P



This gate element, in the 14-pin flat and dual in-line packages, consists of two 5-input NAND gate circuits. The elements may be cross-coupled to form a bistable multivibrator, or the outputs may be connected in parallel to perform the logic "OR" function.



Positive Logic: 6 = 1·2·3·4·5

Negative Logic: 6 = 1+2+3+4+5

Input Loading Factor = 1

Output Loading Factor:

MC1900/MC1800 = 8

MC1901/MC1801 = 7

Total Power Dissipation:

MC1900/MC1800 = 22 mW typ/pkg

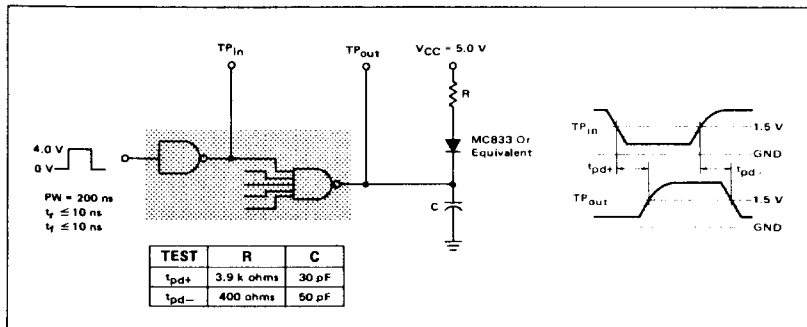
MC1901/MC1801 = 33 mW typ/pkg

Propagation Delay Time:

MC1900/MC1800 = 30 ns typ

MC1901/MC1801 = 25 ns typ

SWITCHING TIME TEST CIRCUIT AND WAVEFORMS



PRODUCT DOCUMENTATION

The three documents listed in the following table are required for a complete description of the DSP56301 and are necessary to design properly with the part. Documentation is available from one of the following locations (see back cover for detailed information):

- A local Motorola distributor
- A Motorola semiconductor sales office
- A Motorola Literature Distribution Center
- The World Wide Web (WWW)

See the **Additional Support** section of the *DSP56300 Family Manual* for detailed information on the multiple support options available to you.

Table 1 DSP56301 Documentation

Name	Description	Order Number
DSP56300 Family Manual	Detailed description of the DSP56300 family processor core and instruction set	DSP56300FM/AD
DSP56301 User's Manual	Detailed functional description of the DSP56301 memory configuration, operation, and register programming	DSP56301UM/AD
DSP56301 Technical Data	DSP56301 features list and physical, electrical, timing, and package specifications	DSP56301/D

