

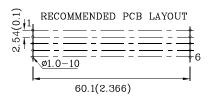
Part Number: XDMDK57C

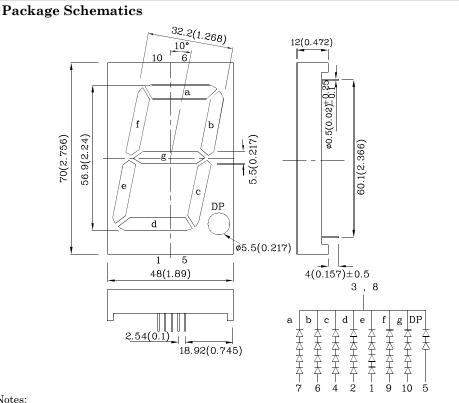
57mm (2.3") SINGLE DIGIT NUMERIC DISPLAY

Features

- \bullet Low power consumption
- \bullet Robust package
- I.C. Compatible
- Standard configuration: Gray face w/ white segments
- Optional black face provides superior color contrast
- RoHS Compliant







Notes: 1. All dimensions are in millimeters (inches), Tolerance is ±0.25(0.01")unless otherwise noted. 2. Specifications are subject to change without notice.

Absolute Maximum Ratings (T _A =25°C)		MDK (AlGaInP)	Unit	
Reverse Voltage (Per Chip) Vi		5	V	
Forward Current (Dp)	$I_{\rm F} = \begin{array}{c} 30 \\ (30) \end{array}$		mA	
Forward Current (Peak) 1/10 Duty Cycle 0.1ms Pulse Width(Dp)	ifs	185 (185)	mA	
Power Dissipation (Per Chip)	P_{D}	150	mW	
Operating Temperature	TA	$-40 \sim +85$	°C	
Storage Temperature	Tstg	$-40 \sim +85$		
Lead Solder Temperature [2mm Below Package Base]	260°C For 3-5 Seconds			

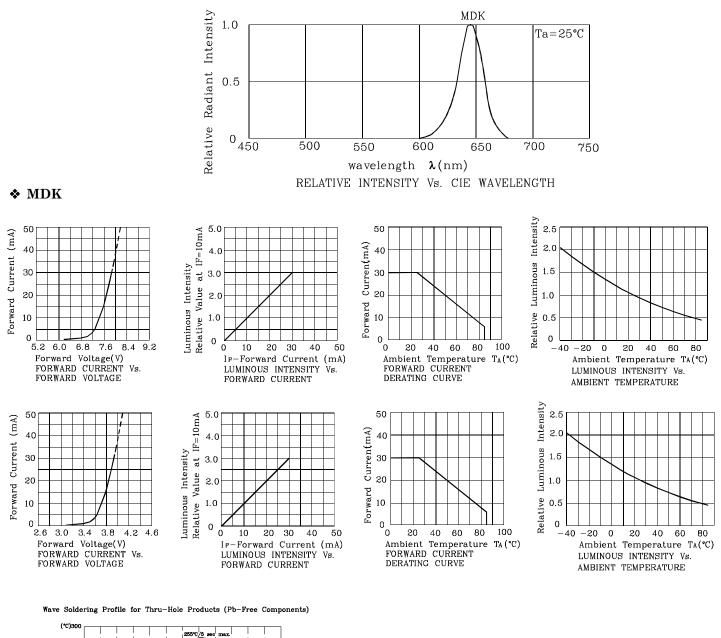
Operating Characteristics (T _A =25°C)		MDK (AlGaInP)	Unit
Forward Voltage (Typ.) (Dp) (I _F =10mA)	V_{F}	7.4 (3.7)	V
Forward Voltage (Max.) (Dp) (I _F =10mA)	V_{F}	10 (5.0)	V
Reverse Current (Max.) (Per Chip) (V _R =5V)	I_{R}	10	uA
Wavelength of Peak Emission CIE127-2007* (Typ.) (I _F =10mA)	λP	645*	nm
Wavelength of Dominant Emission CIE127-2007* (Typ.) (I _F =10mA)	λD	630*	nm
Spectral Line Full Width At Half-Maximum (Typ.) (I _F =10mA)	$ riangle\lambda$	28	nm
Capacitance (Typ.) (V _F =0V, f=1MHz)	С	35	pF

Part Number	Emitting Color	Emitting Material	Luminous Intensity CIE127-2007* (IF=10mA) ucd		Wavelength CIE127-2007* nm λP	Description
			min.	typ.		
XDMDK57C	Red	AlGaInP		439990 119990*	645*	Common Cathode, Rt. Hand Decimal

*Luminous intensity value and wavelength are in accordance with CIE127-2007 standards. Mar 10.2014

XDSB7717 V1-X Layout: Maggie L.





Remarks:

1. Wavelength: +/-1nm

3. Forward Voltage: +/-0.1V

250 200 150 100 50

1. Recommend pre-h thermocouple atta wave with a maxim pre-heat temperature of 105°C or less (as measured attached to the LED pins) prior to immersion in t maximum solder bath temperature of 260°C oldering temperature between 245°C ~ 255°C for 3 s

Time(sec)

3.Do not apply stress to the epoxy resin while the temperature is above 4.Fixtures should not incur stress on the component when mounting and 85°C

during solidering process: success on the component when inducting SAG 205 solider aloy is recommended.
6.No more than one wave soldering pass.
7.During wave soldering, the PCB top-surface temperature should be kept below 105°C.

If special sorting is required (e.g. binning based on forward voltage,

luminous intensity / luminous flux, or wavelength),

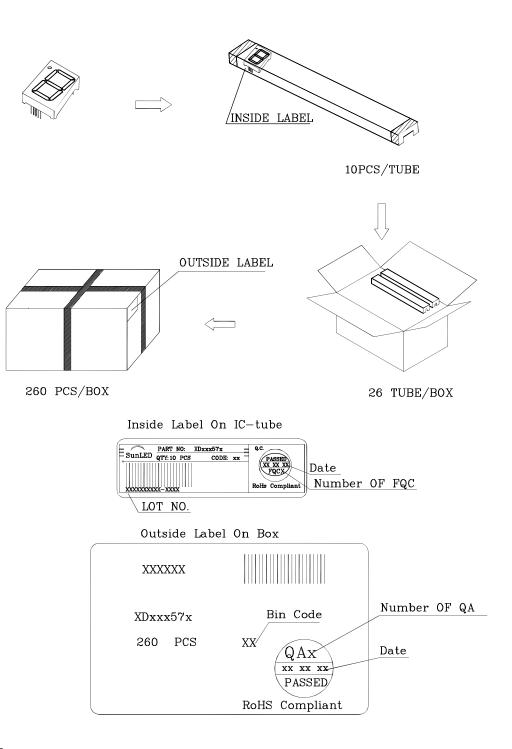
2. Luminous Intensity / Luminous Flux: +/-15%

the typical accuracy of the sorting process is as follows:

Note: Accuracy may depend on the sorting parameters.



PACKING & LABEL SPECIFICATIONS



TERMS OF USE

- 1. Data presented in this document reflect statistical figures and should be treated as technical reference only.
- 2. Contents within this document are subject to improvement and enhancement changes without notice.
- 3. The product(s) in this document are designed to be operated within the electrical and environmental specifications indicated on the datasheet.
- User accepts full risk and responsibility when operating the product(s) beyond their intended specifications.
- 4. The product(s) described in this document are intended for electronic applications in which a person's life is not reliant upon the LED. Please consult with a SunLED representative for special applications where the LED may have a direct impact on a person's life.
- 5. The contents within this document may not be altered without prior consent by SunLED.
- 6. Additional technical notes are available at http://www.SunLEDusa.com/TechnicalNotes.asp