

Features

- Trench Power LV MOSFET Technology
- Excellent Package for Heat Dissipation
- + High Density Cell Design for Low $R_{\text{DS}(\text{ON})}$
- Halogen Free. "Green" Device (Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)
- Moisture Sensitivity Level 1

Maximum Ratings

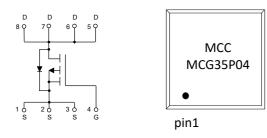
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 3.3°C/W Junction to Case⁽²⁾
- Thermal Resistance: 150°C/W Junction to Ambient⁽²⁾

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	-40	V
Gate-Source Volltage	V _{GS}	±20	V
Continuous Drain Current	I _D	-35	Α
Pulsed Drain Current ⁽³⁾	I _{DM}	-140	Α
Total Power Dissipation	P _D	38	W
Single Pulsed Avalanche Energy ⁽⁴⁾	E _{AS}	50	mJ

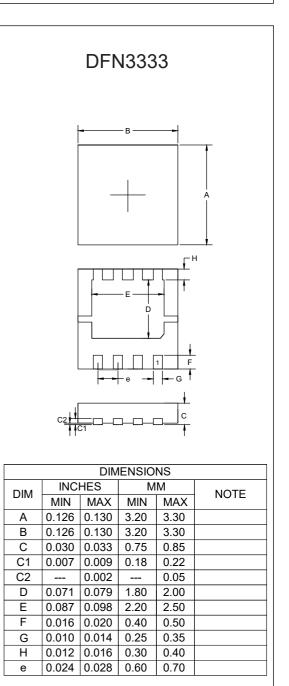
Note:

- 1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 2. The Value of R_{6JA} is Measured with the Device Mounted on 1in2 FR-4 Board with 2oz. Copper, in a Still Air Environment with T_A=25°C. The Value in Any Given Application Depends on the User's Specific Board Design.
 - the Value of R_{BJC} is Measured with Surface Mounted on 1 in^2 $\mbox{ pad area, t } {\leq} 10$ sec.
- 3. Pulse Test: Pulse Width \leq 300us,Duty cycle \leq 2%.
- 4. V_{DS} =-35V, V_{GS} =-10V, L=1mH.
- 5. For design aid only, not subject to production testing.

Internal Structure and Marking Code







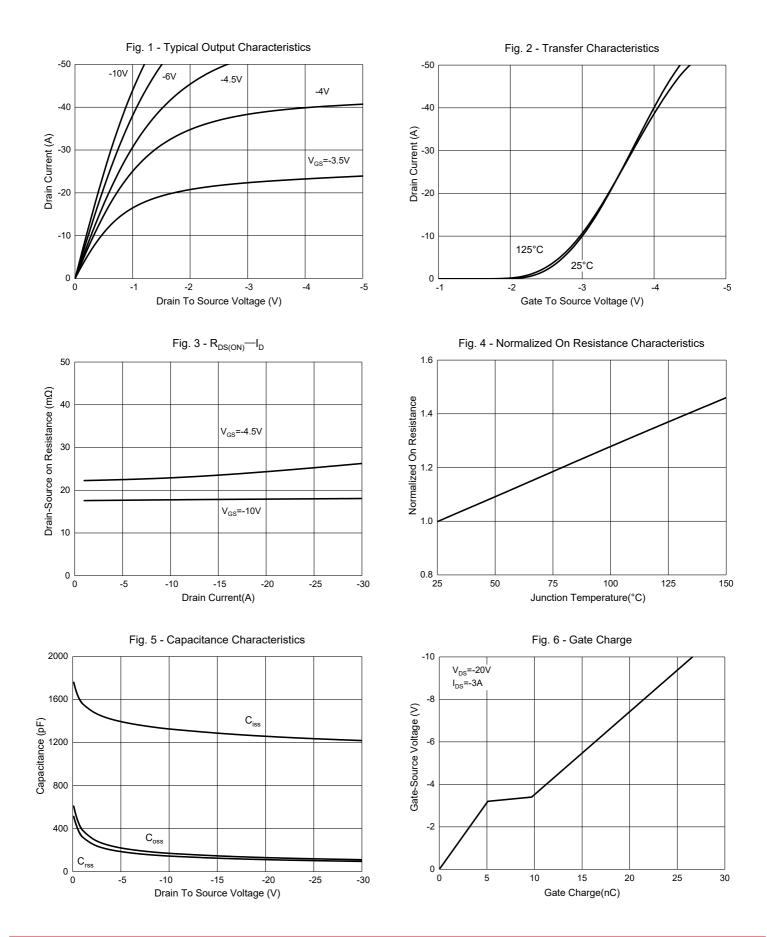


Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static Characteristics			I	1	1		
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =-250µA	-40			V	
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-40V, V _{GS} =0V			-1	μA	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=-250\mu A$	-1	-1.7	-2.5	V	
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =-10V, I _D =-15A		17	25	mΩ	
		V _{GS} =-4.5V, I _D =-10A		25	35	mΩ	
Forward Transconductance ⁽³⁾⁽⁵⁾	gfs	V _{DS} =-5V, I _D =-1A		5.5		S	
Diode Characteristics							
Continuous Body Diode Current	I _S				-35	A	
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =-20A			-1.3	V	
Reverse Recovery Time	t _{rr}			21.5		ns	
Reverse Recovery Charge	Q _{rr}	I _F =-12A, dI _F /dt=100A/μs		5		nC	
Dynamic Characteristics				1	1	<u>.</u>	
Input Capacitance	C _{iss}			1257			
Output Capacitance	C _{oss}	V _{DS} =-20V,V _{GS} =0V,f=1MHz		129		pF	
Reverse Transfer Capacitance	C _{rss}			110		1	
Total Gate Charge	Qg			26.6			
Gate-Source Charge	Q _{gs}	V _{DS} =-20V,V _{GS} =-10V,I _D =-3A		5.1		nC	
Gate-Drain Charge	Q _{gd}			4.6			
Turn-On Delay Time	t _{d(on)}			5.4			
Turn-On Rise Time	t _r	V _{DS} =-20V, V _{GS} =-10V,		21.5			
Turn-Off Delay Time	t _{d(off)}	R _G =3.3Ω, I _{DS} =-3A		94.7		- ns	
Turn-Off Fall Time	t _f			45.2		1	



Curve Characteristics





Curve Characteristics

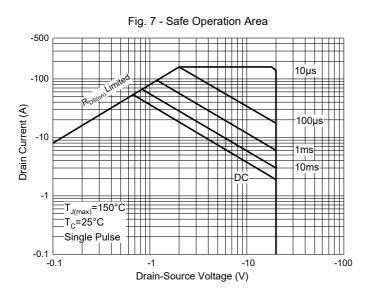
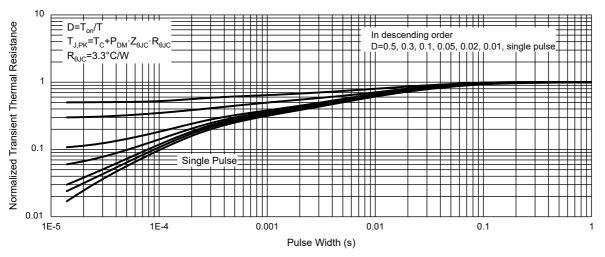


Fig. 8 - Normalized Maximum Transient Thermal Impedance







Ordering Information

Device	Packing	
Part Number-TP	Tape&Reel: 5Kpcs/Reel	

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