

ON Semiconductor®

74AC32, 74ACT32 Quad 2-Input OR Gate

Features

- I_{CC} reduced by 50% on 74AC only
- Outputs source/sink 24mA
- ACT32 has TTL-compatible inputs

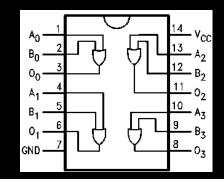
Ordering Information

Package
NumberPackage
Number74AC32SCM14A14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow74AC32SJM14D14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide74AC32MTCMTC1414-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm
Wide74AC32PCN14A14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide74ACT32SCMTC1414-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow
Wide74ACT32MTCMTC1414-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm
Wide74ACT32PCN14A14-Lead Plastic Dual-In-Line Package (TSSOP), JEDEC MO-153, 4.4mm
Wide

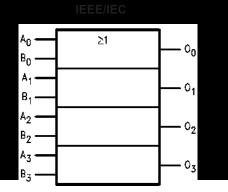
Device also available in Tape and Reel. Specify by appending suffix letter "X" to the ordering number.

🕖 All packages are lead free per JEDEC: J-STD-020B standard.

Connection Diagram



Logic Symbol



Pin Description

Pin Names	Description
A _n , B _n	Inputs
O _n	Outputs

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Rating
V _{CC}	Supply Voltage	–0.5V to +7.0V
l _{IK}		
V		
I _{OK}		
V _O		
Ι _Ο		
I _{CC} or I _{GND}		
T _{STG}		
TJ		

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. ON Semiconductor does not recommend exceeding them or designing to absolute maximum ratings.

T _A = +25°C	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	
Гур.	Guaranteed Limits	Units
1.5 2.1	2.1	V
2.25 3.1	3.15	

DC Electrical Characteristics for AC

Parameter

Symbol

V_{CC}

(V)

 V_{IH} Minimum HIGH Level 3.0 $V_{OUT} = 0.1V$ or Input Voltage $V_{CC} - 0.1V$ 4.5

Conditions

74AC32,
74ACT32 -
— Quad 2
-Input OF
R Gate

DC Electrical Characteristics for ACT

		V _{cc}	Vcc		⊦25°C	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$	
Symbol	ymbol Parameter		Conditions	Typ. G		Juaranteed Limits	Units
V _{IH}	Minimum HIGH Level	4.5	$V_{OUT} = 0.1V$ or	1.5	2.0	2.0	V
Input Voltage	Input Voltage	5.5	V _{CC} – 0.1V	1.5	2.0	2.0	1
V _{IL} Maximum LOW Level	4.5	$V_{OUT} = 0.1V$ or	1.5	0.8	0.8	V	

Notes:

4. All outputs loaded; thresholds on input associated with output under test.

5. Maximum test duration 2.0ms, one output loaded at a time.

AC Electrical Characteristics for AC

			T _A = +25°C, C _L = 50pF		$ \begin{array}{c} T_{A}=-40^{\circ}C \text{ to } +85^{\circ}C,\\ C_{L}=50pF \end{array} $			
Symbol	Parameter	V _{CC} (V) ⁽⁶⁾	Min.	Тур.	Max.	Min.	Max.	Units
t _{PLH}	Propagation Delay	3.3	1.5	7.5	9.0	1.5	10.0	ns
		5.0	1.5	5.5	7.5	1.0	8.5	

Note:

 δ . Voltage range 3.3 is 3.3V \pm 0.3V. Voltage range 5.0 is 5.0V \pm 0.5V.

AC Electrical Characteristics for ACT

Symbol							
t _{PLH}							
t _{PHL}							

Note:

7. Voltage Range 5.0 is 5.0V \pm 0.5V.

Capacitance

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