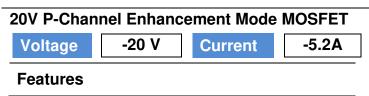
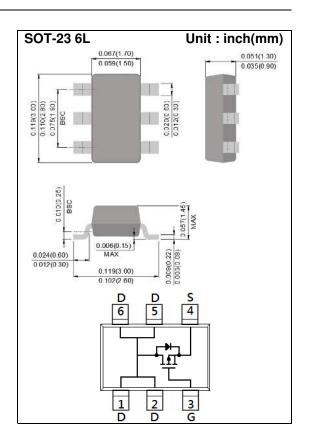
	10 M I I I I I I I I I I I I I I I I I I
ΡΛΝ	JIT
	SEMI
	CONDUCTOR



- RDS(ON) , VGS@-4.5V, ID@-5.2A<46mΩ
- Rds(on) , Vgs@-2.5V, Id@-3.0A<56m $\Omega$
- RDS(ON) , VGS@-1.8V, ID@-1.5A<88mΩ
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

#### **Mechanical Data**

- Case: SOT-23 6L Package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 0.0005 ounces, 0.014 grams



#### Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V <sub>DS</sub>	-20	V
Gate-Source Voltage		V <sub>GS</sub>	<u>+</u> 12	V
Continuous Drain Current		Ι <sub>D</sub>	-5.2	А
Pulsed Drain Current		ldм	-20.8	А
Power Dissipation	T <sub>a</sub> =25⁰C	PD	2	W
	Derate above 25°C		16	mW/°C
Operating Junction and Storage Temperature Range		T <sub>J</sub> ,Т <sub>STG</sub>	-55~150	٥C
Typical Thermal Resistance - Junction to Ambient <sup>(Note 3</sup>	R <sub>θJA</sub>	62.5	°C/W	



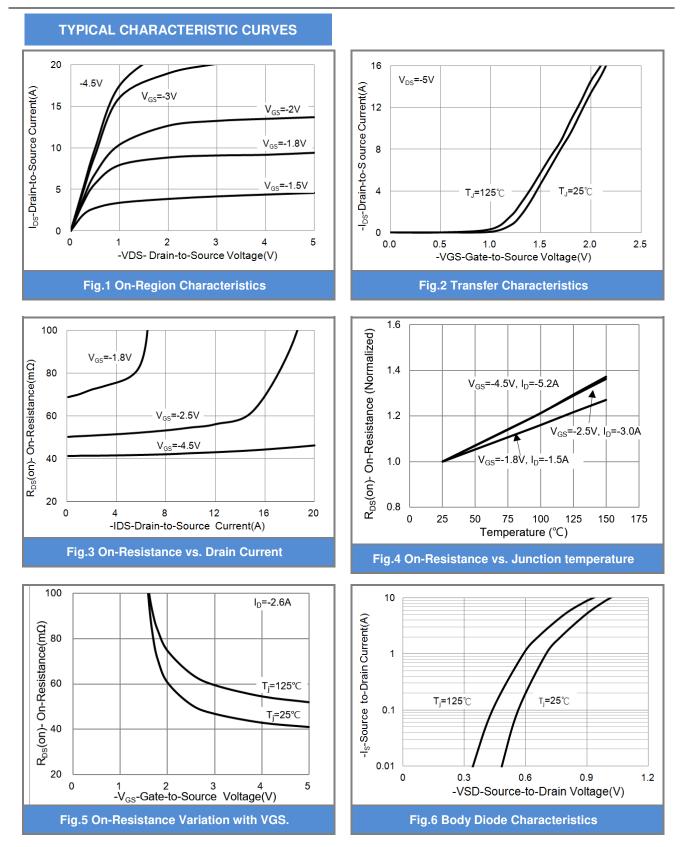
### Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static		·				
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-20	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	-0.5	-0.74	-1.3	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-5.2A	-	38	46	mΩ
		$V_{GS}$ =-2.5V, I <sub>D</sub> =-3.0A	-	47	56	
		V <sub>GS</sub> =-1.8V, I <sub>D</sub> =-1.5A	-	68	88	
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =-16V, V <sub>GS</sub> =0V	-	-	-1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = <u>+</u> 12V, V <sub>DS</sub> =0V	-	-	<u>+</u> 100	nA
Dynamic						
Total Gate Charge	Qg		-	10	-	nC
Gate-Source Charge	$Q_{gs}$	$\label{eq:VDS} \begin{split} V_{\text{DS}} = -10V, \ I_{\text{D}} = -5.2A, \\ V_{\text{GS}} = -4.5 V^{(\text{Note } 1,2)} \end{split}$	-	1.7	-	
Gate-Drain Charge	$Q_{gd}$		-	2.4	-	
Input Capacitance	Ciss	V <sub>DS</sub> =-10V, V <sub>GS</sub> =0V, f=1.0MHZ	-	980	-	pF
Output Capacitance	Coss		-	100	-	
Reverse Transfer Capacitance	Crss		-	81	-	
Switching						
Turn-On Delay Time	td <sub>(on)</sub>		-	9.8	-	
Turn-On Rise Time	tr	$V_{DD}=-10V, I_{D}=-5.2A,$	-	54	-	
Turn-Off Delay Time	td <sub>(off)</sub>	V <sub>GS</sub> =-4.5V, R <sub>G</sub> =6Ω <sup>(Note 1,2)</sup>	-	44	-	ns
Turn-Off Fall Time	tf	HG=012(1010 1,2)	-	31	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	ls		-	-	-2.0	A
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-1.0A, V <sub>GS</sub> =0V	_	-0.78	-1.2	v

NOTES :

- 1. Pulse width</br>
- 2. Essentially independent of operating temperature typical characteristics.
- 3. Reja is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited







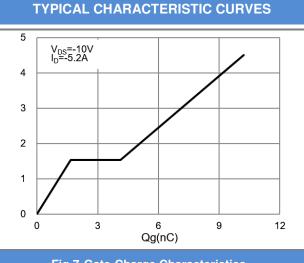


Fig.7 Gate-Charge Characteristics

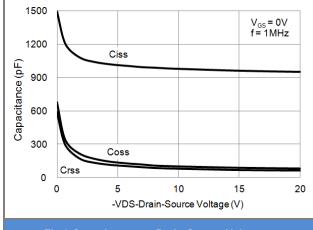
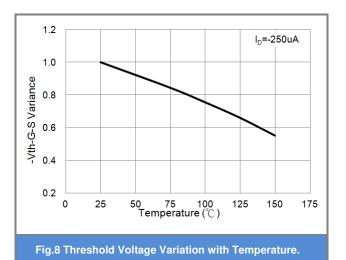


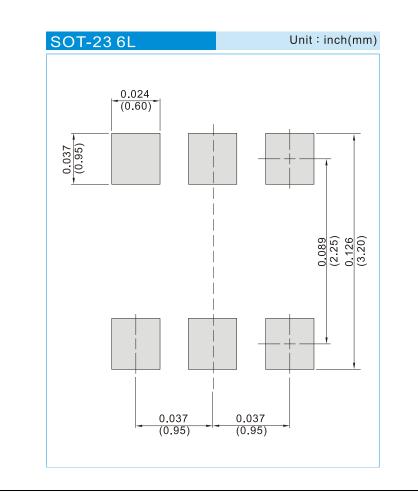
Fig.9 Capacitance vs. Drain-Source Voltage.





Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJS6415A_S1_00001	SOT-23 6L	3K pcs / 7" reel	S5A	Halogen free RoHS compliant
PJS6415A_S2_00001	SOT-23 6L	10K pcs / 13" reel	S5A	Halogen free RoHS compliant

### MOUNTING PAD LAYOUT





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