

# Description

The FMN-4306S is a fast recovery diode of 600 V / 30 A. The maximum  $t_{rr}$  of 100 ns is realized by optimizing a life-time control.

### **Features**

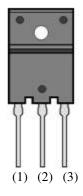
- Bare Leads: Pb-free (RoHS Compliant)
- Flammability: Equivalent to UL94V-0

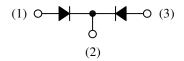
# Applications

- PFC Circuit
- Inverter Circuit

### Package

TO3PF-3L





- (1) Anode
- (2) Cathode
- (3) Anode

Not to scale

## **Absolute Maximum Ratings**

Parameter	Symbol	Conditions	Rating	Unit
Nonrepetitive Peak Reverse Voltage <sup>(1)</sup>	V <sub>RSM</sub>		600	V
Repetitive Peak Reverse Voltage <sup>(1)</sup>	$V_{RM}$		600	V
Average Forward Current	$I_{F\left(AV\right)}$	See Figure 1 and Figure 2	30	А
Surge Forward Current <sup>(1)</sup>	I <sub>FSM</sub>	Half cycle sine wave, positive side, 10 ms, 1 shot	150	А
I <sup>2</sup> t Limiting Value <sup>(1)</sup>	I <sup>2</sup> t	$1 \text{ ms} \le t \le 10 \text{ ms}$	112.5	A <sup>2</sup> s
Junction Temperature	$T_J$		-40 to 150	°C
Storage Temperature	T <sub>STG</sub>		-40 to 150	°C

Unless otherwise specified,  $T_A = 25$  °C.

## **Electrical Characteristics**

Unless otherwise specified,  $T_A = 25$  °C.

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage Drop <sup>(1)</sup> V <sub>F</sub>	X7	$T_J = 25 \ ^{\circ}C, I_F = 15 \ A$	_		1.3	V
	VF	$T_J = 100 \ ^{\circ}C, I_F = 15 \ A$	_	1.07	_	V
Reverse Leakage Current <sup>(1)</sup>	I <sub>R</sub>	$V_R = V_{RM}$			100	μA
Reverse Leakage Current under High Temperature <sup>(1)</sup>	$H \cdot I_R$	$V_{R} = V_{RM}, T_{J} = 150 \ ^{\circ}C$		_	10	mA
Reverse Recovery Time <sup>(1)</sup>	t <sub>rr1</sub>	$I_F = I_{RP} = 100 \text{ mA},$ 90% recovery point, $T_J = 25 \text{ °C}$	_	_	100	ns
	$I_{F} = 100 \text{ mA},$ $I_{RP} = 200 \text{ mA},$ 75%  recovery point, $T_{J} = 25 ^{\circ}\text{C}$			50	ns	
Thermal Resistance <sup>(2)</sup>	$R_{th(J-C)}$				2.0	°C/W

# **Mechanical Characteristics**

Parameter	Conditions	Min.	Тур.	Max.	Unit
Heatsink Mounting Screw Torque		0.686	_	0.882	N∙m
Package Weight			6.5		g

<sup>&</sup>lt;sup>(1)</sup> Specifies a value per chip; the FMN-4306S consists of two chips.

<sup>&</sup>lt;sup>(2)</sup> Refers to thermal resistance between junction and the case. The case temperature is measured at the backside near the screw hole.

### **Rating and Characteristic Curves**

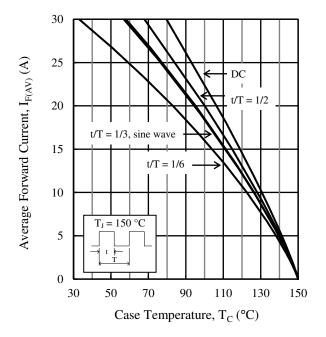
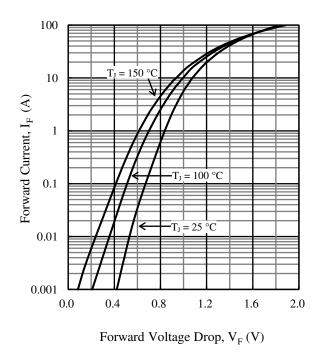


Figure 1. Typical Characteristics:  $I_{F(AV)} \mbox{ vs. } T_C$   $(V_R = 0 \ V)$ 





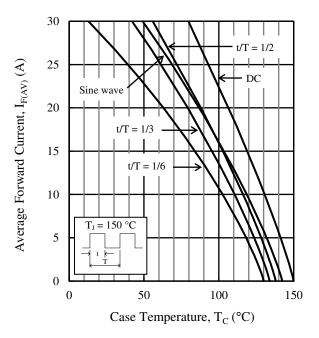
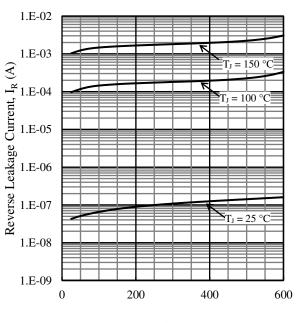


Figure 2. Typical Characteristics:  $I_{F(AV)}$  vs.  $T_C$ ( $V_R = 600$  V)

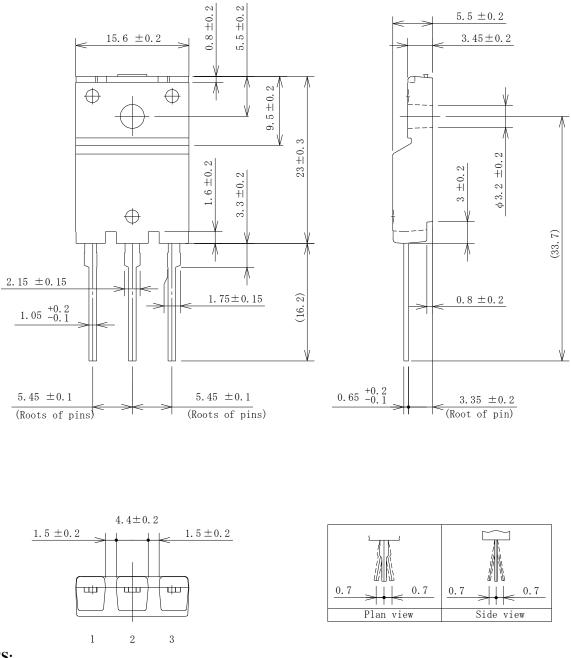


Reverse Voltage,  $V_{R}(V)$ 

Figure 4. Typical Characteristics: I<sub>R</sub> vs. V<sub>R</sub>

### **Physical Dimensions**

#### • TO3PF-3L



#### **NOTES:**

- Dimensions in millimeters
- Maximum gate burr height is 0.3 mm.
- Bare lead frame: Pb-free (RoHS compliant)
- When soldering the products, it is required to minimize the working time within the following limits: Flow: 260  $^{\circ}C$  / 10 s, 1 time
  - Soldering Iron: 350 °C / 3.5 s, 1 time

Soldering should be at a distance of at least 1.5 mm from the body of the product.

# **Marking Diagram**

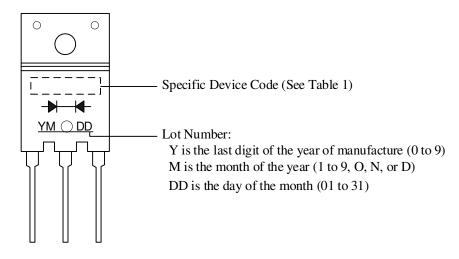


Table 1. Specific Device Code

Specific Device Code	Part Number
N4306S	FMN-4306S

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