MAXIM

Single +5V, Fully Integrated, 1.25Gbps Laser Diode Driver

MAX3261

General Description

The MAX3261 is a complete, easy-to-program, single +5V-powered, 1.25Gbps laser diode driver with complementary enable inputs and automatic power control (APC). The MAX3261 accepts differential PECL inputs and provides complementary output currents. A temperature-stabilized reference voltage is provided to simplify laser current programming. This allows modulation current to be programmed up to 30mA and bias current to be programmed up to 60mA with two external resistors.

Complementary enable inputs allow the MAX3261 to interface with open-fiber-control architecture—a feature not found in other 1.25Gbps laser diode drivers.

An APC circuit is provided to maintain constant laser power in transmitters that use a monitor photodiode. Only two external components are required to implement the APC function.

The MAX3261's fully integrated feature set includes a TTL-compatible laser failure indicator and a programmable slow-start circuit to prevent laser damage. The slow-start is preset to 50ns and can be extended by adding an external capacitor.

Applications

Laser Diode Transmitters

531Mbps and 1062Mbps Fibre Channel

622Mbps SDH/SONET

Gigabit Ethernet
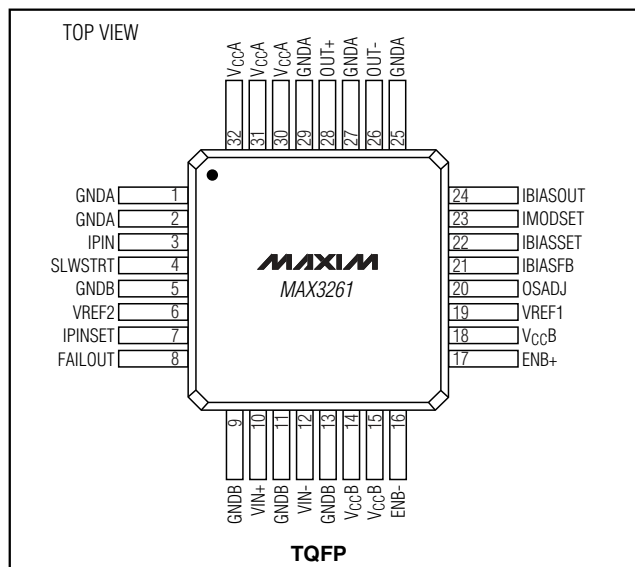
Features

♦ **Rise Times Less than 250ps**

♦ **Differential PECL Inputs**

♦ **Single +5V Supply**

♦ **Automatic Power Control**

♦ **Temperature-Compensated Reference Voltage**

♦ **Complementary Enable Inputs**

Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
MAX3261CCJ	0°C to +70°C	32 TQFP
MAX3261ECJ	-40°C to +85°C	32 TQFP
MAX3261E/D	-40°C to +85°C	Dice*

*Dice are designed to operate over a -40°C to +140°C junction temperature (Tj) range. Tested and guaranteed at Tj = +25°C.

Typical Operating Circuit

Pin Configuration
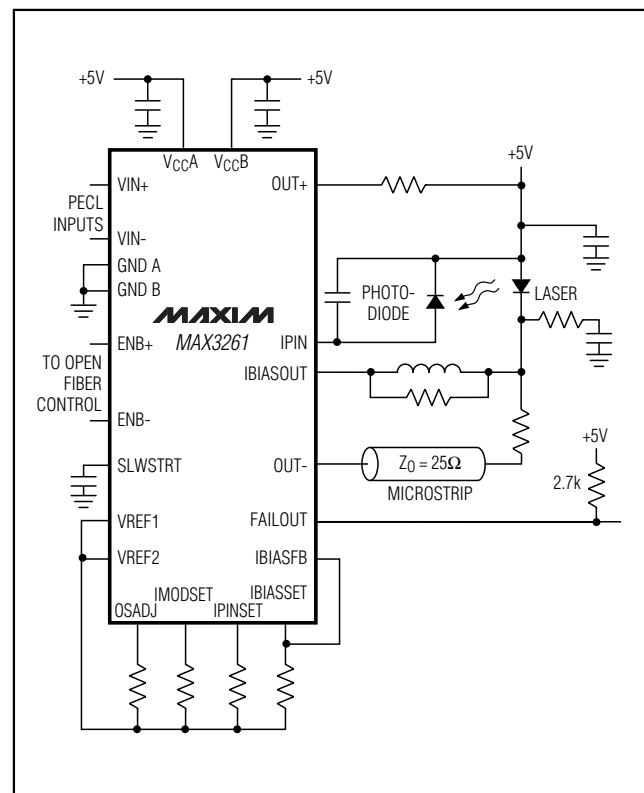
MAXIM _____ **Maxim Integrated Products** 1

For pricing, delivery, and ordering information, please contact Maxim/Dallas Direct! at
1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

Single +5V, Fully Integrated, 1.25Gbps Laser Diode Driver

ABSOLUTE MAXIMUM RATINGS

Terminal Voltage (with respect to GND)
Supply Voltages ($V_{CC}A$, $V_{CC}B$)................................-0.3V to 6V
VIN+, VIN-, FAILOUT................................0V to $V_{CC\_}$
OUT+, OUT-, IBIASOUT................................1.5V to $V_{CC\_}$
ENB+, ENB-.......................$V_{CC\_}$ or 5.5V, whichever is smaller
Differential Input Voltage (|VIN+ - VIN-|)............................3.8V
Input Current
IBIASOUT................................0mA to 75mA
OUT+, OUT-................................0mA to 40mA
IBIASSET................................0mA to 1.875mA
IMODSET................................0mA to 2mA
IPIN, IPINSET, OSADJ................................0mA to 2mA
FAILOUT................................0mA to 10mA
IBIASFB................................-2mA to 2mA

Output Current
VREF1, VREF2................................0mA to 20mA
SLWSTRT................................0mA to 5mA
Continuous Power Dissipation (T_A = +70°C)
TQFP (derate 10.2mW/°C above +70°C)................................816mW
Operating Temperature Ranges
MAX3261CCJ................................0°C to +70°C
MAX3261ECJ................................-40°C to +85°C
Junction Temperature................................+150°C
Storage Temperature Range................................-55°C to +175°C
Processing Temperature (die)................................+400°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

DC ELECTRICAL CHARACTERISTICS

(V_{CC} = $V_{CC}A$ = $V_{CC}B$ = +4.75V to +5.25V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at V_{CC} = +5V and T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Range of Programmable Laser Bias Current	I_{BIAS}				60	mA
Reference Voltage	V_{REF}	T_A = +25°C	3.15	3.3	3.55	V
Available Reference Current	I_{REF}			12		mA
Supply Current	I_{VCC}	(Note 2)			50	mA
PECL Input High	V_{IH}		V_{CC} - 1.165			V
PECL Input Low	V_{IL}				V_{CC} - 1.475	V
TTL Input High	V_{IH}		2			V
TTL Input Low	V_{IL}				0.8	V
FAILOUT Output High	V_{OH}	Loaded with 2.7kΩ pull-up resistor to V_{CC}	4.5			V
FAILOUT Output Low	V_{OL}	Loaded with 2.7kΩ pull-up resistor to V_{CC}			0.5	V

Note 1: Dice are tested at T_A = +25°C.
Note 2: I_{VCC} = $I_{VCC}A$ + $I_{VCC}B$, I_{BIAS} = 60mA, I_{MOD} = 30mA, and I_{PIN} = 140µA.

AC ELECTRICAL CHARACTERISTICS

(V_{CC} = $V_{CC}A$ = $V_{CC}B$ = +4.75V to +5.25V, R_{LOAD} (at OUT+ and OUT-) = 25Ω connected to V_{CC}, T_A = -40°C to +85°C, unless otherwise noted. Typical values are at V_{CC} = +5V and T_A = +25°C.) (Note 3)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Range of Programmable Modulation Current	I_{MOD}	Minimum differential input swing is 1100mVp-p (Note 4)				30	mA
Modulation-Current Rise and Fall Time	t_R, t_F	I_{BIAS} = 25mA, I_{MOD} = 12mA, 4ns unit interval; measured from 10% to 90%.				250	ps
Aberrations, Rising and Falling Edge		I_{MOD} = 12mA, T_A = +25°C	MAX3261E/D		±10		%
			MAX3261ECJ		±15		
Modulation-Current Pulse-Width Distortion	PWD	I_{BIAS} = 25mA, I_{MOD} = 12mA, 4ns unit interval				80	ps

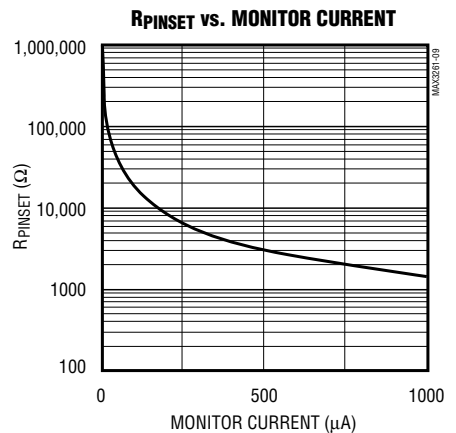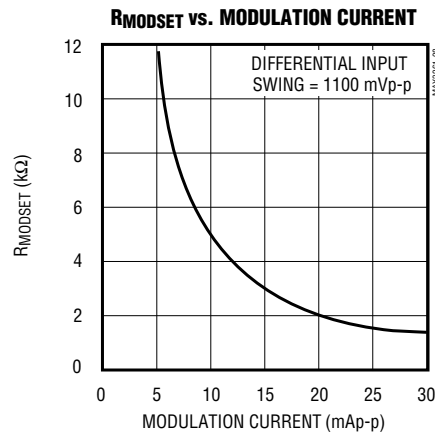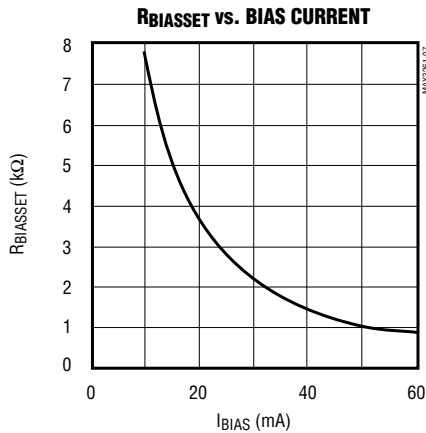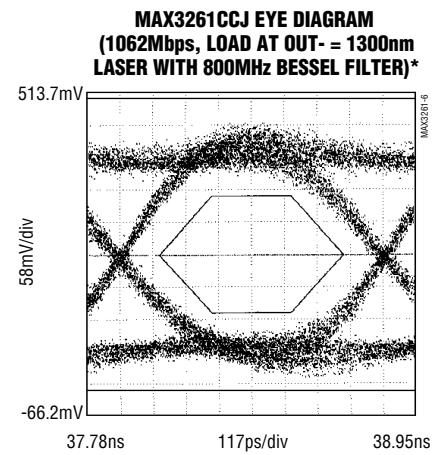**Note 3:** AC characteristics are guaranteed by design and characterization.
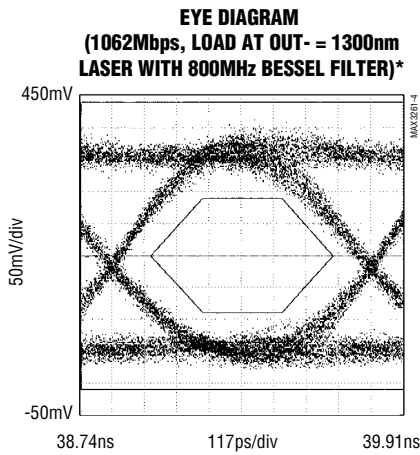**Note 4:** An 1100mVp-p differential is equivalent to complementary 550mVp-p signals on VIN+ and VIN-.

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Typical Operating Characteristics

(MAX3261E/D, load at OUT+ and OUT- = 25Ω, V_{CC} = V_{CCA} = V_{CCB} = +5V, T_A = +25°C, unless otherwise noted.)
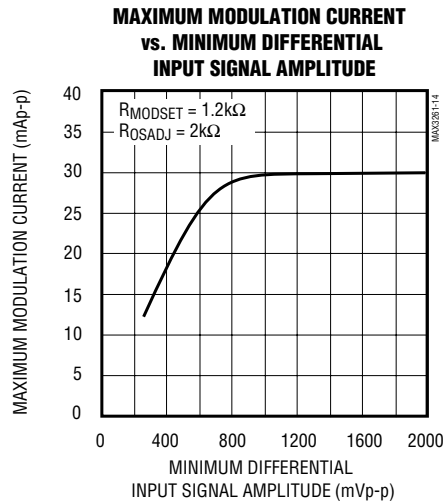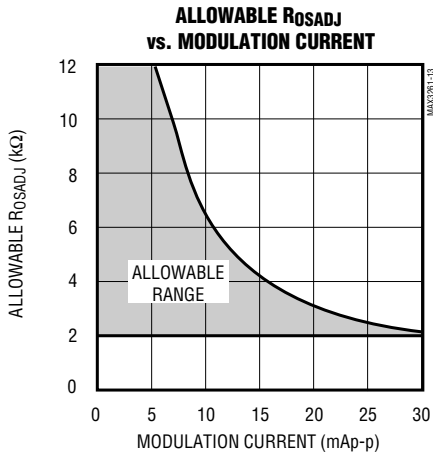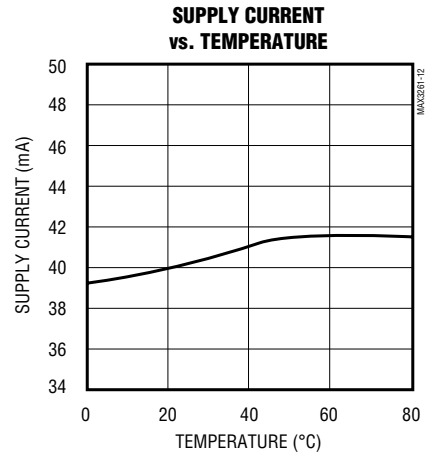

EYE DIAGRAM
(622Mbps, LOAD = 25Ω, NOT FILTERED)

EYE DIAGRAM
(622Mbps, LOAD AT OUT- = 1300nm LASER WITH 467MHz BESSEL FILTER)*

EYE DIAGRAM
(1062Mbps, LOAD = 25Ω, NOT FILTERED)

EYE DIAGRAM
(1062Mbps, LOAD AT OUT- = 1300nm LASER WITH 800MHz BESSEL FILTER)*

MAX3261CCJ EYE DIAGRAM
(622Mbps, LOAD AT OUT- = 1300nm LASER WITH 467MHz BESSEL FILTER)*

MAX3261CCJ EYE DIAGRAM
(1062Mbps, LOAD AT OUT- = 1300nm LASER WITH 800MHz BESSEL FILTER)*

$R_{BIASSET}$ vs. BIAS CURRENT

R_{MODSET} vs. MODULATION CURRENT

DIFFERENTIAL INPUT SWING = 1100 mVp-p

R_{PINSET} vs. MONITOR CURRENT

* LASER = EPITAXX EDL 1300RFC TO-STYLE HEADER

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_____Typical Operating Characteristics (continued)

(MAX3261E/D, LOAD at OUT+ = OUT- = 25Ω, V_{CC} = V_{CCA} = V_{CCB} = +5V, T_A = +25°C, unless otherwise noted.)

PERCENT CHANGE IN MODULATION CURRENT vs. TEMPERATURE

PERCENT CHANGE IN BIAS CURRENT vs. TEMPERATURE

APC DISABLED

SUPPLY CURRENT vs. TEMPERATURE

ALLOWABLE R_{OSADJ} vs. MODULATION CURRENT

ALLOWABLE RANGE

MAXIMUM MODULATION CURRENT vs. MINIMUM DIFFERENTIAL INPUT SIGNAL AMPLITUDE

R_{MODSET} = 1.2kΩ
R_{OSADJ} = 2kΩ

/VI/IXI/VI
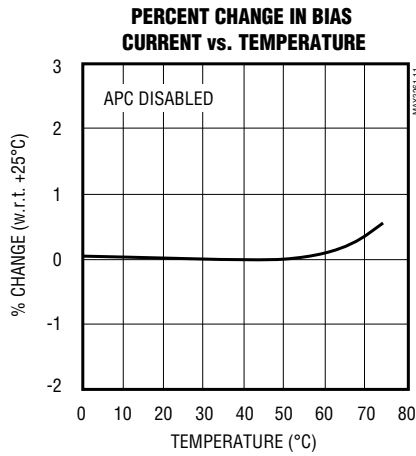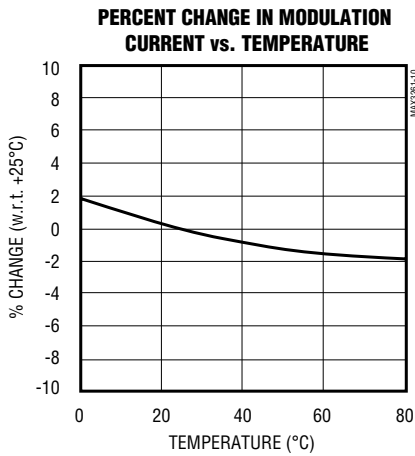
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PIN	NAME	FUNCTION
1, 2, 25, 27, 29	GNDA	Ground for Bias and Modulation Current Drivers
3	IPIN	Monitor Photodiode Current Input. Connect IPIN to photodiode's anode.
4	SLWSTRT	Slow-Start Capacitor Input. Connect capacitor to ground or leave unconnected to set start-up time, $t_{STARTUP}$ = 25.4kΩ ($C_{SLWSTRT}$ + 2pF).
5, 9, 11, 13	GNDB	Ground for Voltage Reference and Automatic Power-Control Circuitry
6	VREF2	Temperature-Compensated Reference Output. VREF2 is internally connected to VREF1.
7	IPINSET	Monitor Photodiode Programming Input. Connect IPINSET to VREF1 or VREF2 through a resistor to set the monitor current when using automatic power control (see *Typical Operating Characteristics*).
8	FAILOUT	Failout Output. Active-low, open-collector TTL output indicates if automatic power-control loop is out of regulation due to insufficient monitor-diode current (when V_{IPIN} is below the 2.6V threshold). Connect FAILOUT to $V_{CC\_}$ through a 2.7kΩ pull-up resistor.
10	VIN+	Noninverting PECL Data Input
12	VIN-	Inverting PECL Data Input
14, 15, 18	V_{CC}B	+5V Supply Voltage for Voltage Reference and Automatic Power-Control Circuitry. Connect V_{CC}B to the same potential as V_{CC}A, but provide separate bypassing for V_{CC}A and V_{CC}B.
16	ENB-	Inverting Enable TTL Input. Output currents are enabled only when ENB+ is high and ENB- is low.
17	ENB+	Noninverting Enable TTL Input. Output currents are enabled only when ENB+ is high and ENB- is low.
19	VREF1	Temperature-Compensated Reference Output. VREF1 is internally connected to VREF2.
20	OSADJ	Overshoot-Adjust Input. Connect to internal voltage reference through a resistor to adjust the over-shoot of the modulation output signal (see *Typical Operating Characteristics*).
21	IBIASFB	Bias-Feedback Current Output. Output from automatic power-control circuit. Connect to $I_{BIASSET}$ when using APC.
22	IBIASSET	Laser Bias Current-Programming Input. Connect to internal voltage reference through a resistor to set bias current (see *Typical Operating Characteristics*). $I_{BIASOUT}$ = 40 x ($I_{IBIASSET}$ + $I_{IBIASFB}$).
23	IMODSET	Laser Modulation Current-Programming Input. Connect to internal voltage reference through a resistor to set modulation current (see *Typical Operating Characteristics*). I_{MOD} = 20 x $I_{IMODSET}$.
24	IBIASOUT	Laser Bias Current Output. Connect to laser cathode through an R-L compensation network (see the *Bias Network Compensation* section).
26	OUT-	Modulation Output. When VIN+ is high and VIN- is low, OUT- sinks I_{MOD}.
28	OUT+	Modulation Output. When VIN+ is low and VIN- is high, OUT+ sinks I_{MOD}.
30, 31, 32	V_{CC}A	+5V Supply Voltage for Bias and Modulation Current Drivers. Connect V_{CC}A to the same potential as V_{CC}B, but provide separate bypassing for V_{CC}A and V_{CC}B.

Single +5V, Fully Integrated, 1.25Gbps Laser Diode Driver

Detailed Description

The MAX3261 laser driver has three main sections: a reference generator with temperature compensation, a laser bias block with automatic power control, and a high-speed modulation driver.

The reference generator provides temperature-compensated biasing and a voltage-reference output. The voltage reference is used to program the current levels of the high-speed modulation driver, laser diode, and PIN (p+, intrinsic, n-) monitor diode.

The laser bias block sets the bias current in the laser diode and maintains it above the threshold current. A current-controlled current source (current mirror) programs the bias, with IBIASSET as the input. The mirror's gain is approximately 40. Keep the output voltage of the bias stage above 2.2V to prevent saturation.

The modulation driver consists of a high-speed input buffer and a common-emitter differential output stage. The modulation current mirror sets the laser modulation current in the output stage. This current is switched between the OUT+ and OUT- ports of the laser driver. The modulation
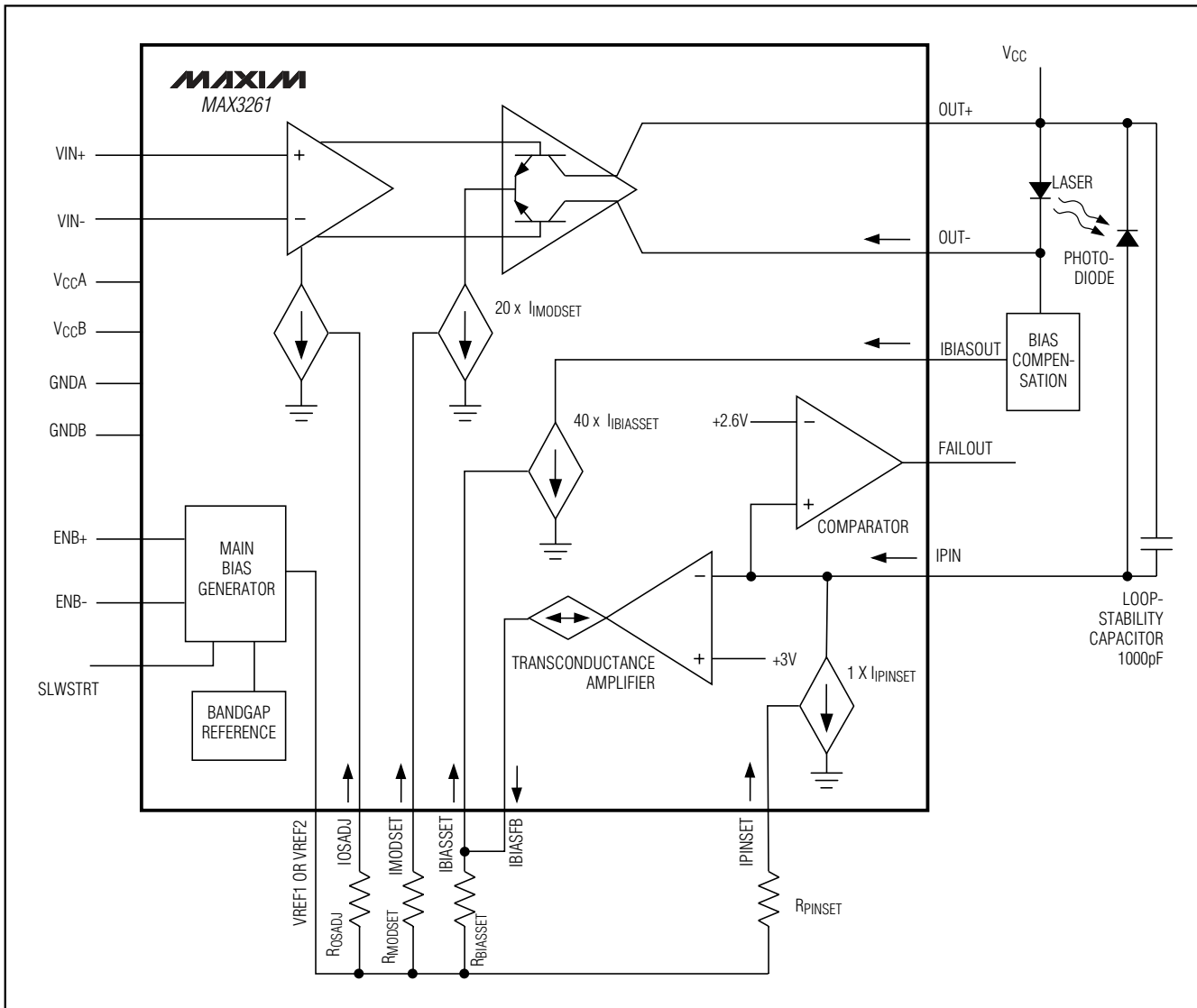

Figure 1. Functional Diagram
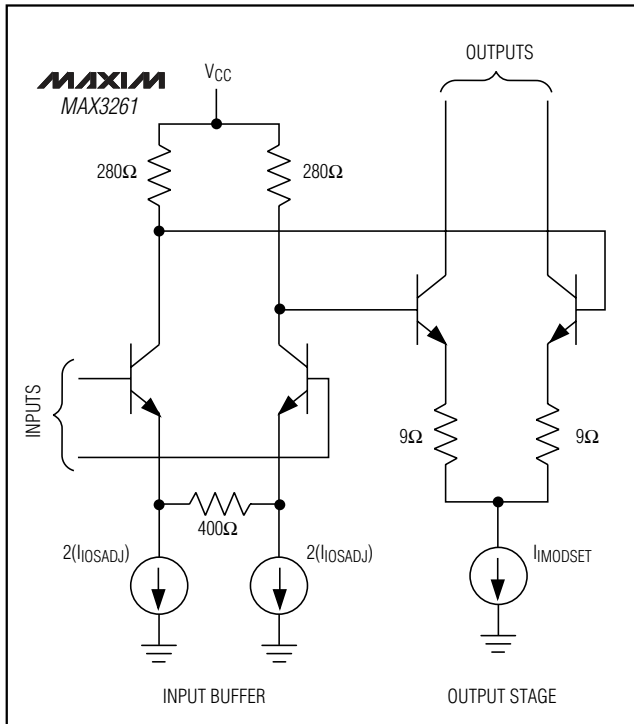
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Figure 2. MAX3261 Modulation Driver (Simplified)

Figure 3. Enable/Disable Operation

current mirror has a gain of approximately 20. Keep the voltages at OUT+ and OUT- above 2.2V to prevent saturation.

The overshoot mirror sets the bias in the input buffer stage (Figure 2). Reducing this current slows the input stage and reduces overshoot in the modulation signal. At the same time, the peak-to-peak output swing of the input buffer stage is reduced. Careful design must be used to ensure that the buffer stage can switch the output stage completely. The input swing required to completely switch the output stage depends on both ROSADJ and the modulation current. See *Allowable ROSADJ Range vs. Modulation Current* and *Modulation Current vs. Differential Input Signal* graphs in the *Typical Operating Characteristics*.

Failure to ensure that the output stage switches completely results in a loss of modulation current (and extinction ratio). In addition, if the modulation port does not switch completely off, the modulation current will contribute to the bias current, and may complicate module assembly.
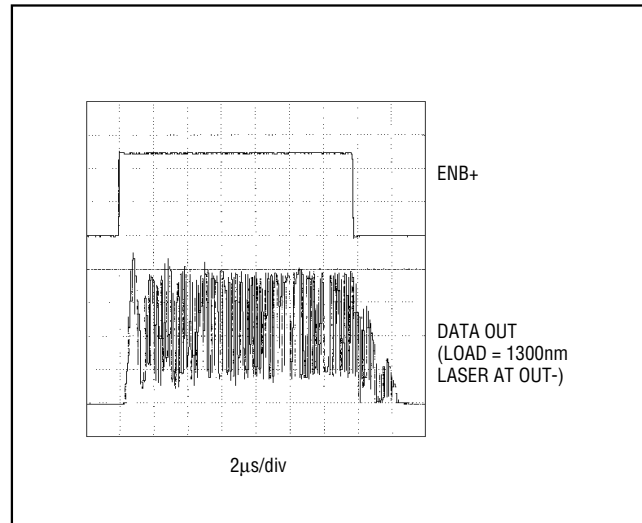
Automatic Power Control

The automatic power control (APC) feature allows an optical transmitter to maintain constant power, despite changes in laser efficiency with temperature or age. The APC feature requires the use of a monitor photodiode.

The APC circuit incorporates the laser diode, the monitor photodiode, the PIN set current mirror, a transconductance amplifier, the bias set current mirror, and the laser fail comparator (Figure 1). Light produced by the laser diode generates an average current in the monitor photodiode. This current flows into the MAX3261's IPIN input. The PIN set current mirror draws current away from the IPIN node. When the current into the IPIN node equals the current drawn away by IPINSET, the node voltage is set by the $^3/_5$ x V$_{CC}$ reference of the transconductance amplifier. When the monitor current exceeds IPINSET, the IPIN node voltage will be forced higher. If the monitor current decreases, the IPIN node voltage is decreased. In either case, the voltage change is amplified by the transconductance amplifier, and results in a feedback current at the IBIASFB node. Under normal APC operation, IBIASFB is summed with IBIASSET, and the laser bias level is adjusted to maintain constant output power. This feedback process continues until the monitor-diode current equals IPINSET.

If the monitor-diode current is sufficiently less than IPINSET (i.e., the laser stops functioning), the voltage on

the IPIN node will drop below 2.6V. This will trigger the failout comparator, which provides a TTL signal indicating laser failure. The FAILOUT output asserts only if the monitor-diode current is low, not in the reverse situation where the monitor current exceeds IPINSET. FAILOUT is an open-collector output that requires an external pull-up resistor of 2.7kΩ to V_{CC}.

The transconductance amplifier can source or sink currents up to approximately 1mA. Since the laser bias generator has a gain of approximately 40, the APC function has a limit of approximately 40mA (up or down) from the initial set point. To take full advantage of this adjustment range, it may be prudent to program the laser bias current slightly higher than required for normal operation. However, do not exceed the $I_{BIASOUT}$ absolute maximum rating of 75mA.

To maintain APC loop stability, a 1000pF bypass capacitor may be required across the photodiode. If the APC function is not used, leave IBIASFB unconnected.

Enable Inputs

The MAX3261 provides complementary enable inputs (ENB+, ENB-) for interfacing with open-fiber-control architecture. The laser is disabled by reducing the reference voltage outputs (VREF1, VREF2). Only one logic state will enable laser operation (Table 1).

With a 1000pF stability capacitor, the MAX3261 modulation and bias can be enabled and disabled within 5µs (Figure 3). This timing satisfies the requirements of the Open Fiber Control system used in Fibre Channel networks.

Temperature Considerations

The MAX3261 output currents are programmed by current mirrors. These mirrors each have a $2V_{BE}$ temperature coefficient. The reference voltage (VREF) is adjusted $2V_{BE}$ so these changes largely cancel, resulting in output currents that are very stable with respect to temperature (see *Typical Operating Characteristics*).

Wire Bonding Die

For reliable operation, the MAX3261 has gold metallization. Make connections to the die with gold wire only, using ball bonding techniques. Wedge bonding is not recommended. Pad size is 4mils.

Table 1. MAX3261 Truth Table

ENB-	ENB+	OUTPUT CURRENTS
0	0	DISABLED
0	1	ENABLED
1	0	DISABLED
1	1	DISABLED

Design Procedure

Interfacing Suggestions

Use high-frequency design techniques for the board layout of the MAX3261 laser driver. High-speed interfaces often require fixed-impedance transmission lines (Figure 5). Adding some damping resistance in series with the laser raises the load impedance, making the transmission line more realizable, and it also helps reduce power consumption (see the section *Reducing Power Consumption*). Minimize any series inductance to the laser, and place a bypass capacitor as close to the laser's anode as possible.

Power connections labeled $V_{CC}A$ are used to supply the laser modulation and laser bias circuits. $V_{CC}B$ connections supply the bias-generator and automatic-power control circuits. For optimum operation, isolate these supplies from each other by independent bypass filtering.

$V_{CC}A$, $V_{CC}B$, GNDA, and GNDB all have multiple pins. Connect all pins to optimize the MAX3261's high-frequency performance. Ground connections between signal lines (VIN+, VIN-, OUT+, OUT-) improve the quality of the signal path by reducing the impedance of the interconnect. Multiple connections, in general, reduce inductance in the signal path and improve the high-speed signal quality. GND pins should be tied to the ground plane with short runs and multiple vias. Avoid ground loops, since they are a source of high-frequency interference.

The MAX3261 data inputs accept PECL input signals, which require 50Ω termination to (V_{CC} - 2V). Figure 4 shows alternative termination techniques. When a termination voltage is not available, use the Thevenin-equivalent termination. When interfacing with a non-PECL signal source, use one of the other alternative termination methods shown in Figure 4.

Bias Network Compensation

When driving the laser diode with transmission lines, it is important to maintain a constant load impedance in order to minimize aberrations due to reflections. The inductive nature of laser packages will cause the laser impedance to increase with frequency, and the parasitic capacitance of the laser driver bias output (IBIASOUT) has some loading effects at high frequency. Of these two effects, the loading due to the laser lead inductance dominates. Impedance variation must be compensated for high-frequency operation. One possible approach is to use a shunt R-C network in parallel with the laser diode to compensate for the laser impedance (Figures 5 and 6). Add an R-L circuit in series with the bias output to compensate for the IBIASOUT capacitance (Figures 5 and 7).

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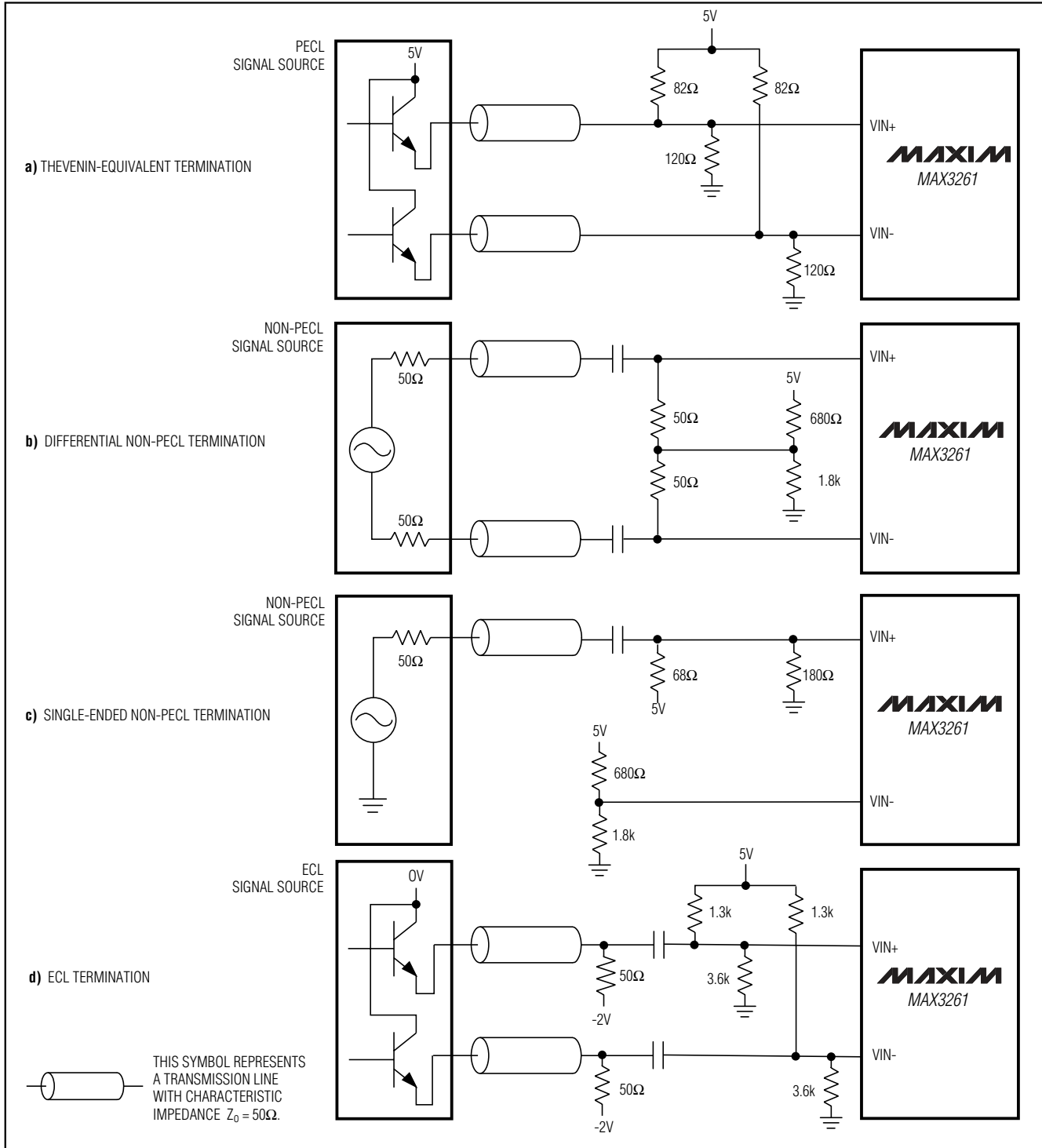
Figure 4. Alternative PECL Data-Input Terminations

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Reducing Power Consumption

The laser driver typically consumes 40mA of current for internal functions. Typical load currents, such as 12mA of modulation current and 20mA of bias current, bring the total current requirement to 72mA. If this were dissipated entirely in the laser driver, it would generate 360mW of heat. Fortunately, a substantial portion of this power is dissipated across the laser diode. A typical laser diode will drop approximately 1.6V when forward biased. This leaves 3.4V at the MAX3261's OUT- terminal. It is safe to reduce the output terminal voltage even further with a series damping resistor. Terminal voltage levels down to 2.2V can be used without degrading the laser driver's high-frequency performance. Power dissipation can be further reduced by adding a series resistor on the laser driver's OUT+ side. Select the series resistor so the OUT+ terminal voltage does not drop below 2.2V with the maximum modulation current.

Applications Information

Programming the MAX3261 Laser Driver

Programming the MAX3261 is best explained by an example. Assume the following laser diode characteristics:

Wavelength λ 780nm

Threshold Current I_{TH} 20mA at +25°C
(+0.35mA/°C temperature variation)

Monitor Responsivity ρ_{mon} 0.1A/W (monitor current / average optical power into the fiber)

Modulation Efficiency η 0.1mW/mA (worst case)

Now assume the communications system has the following requirements:

Average Power P_{AVE} 0dBm (1mW)

Extinction Ratio Er 6dB (Er = 4)

Temperature Range Tr 0°C to +70°C

1) Determine the value of IPINSET:

The desired monitor-diode current is $(P_{AVE})(\rho_{mon}) = (1mW)(0.1A/W) = 100\mu A$. The R_{PINSET} vs. Monitor Current graph in the *Typical Operating Characteristics* shows that R_{PINSET} should be 18kΩ.

2) Determine R_{MODSET}:

The average power is defined as (P1 + P0) / 2, where P1 is the average amplitude of a transmitted "one" and P0 is the average amplitude of a transmitted "zero." The extinction ratio is P1/P0. Combining these

equations results in P1 = (2 x P_{AVE} x Er) / (Er + 1) and P0 = (2 x P_{AVE}) / (Er + 1). In this example, P1 = 1.6mW and P0 = 0.4mW. The optical modulation is 1.2mW. The modulation current required to produce this output is 1.2mW / η = (1.2mW) / (0.1mA/mW) = 12mA. The *Typical Operating Characteristics* show that R_{MODSET} = 3.9kΩ yields the desired modulation current.

3) Determine the value of R_{OSADJ}:

Using the Allowable R_{OSADJ} vs. Modulation Current graph in the *Typical Operating Characteristics*, a 5.6kΩ resistor is chosen for 12mA of modulation current. The maximum R_{OSADJ} values given in the graph minimize aberrations in the waveform and ensure that the driver stage operates fully limited.

4) Determine the value of $R_{BIASSET}$:

The automatic power control circuit can adjust the bias current 40mA from the initial setpoint. This feature makes the laser driver circuit reasonably insensitive to variations of laser threshold from lot to lot. The bias setting can be determined using one of two methods:

a) Set the bias at the laser threshold.

b) Set the bias at the midpoint of the highest and lowest expected threshold values.

Method A is straightforward. In the second method, it is assumed that the laser threshold will increase with age. The lowest threshold current occurs at 0°C, when the laser is new. The highest threshold current occurs at +70°C, at the end of the product's life. Assume the laser is near the end of life when its threshold reaches two-times its original value.

Lowest Bias Current:

$I_{TH} + \Delta I_{TH}$ = 20mA + (0.35mA/°C)(-25°C) = 11.25mA

Highest Bias Current:

2 x $I_{TH} + \Delta I_{TH}$ = 40mA + (0.35mA/°C)(+45°C) = 55.8mA

In this case, set the initial bias value to 34mA (which is the midpoint of the two extremes). The adjustment range of the MAX3261 maintains the average laser power at either extreme.

The *Typical Operating Characteristics* show that $R_{BIASSET}$ = 1.8kΩ delivers the required bias current.
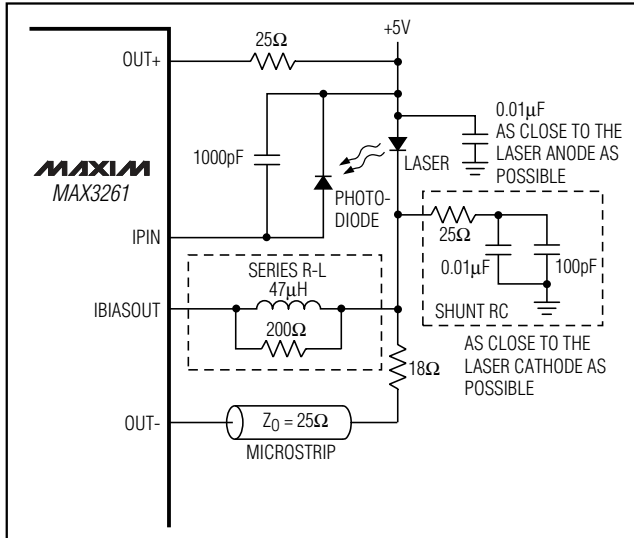
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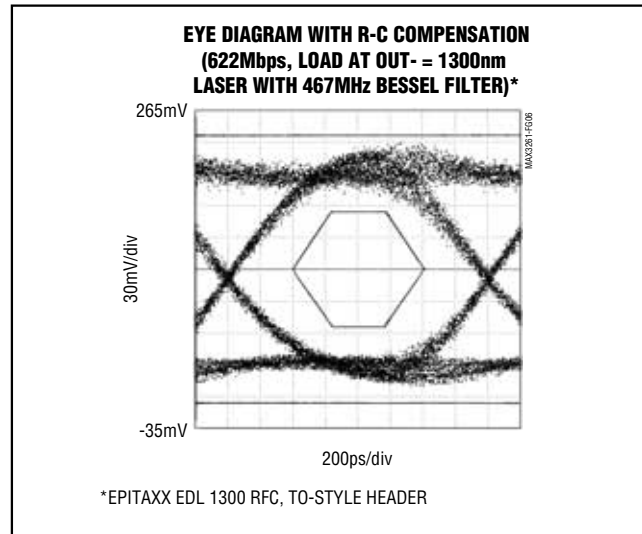Figure 5. Typical Laser Interface with Bias Compensation

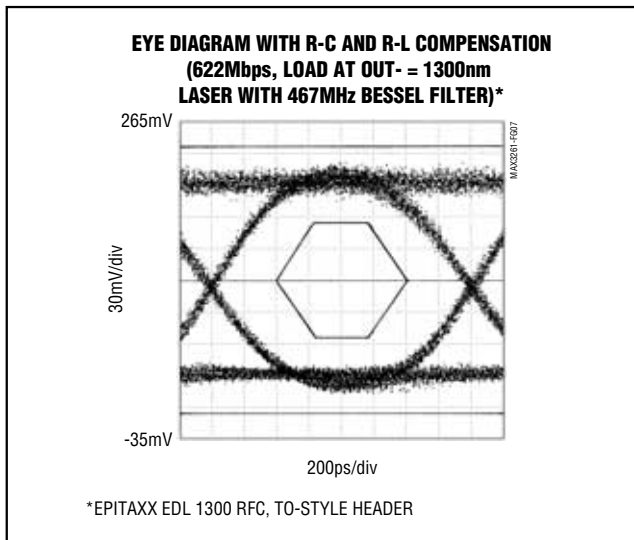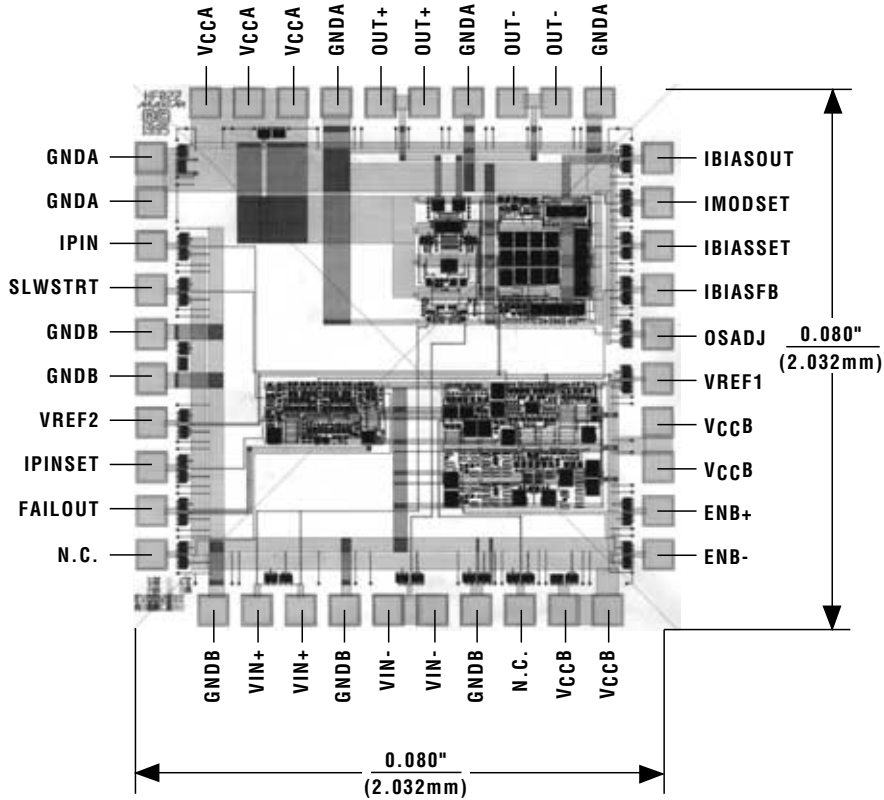Figure 6. Eye Diagram with R-C Compensation (LOAD at OUT- = 1300nm Laser)

Figure 7. Eye Diagram with R-C and R-L Compensation (LOAD at OUT- = 1300nm Laser)

Laser Safety and IEC 825

Using the MAX3261 laser driver alone does not ensure that a transmitter design is compliant with IEC 825. The entire transmitter circuit and component selections must be considered. Each customer must determine the level of fault tolerance required by their application, recognizing that Maxim products are not designed or authorized for use as components in systems intended for surgical implant into the body, for applications intended to support or sustain life, or for any other application where the failure of a Maxim product could create a situation where personal injury or death may occur.

Single +5V, Fully Integrated, 1.25Gbps Laser Diode Driver

MAX3261

_____*Chip Topography*

VCCA VCCA VCCA GNDA OUT+ OUT+ GNDA OUT- OUT- GNDA

GNDA
GNDA
IPIN
SLWSTRT
GNDB
GNDB
VREF2
IPINSET
FAILOUT
N.C.

IBIASOUT
IMODSET
IBIASSET
IBIASFB
OSADJ
VREF1
V$_{CC}$B
V$_{CC}$B
ENB+
ENB-

0.080"
(2.032mm)

GNDB VIN+ VIN+ GNDB VIN- VIN- GNDB N.C. VCCB VCCB

0.080"
(2.032mm)

TRANSISTOR COUNT: 197
SUBSTRATE CONNECTED TO GNDA AND GNDB