

Schottky Barrier Diode

DB2F43100L1

Panasonic DB2F43100L1

For rectification

Features

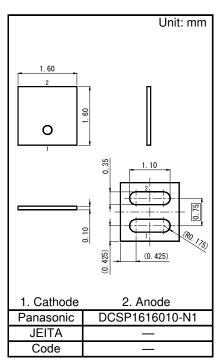
- Low forward voltage VF •
- Forward current (Average) IF(AV) ≤ 5.0 A rectification is possible
- **RoHS** compliant
- (EU RoHS / MSL:Level 1 compliant)
- Marking Symbol: E6

Packaging

Embossed type (Thermo-compression sealing) : 1 000 pcs / reel (standard)

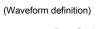
Absolute Maximum Ratings

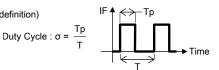
Parameter	Symbol	Min	Max	Unit
Reverse Voltage ^{*1}	VR	-	40	V
Maximum Peak Reverse Voltage ^{*1}	VRM	-	40	V
Average Forward Current *2,3	IF(AV)	-	5.0	Α
Non-repetitive Peak Surge Forward Current *1.4	IFSM	-	40	А
Operating Junction Temperature *5	Tj	-	150	С°
Ambient Temperature	Та	-40	+150	С°
Storage Temperature	Tstg	-55	+150	С°
				,



Note) *1: Ta = Tj = 25°C

- *2: Square wave : $\sigma = 0.5$
- *3: Solder Point Temperature : Tsp ≦ 122°C
- *4: Square wave : Tp = 5 ms
- *5: Power derating is necessary so that Tj < 150°C.





Electrical Characteristics Ta = 25 °C ± 3 °C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Forward Voltage	VF	IF = 5.0 A	-	0.51	0.6	V
Reverse Current	IR	VR = 40 V	-	15	100	μA
Terminal Capacitance	Ct	VR = 10 V, f = 1 MHz	-	140	-	pF
Reverse Recovery Time ^{*1}	trr	IF = IR = 100 mA, Irr = 10 mA	-	45	-	ns

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7031 measuring methods for diodes. 2. This product is sensitive to electric shock (static electricity, etc.).

Due attention must be paid on the charge of a human body and the leakage of current from the operating equipment. 3. *1: Measurement circuit, input pulse, output pulse for Reverse recovery time

(Input pulse)

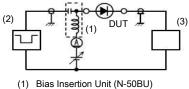
tp

10%

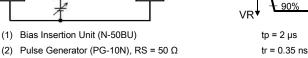
90%

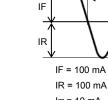
 $\sigma = 0.05$

(Measurement circuit)

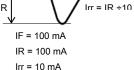


(3) Wave Form Analyzer (SAS-8130), Ri = 50 Ω





(Output pulse)



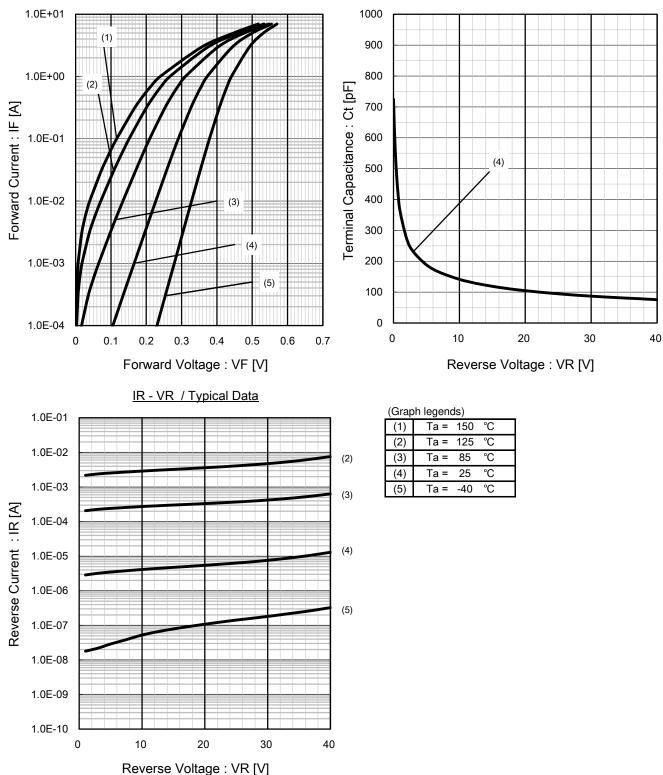
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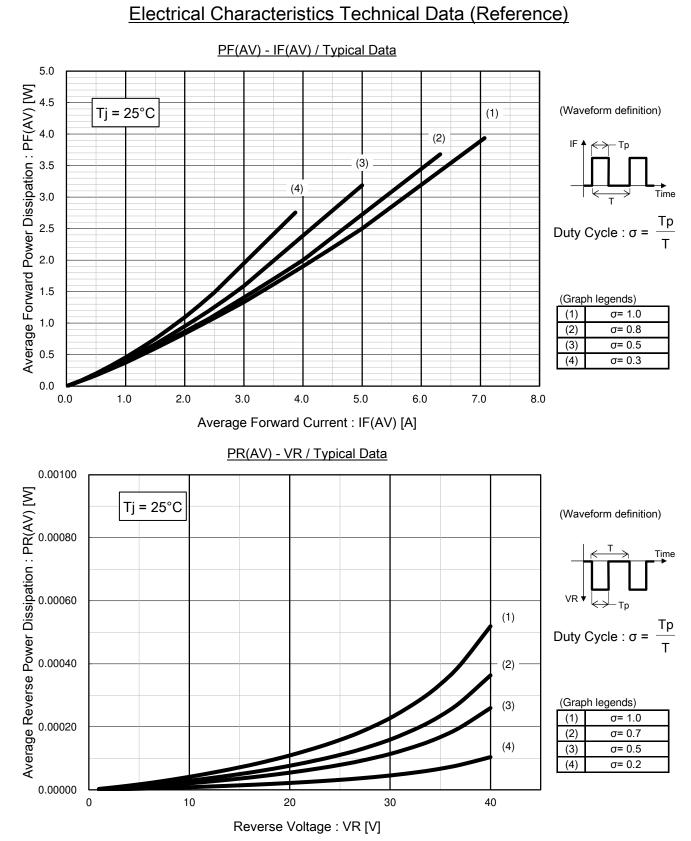


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Average Forward Power Dissipation : PF(AV) [W]

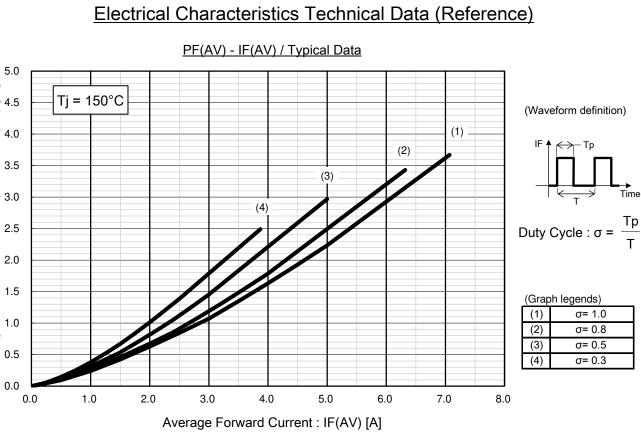
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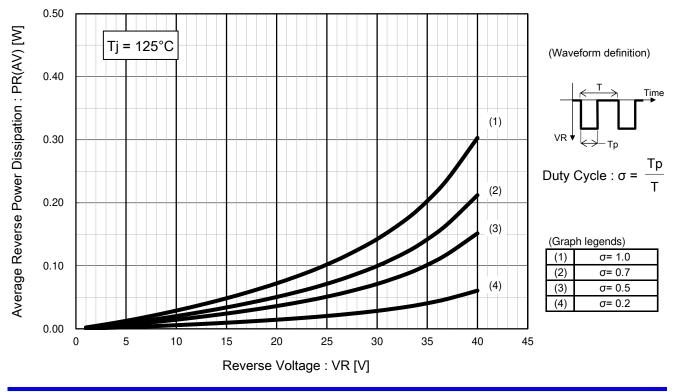
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Time





PR(AV) - VR / Typical Data



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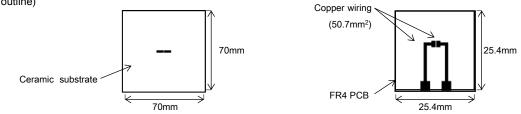
Thermal Characteristics

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Thermal Resistance, Junction to Solder Point	$R_{th(j-sp)}$	Ta = 25℃, in free air	-	8	-	°C/W
Thermal Resistance, Junction to Ambient ^{*1}	R _{th(j-a)}	Ta = 25°C, in free air	-	43	-	°C/W
Thermal Resistance, Junction to Ambient ^{*2}	R _{th(j-a)}	Ta = 25°C, in free air	-	250	-	°C/W

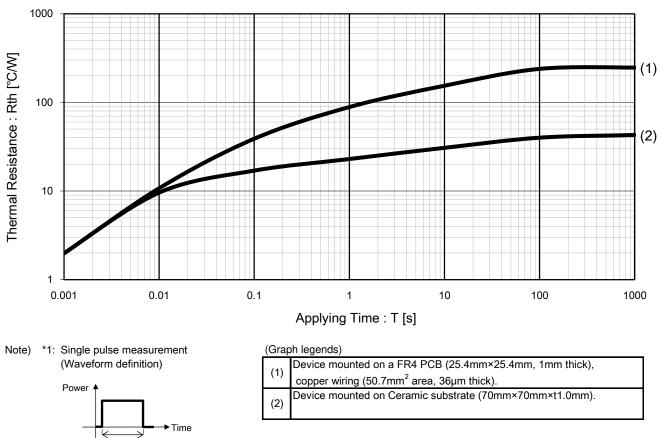
*1: Device mounted on Ceramic substrate (70mm×70mm×t1.0mm). Note)

*2: Device mounted on a FR4 PCB (25.4mm×25.4mm, 1mm thick), copper wiring (50.7mm² area, 36µm thick).

(Evaluation board outline)



Thermal Characteristics Technical Data (Reference)



Rth - T *1 / Typical Data

Applying Time : T

Doc No. TT4-ZZ-02049 Revision. 0

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(1)

(2)

(3)

(4)

(5)

(6)

0.001

1000

100

10

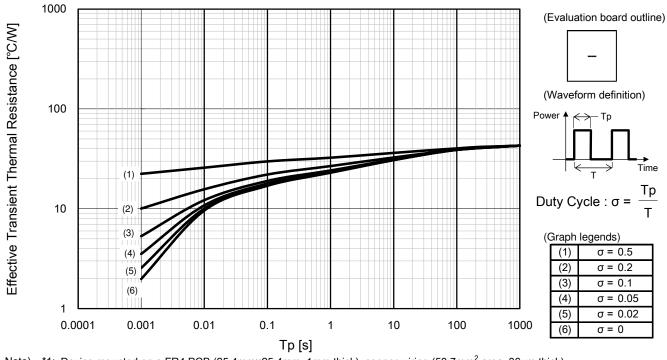
1

0.0001

Effective Transient Thermal Resistance [°C/W]

Thermal Characteristics Technical Data (Reference) Effective Transient Thermal Resistance - Tp *1 / Typical Data (Evaluation board outline) (Waveform definition) Power A Time <u>Тр</u> Т Duty Cycle : σ = (Graph legends) **σ** = 0.5 (1) σ = 0.2 (2)(3) $\sigma = 0.1$ (4) σ = 0.05 (5) $\sigma = 0.02$ 1 0.01 0.1 10 100 1000 (6) σ = 0 Tp [s]





Note) *1: Device mounted on a FR4 PCB (25.4mm×25.4mm, 1mm thick), copper wiring (50.7mm² area, 36µm thick). *2: Device mounted on Ceramic substrate (70mm×70mm×t1.0mm).

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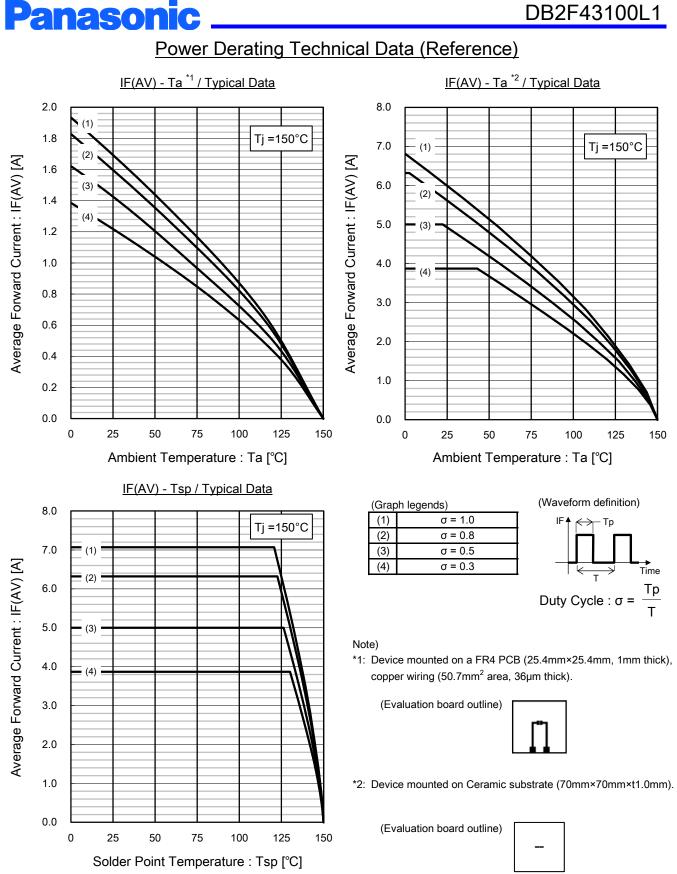
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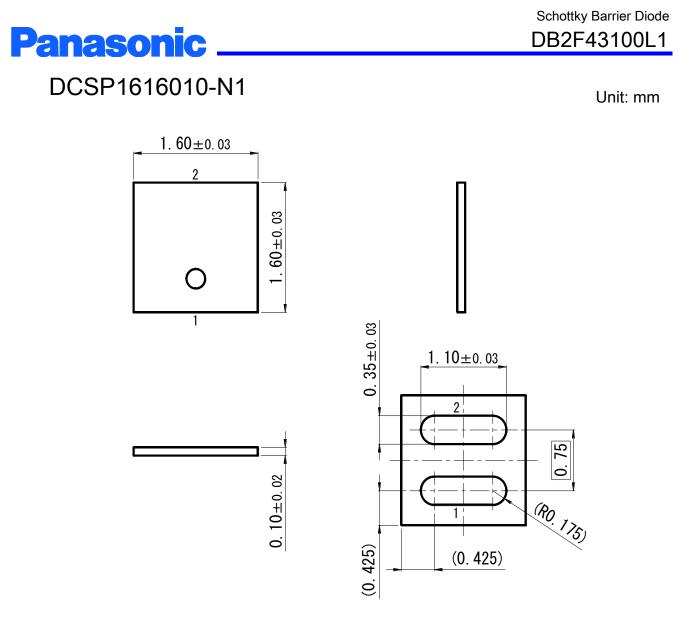
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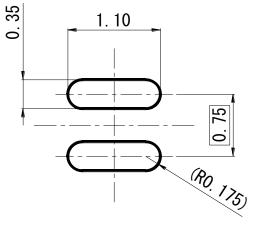




■ Land Pattern (Reference)

Unit: mm

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