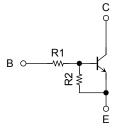
TOSHIBA Transistor Silicon NPN Epitaxial Type (PCT process) (Bias Resistor built-in Transistor)

RN1107CT, RN1108CT, RN1109CT

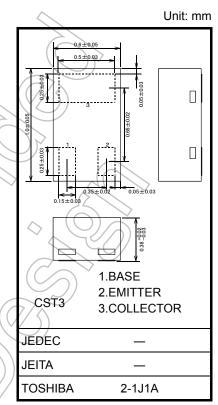
Switching Applications Inverter Circuit Applications Interface Circuit Applications Driver Circuit Applications

- Incorporating a bias resistor into a transistor reduces the number of parts, which enable the manufacture of ever more compact equipment and saves assembly cost.
- Complementary to RN2107CT to RN2109CT

Equivalent Circuit and Bias Resistor Values



Type No.	R1 (kΩ)	R2 (kQ)
RN1107CT	10 🗸	47
RN1108CT	22	47
RN1109CT	47	22



Weight: 0.75 mg (typ.)

A	Absolute Maximum Ratings (Ta = 25°C)									
	Characteristics		Symbol	Rating	Unit					
	Collector-base voltage	RN1107CT to RN1109CT	V _{CBO}	20	V					
	Collector-emitter voltage	RIVI 107 CT 10 RIVI 109CT	VCEO	20	V					
		RN1107CT		6						
	Emitter-base voltage	RN1108CT	V _{EBO}	7	V					
		RN1109CT		15						
	Collector current		Ι _C	50	mA					
	Collector power dissipation	RN1107CT to RN1109CT	P _C	50	mW					
	Junction temperature	KNUD/CI IO KIVI IO9CI	Tj	150	°C					
	Storage temperature range		T _{stg}	-55 to 150	°C					

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e.operatingtemperature/current/voltage, etc.) are within the absolute maximum ratings.

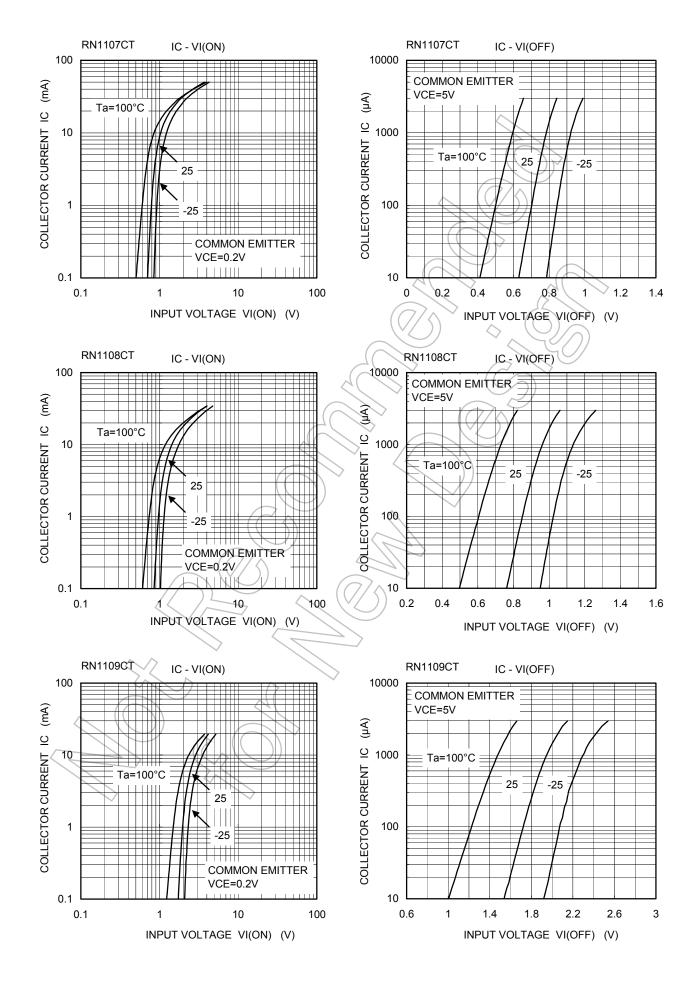
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production 2004-10

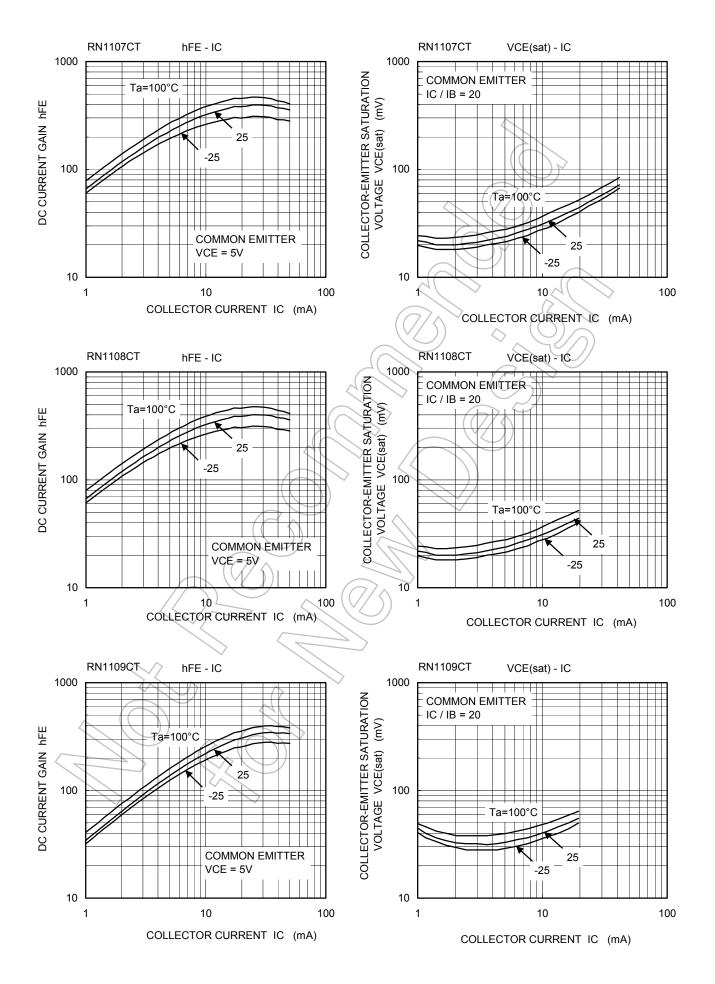
Electrical Characteristics (Ta = 25°C)

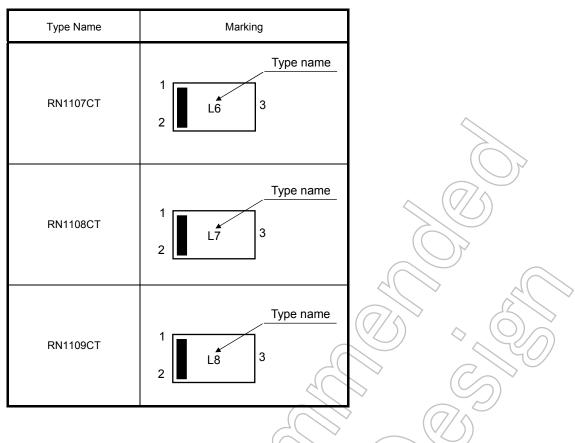
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector out off ourrent	RN1107CT to 1109CT	I _{CBO}	$V_{CB} = 20 \text{ V}, \text{ I}_{E} = 0$	_	—	100	nA
Collector cut-off current	RINTIO7CT to TTO9CT	ICEO	$V_{CE}=20~V,~I_B=0$		—	500	ПА
	RN1107CT		$V_{EB} = 6 V, I_{C} = 0$	0.088	—	0.131	mA
Emitter cut-off current	RN1108CT	I _{EBO}	$V_{EB} = 7 V, I_{C} = 0$	0.085		0.126	
	RN1109CT		$V_{EB} = 15 \text{ V}, \text{ I}_{C} = 0$	0.182)}	0.271	
	RN1107CT		$V_{CE} = 5 V, I_{C} = 10 mA$	120	_	_	
DC current gain	RN1108CT	h _{FE}		120			
	RN1109CT			100			
Collector-emitter saturation voltage	RN1107CT to 1109CT	V _{CE (sat)}	$I_{C} = 5 \text{ mA}, I_{B} = 0.25 \text{ mA}$			0.15	V
	RN1107CT		$\leq (>$	0.7	A	1.5	
Input voltage (ON)	RN1108CT	V _{I (ON)}	$V_{CE} = 0.2 V; I_{C} = 5 mA$	0.8	\sum	2.2	V
	RN1109CT			1.6	D + c	5.0	
	RN1107CT	G	V _{CE} = 5 V, I _C = 0.1 mA	0.5	Y)	1.0	v
Input voltage (OFF)	RN1108CT	VI (OFF)		0.6	\geq	1.1	
	RN1109CT		\diamond	1.3	_	2.6	
Collector output capacitance	RN1107CT to 1109CT	Cob	V _{CB} = 10 V, I _E = 0, f = 1 MHz		1.2	_	pF
	RN1107CT			8	10	12	
Input resistor	RN1108CT	R1	$\langle \langle - \rangle \rangle$	17.6	22	26.4	kΩ
	RN1109CT			37.6	47	56.4	
	RN1107CT	2		0.17	0.213	0.255	
Resistor ratio	RN1108CT	R1/R2	_ // _	0.374	0.468	0.562	
	RN1109CT			1.71	2.14	2.56	

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Handling Precaution

When handling individual devices (which are not yet mounted on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

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