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June 2014



FQA140N10

N-Channel QFET® MOSFET

100 V, 140 A, 10 m Ω

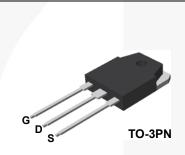
Description

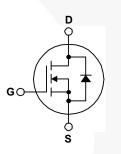
This N-Channel enhancement mode power MOSFET is • 140 A, 100 V, $R_{DS(on)}$ = 10 m Ω (Max.) @ V_{GS} = 10 V, produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state • Low Gate Charge (Typ. 220 nC) resistance, and to provide superior switching performance and • Low Crss (Typ. 470 pF) high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor • 100% Avalanche Tested control, and variable switching power applications.

Features

- I_D = 70 A

- 175°C Maximum Junction Temperature Rating





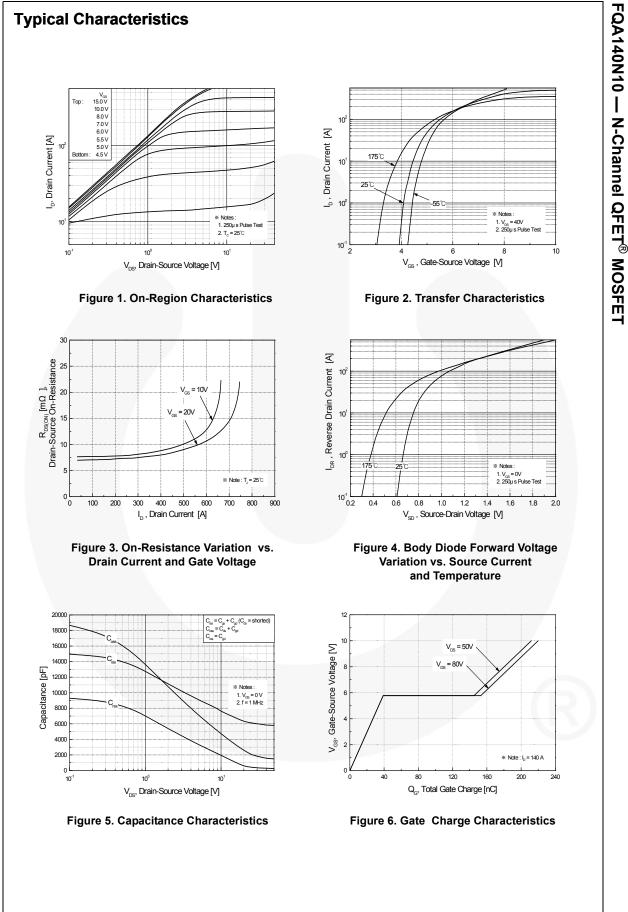
Absolute Maximum Ratings T_c = 25°C unless otherwise noted.

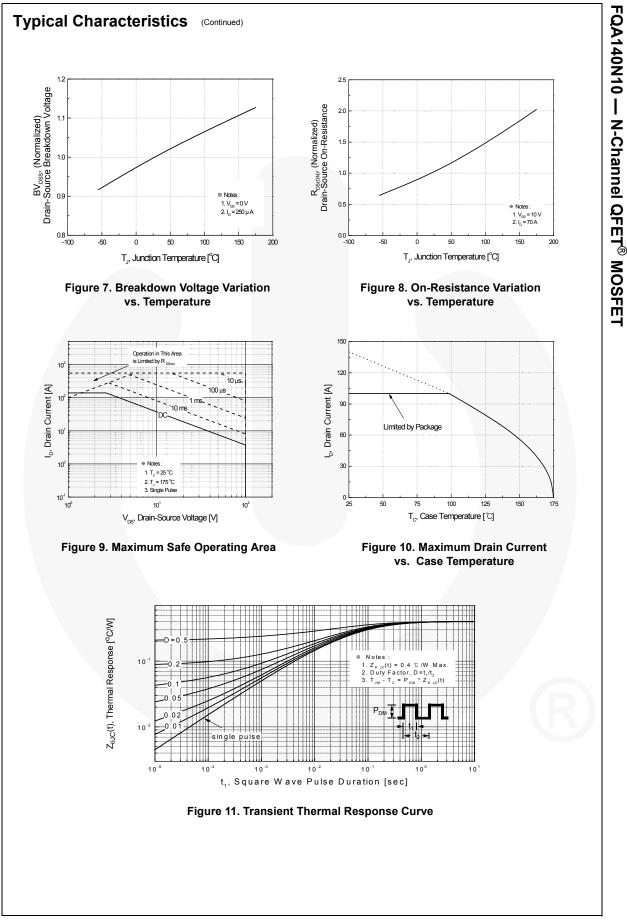
Symbol	Parameter	FQA140N10	Unit
V _{DSS}	Drain-Source Voltage	100	V
I _D	Drain Current - Continuous (T _C = 25°C)	140	A
	- Continuous (T _C = 100°C)	99	A
I _{DM}	Drain Current - Pulsed (Note 1)	560	A
V _{GSS}	Gate-Source Voltage	± 25	V
E _{AS}	Single Pulsed Avalanche Energy (Note 2)	1500	mJ
AR	Avalanche Current (Note 1)	140	А
E _{AR}	Repetitive Avalanche Energy (Note 1)	37.5	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	6.5	V/ns
PD	Power Dissipation ($T_C = 25^{\circ}C$)	375	W
	- Derate above 25°C	2.5	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +175	°C
TL	Maximum lead temperature for soldering purposes,1/8" from case for 5 seconds.	300	°C

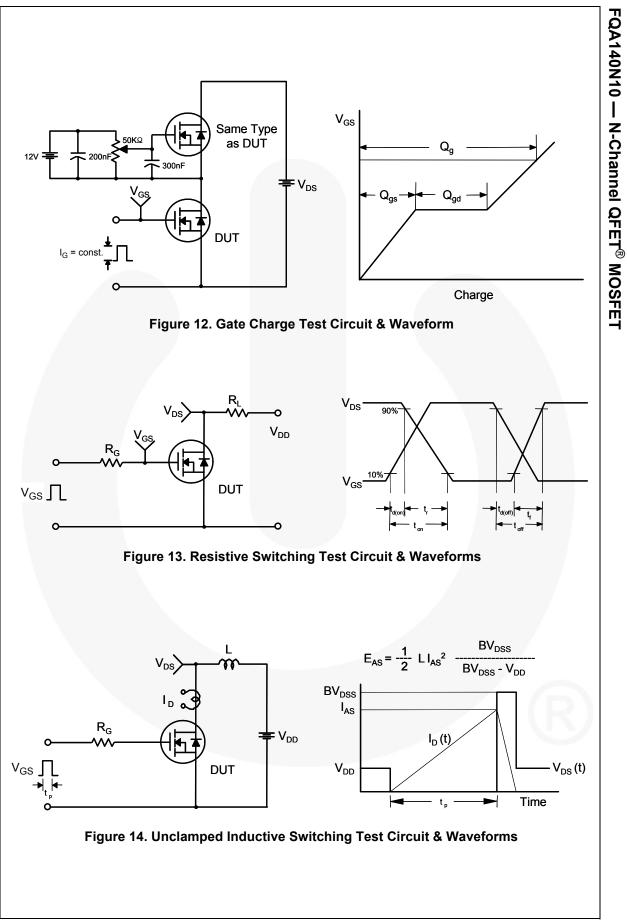
Thermal Characteristics

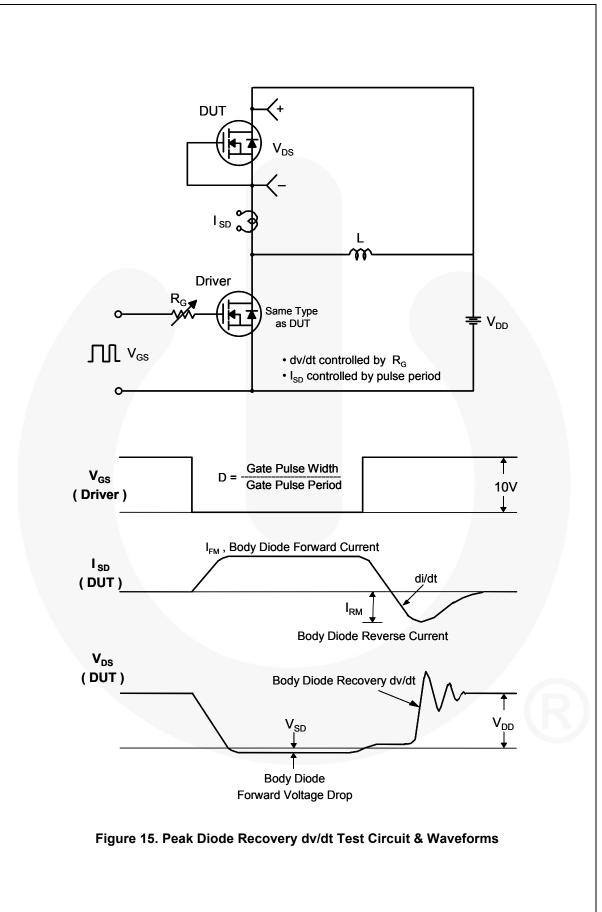
Symbol	Parameter	FQA140N10	Unit	
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction-to-Case, Max.	0.4	°C/W	
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction-to-Ambient, Max.	40	°C/W	

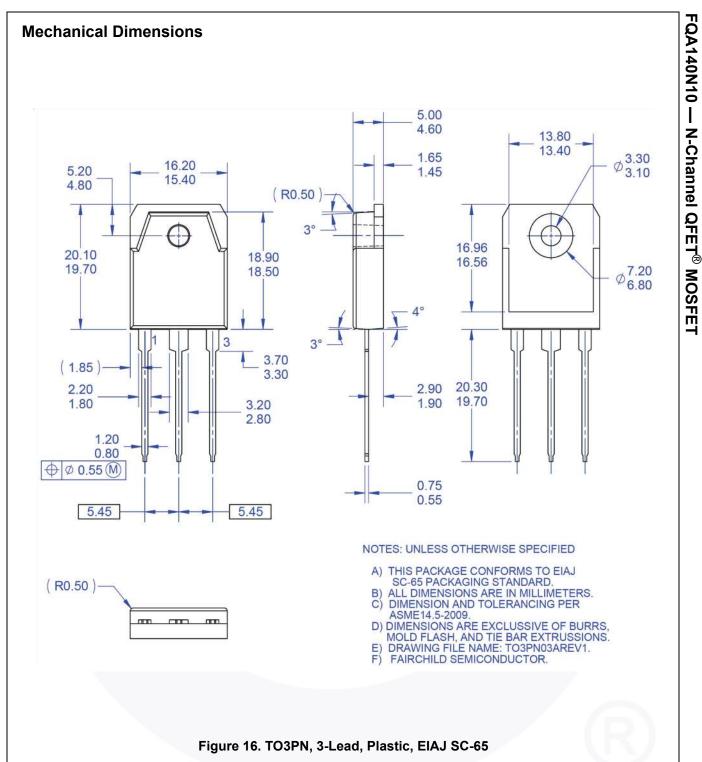
EOA?	Number	Top Mark	Pack	kage Packing Method		od Ree	l Size	Tape Wi	idth C	Quantity	
TQA	140N10	FQA140N10	TO-	3PN	Tube N		I/A	N/A	3	30 units	
lectri	cal Cha	racteristics	To = 25°C	Cunless off	erwise noted.						
Symbol		Parameter			Test Conditions	3	Min.	Тур.	Max.	Unit	
	aracterist	ico									
BV _{DSS}	Drain-Source Breakdown Voltage			Voc =	0 V, I _D = 250 μA		100			V	
ΔBV_{DSS}	Breakdown Voltage Temperature					100			v		
$/ \Delta T_J$	Coefficient		I_D = 250 µA, Referenced to 25°C				0.08		V/°C		
I _{DSS}	Zoro Coto	Zoro Cato Voltago Drain Current		V _{DS} = 80 V, V _{GS} = 0 V					1	μA	
	Zero Gate Voltage Drain Current		$V_{DS} = 64 \text{ V}, \text{ T}_{C} = 150^{\circ}\text{C}$					10	μA		
I _{GSSF}	Gate-Body	y Leakage Current, Fo	orward	V _{GS} = 2	25 V, V _{DS} = 0 V			-	100	nA	
I _{GSSR}	Gate-Bod	y Leakage Current, Re	everse	V _{GS} = ·	25 V, V _{DS} = 0 V				-100	nA	
On Cha	racterist	ics									
V _{GS(th)}	1	eshold Voltage	-	V _{DS} = V	/ _{GS} , I _D = 250 μA		2.0		4.0	V	
R _{DS(on)}	Static Dra On-Resist			V _{GS} =	10 V, I _D = 70 A			0.008	0.01	Ω	
9 _{FS}	Forward T	ransconductance	-	V _{DS} = 3	30 V, I _D = 70 A			80		S	
C _{iss} C _{oss}		apacitance		V _{DS} = 2 f = 1.0	25 V, V _{GS} = 0 V, MHz			6100 2000 420	7900 2600 550	pF pF	
C _{rss}	Reverse T	Transfer Capacitance								pi	
C _{rss}	1									p	
c _{rss} Switchi	ing Chara	acteristics					1	75			
C _{rss} Switchi t _{d(on)}	i ng Chara Turn-On D	acteristics Delay Time			40 V, I _D = 140 A,			75	160	ns	
C _{rss} Switchi t _{d(on)} t _r	i ng Chara Turn-On E Turn-On F	acteristics Delay Time Rise Time		V _{DD} = 4 R _G = 2	-			940	160 1890	ns	
C _{rss} Switchi t _{d(on)} t _r t _{d(off)}	i ng Chara Turn-On E Turn-On F Turn-Off E	acteristics Delay Time Rise Time Delay Time			-	(Note 4)		940 350	160 1890 710	ns ns ns	
C _{rss} Switchi t _{d(on)} t _r t _{d(off)} t _f	i ng Chara Turn-On E Turn-On F Turn-Off E Turn-Off F	acteristics Delay Time Rise Time Delay Time Fall Time		R _G = 2	5Ω	(Note 4)		940 350 360	160 1890 710 730	ns ns ns ns	
C _{rss} Switch i t _{d(on)} t _r t _{d(off)} t _f Q _g	ing Chara Turn-On E Turn-On F Turn-Off E Turn-Off F Total Gate	acteristics Delay Time Rise Time Delay Time Fall Time e Charge		R _G = 2	5 Ω 64 V, I _D = 140 A,	(Note 4)		940 350 360 220	160 1890 710	ns ns ns nC	
$\frac{C_{rss}}{Switch}$ $\frac{t_{d(on)}}{t_r}$ $\frac{t_{d(off)}}{t_f}$ $\frac{t_f}{Q_g}$ Q_{gs}	ing Chara Turn-On E Turn-On F Turn-Off E Turn-Off F Total Gate	acteristics Delay Time Rise Time Delay Time Fall Time to Charge rice Charge		R _G = 2	5 Ω 64 V, I _D = 140 A,	(Note 4) (Note 4)	 	940 350 360	160 1890 710 730	ns ns ns ns	
$\frac{C_{rss}}{Switch}$ $\frac{t_{d(on)}}{t_r}$ $\frac{t_{d(off)}}{t_f}$ Q_g	ing Chara Turn-On E Turn-On F Turn-Off E Turn-Off F Total Gate Gate-Sou	acteristics Delay Time Rise Time Delay Time Fall Time to Charge rice Charge		R _G = 2	5 Ω 64 V, I _D = 140 A,	. ,	 	940 350 360 220 39	160 1890 710 730 285 	ns ns ns nC nC	
$\frac{C_{rss}}{Switchi}$ $\frac{Switchi}{t_{d(on)}}$ $\frac{t_{d(off)}}{t_{f}}$ $\frac{d_{d(off)}}{Q_{g}}$ $\frac{Q_{gg}}{Q_{gg}}$ $Drain-S$	ing Chara Turn-On E Turn-On F Turn-Off E Turn-Off F Total Gate Gate-Sou Gate-Drai	acteristics Delay Time Rise Time Delay Time Fall Time to Charge rice Charge	tics ar	$R_{G} = 2$ $V_{DS} = 0$ $V_{GS} = 1$	5 Ω 64 V, I _D = 140 A, 10 V	(Note 4)	 	940 350 360 220 39	160 1890 710 730 285 	ns ns ns nC nC	
$\frac{C_{rss}}{Switchi}$ $\frac{Switchi}{t_{d(on)}}$ $\frac{t_r}{t_f}$ $\frac{Q_g}{Q_{gs}}$ $\frac{Q_{gg}}{Q_{gd}}$ $Drain-S$	ing Chara Turn-On E Turn-Off E Turn-Off F Total Gate Gate-Sou Gate-Drai	acteristics Delay Time Rise Time Delay Time all Time e Charge rce Charge n Charge n Charge	urce Dio	$R_{G} = 2$ $V_{DS} = 0$ $V_{GS} = 0$ $M_{GS} = 0$ $M_{GS} = 0$	5 Ω 64 V, I _D = 140 A, 10 V imum Rating ard Current	(Note 4)	 	940 350 360 220 39	160 1890 710 730 285 	ns ns ns nC nC	
$\frac{C_{rss}}{Switchi}$ $\frac{Switchi}{t_{d(on)}}$ $\frac{t_r}{t_f}$ Q_g Q_{gg} Q_{gg} $Drain-S$ I_{SM}	ing Chara Turn-On E Turn-On F Turn-Off E Turn-Off F Total Gate Gate-Sou Gate-Drai Source Di Maximum Maximum	acteristics Delay Time Rise Time Delay Time Fall Time e Charge rce Charge n Charge n Charge code Characteris Continuous Drain-Source	urce Dio Diode F	$R_G = 2$ $V_{DS} = 0$ $V_{GS} = 1$ de Forw forward (5 Ω A = 140 A, B = 140 A	(Note 4)		940 350 360 220 39 114	160 1890 710 730 285 140 560	ns ns ns nC nC nC A A	
C _{rss} Switch i t _{d(on)} t _r t _{d(off)} t _f Q _g Q _{gg} Q _{gd} Drain-S I _S I _{SM} V _{SD}	ing Chara Turn-On E Turn-On F Turn-Off E Turn-Off F Total Gate Gate-Sou Gate-Drai Source Di Maximum Maximum Drain-Sou	acteristics Delay Time Rise Time Delay Time Fall Time Charge rce Charge n Charge n Charge ode Characteris Continuous Drain-So Pulsed Drain-Source rrce Diode Forward Vo	urce Dio Diode F	$R_{G} = 2$ $V_{DS} = 0$ $V_{GS} = 0$ $M = 0$ $V_{GS} = 0$ $V_{GS} = 0$	5 Ω $A V, I_D = 140 A,$ 10 V imum Rating ard Current Current D V, I_S = 140 A	(Note 4)		940 350 360 220 39 114 	160 1890 710 730 285 140 560 1.5	ns ns ns nC nC nC A A A V	
$\frac{C_{rss}}{Switchi}$ $\frac{Switchi}{t_{d(on)}}$ $\frac{t_r}{t_f}$ $\frac{Q_g}{Q_{gs}}$ $\frac{Q_{gg}}{Q_{gd}}$ $Drain-S$	ing Chara Turn-On F Turn-Off E Turn-Off F Total Gate Gate-Sour Gate-Drai Source Di Maximum Maximum Drain-Sou Reverse F	acteristics Delay Time Rise Time Delay Time Fall Time e Charge rce Charge n Charge n Charge code Characteris Continuous Drain-Source	urce Dio Diode F	$R_{G} = 2$ $V_{DS} = 0$ $V_{GS} = 0$ $V_{GS} = 0$ $V_{GS} = 0$ $V_{GS} = 0$	5 Ω A = 140 A, B = 140 A	(Note 4)	 	940 350 360 220 39 114	160 1890 710 730 285 140 560	ns ns ns nC nC nC A A	











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