

September 2007

# **BC318C PNP Epitaxial Silicon Transistor**

- This device is designed for general purpose amplifier application at collector currents to 800m.
- Sourced from process 38.



## Absolute Maximum Ratings $T_C=25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Value	Units
V <sub>CBO</sub>	Collector-Base Voltage	30	V
V <sub>CEO</sub>	Collector-Emitter Voltage	20	V
V <sub>EBO</sub>	Emitter-Base Voltage	5	V
I <sub>C</sub>	Collector Current (DC)	100	mA
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	-55 ~ 150	°C

<sup>\*</sup> These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

### Thermal Characteristics T<sub>a</sub>=25°C unless otherwise noted

Symbol	Parameter	Max.	Units
$P_{D}$	Total Device Dissipation	625	mW
	Derate above 25°C	5.0	mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	83.3	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	200	°C/W

<sup>\*</sup>Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06".

## Electrical Characteristics $T_C=25$ °C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	$I_C = 10\mu A$	30			V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	$I_C = 1mA$	20			V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = 100μA	5			V
BV <sub>CES</sub>	Collector-Emitter Breakdown Voltage	$I_{C} = 100 \mu A$	30			V
I <sub>CBO</sub>	Collector Cut-off Current	$V_{CB} = 20V$ $T = 25 °C$ $T = 100 °C$			30 15	nA μA
h <sub>FE</sub>	DC Current Gain	$V_{CE} = 5V, I_{C} = 10 \mu A$ $V_{CE} = 5V, I_{C} = 2 mA$	100 420		800	
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	$I_C = 10 \text{ mA}, I_B = 0.5 \text{ mA}$ $I_C = 100 \text{ mA}, I_B = 5 \text{ mA}$			0.2 0.5	V
V <sub>BE</sub> (on)	Base-Emitter On Voltage	$V_{CE} = 5V, I_{C} = 2m$ $V_{CE} = 5V, I_{C} = 10mA$	0.57		0.72 0.77	V
C <sub>cb</sub>	Output Capacitance	VCB = 10V, IE = 0, f = 1MHz			4	pF

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- These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.
   These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.
   These ratings are based on a maximum junction temperature of 150degrees C.

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<sup>2)</sup> These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.





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