

# N0434N

N-channel MOSFET

40 V, 100 A, 3.7 mΩ

R07DS0556EJ0200

Rev.2.00

2020.6.10

## Features

- Low on-state resistance :  $R_{DS(on)} = 3.7 \text{ m}\Omega \text{ MAX.}$  ( $V_{GS} = 10 \text{ V}$ ,  $I_D = 50 \text{ A}$ )
- Low  $C_{iss}$  :  $C_{iss} = 5550 \text{ pF TYP.}$  ( $V_{DS} = 25 \text{ V}$ ,  $V_{GS} = 0 \text{ V}$ )
- High current :  $I_{D(DC)} = \pm 100 \text{ A}$
- RoHS Compliant
- Quality Grade : Standard
- Applications : For high current switching

## Ordering Information

Part No.	Package	Packing
N0434N-S23-AY	TO-262, Pb-free <sup>Note1</sup>	50 pcs / Magazine (Tube)

Note: 1. Pb-free means that this product does not contain lead in the external electrode.

## Absolute Maximum Ratings ( $T_A = 25^\circ\text{C}$ )

Item	Symbol	Ratings	Unit
Drain to Source Voltage ( $V_{GS} = 0 \text{ V}$ )	$V_{DSS}$	40	V
Gate to Source Voltage ( $V_{DS} = 0 \text{ V}$ )	$V_{GSS}$	$\pm 20$	V
Drain Current (DC) ( $T_C = 25^\circ\text{C}$ )	$I_{D(DC)}$	$\pm 100$	A
Drain Current (pulse) <sup>Note2</sup>	$I_{D(pulse)}$	$\pm 400$	A
Total Power Dissipation ( $T_C = 25^\circ\text{C}$ )	$P_{T1}$	119	W
Total Power Dissipation ( $T_A = 25^\circ\text{C}$ )	$P_{T2}$	1.5	W
Channel Temperature	$T_{ch}$	150	$^\circ\text{C}$
Storage Temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$
Single Avalanche Current <sup>Note3</sup>	$I_{AS}$	55	A
Single Avalanche Energy <sup>Note3</sup>	$E_{AS}$	300	mJ

Note: Continuous heavy condition (e.g. high temperature/voltage/current or high variation of temperature) may affect a reliability even if it is within the absolute maximum ratings. Please consider derating condition for appropriate reliability in reference Renesas Semiconductor Reliability Handbook (Recommendation for Handling and Usage of Semiconductor Devices) and individual reliability data.

Notes: 2.  $PW \leq 10 \mu\text{s}$ , Duty Cycle  $\leq 1\%$

3. Starting  $T_{ch} = 25^\circ\text{C}$ ,  $R_G = 25 \Omega$ ,  $V_{DD} = 25 \text{ V}$ ,  $V_{GS} = 20 \rightarrow 0 \text{ V}$ ,  $L = 100 \mu\text{H}$

## Thermal Resistance

Item	Symbol	Max. Value <sup>Note4</sup>	Unit
Channel to Case Thermal Resistance	$R_{th(ch-C)}$	1.05	$^\circ\text{C/W}$
Channel to Ambient Thermal Resistance	$R_{th(ch-A)}$	83.3	$^\circ\text{C/W}$

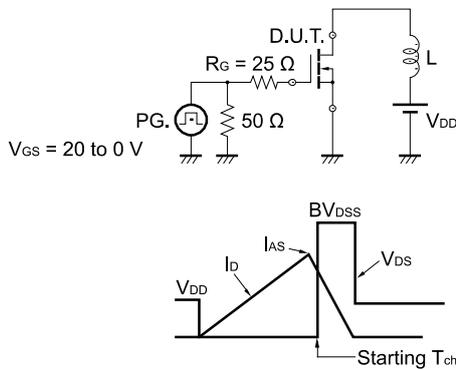
Notes: 4. This data is the designed target maximum value on Renesas's measurement condition. (Not tested)

Electrical Characteristics (T<sub>A</sub> = 25°C)

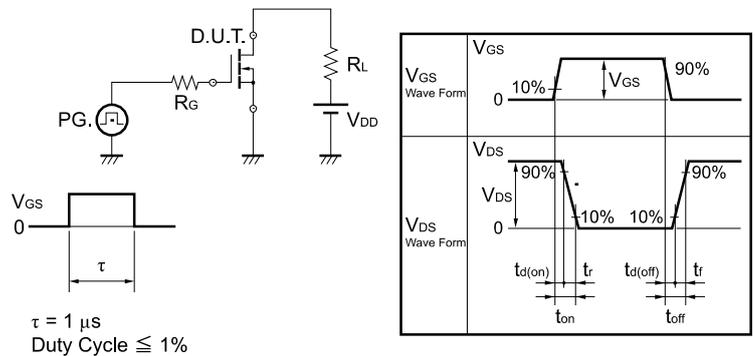
Item	Symbol	MIN.	TYP.	MAX.	Unit	Test Conditions
Zero Gate Voltage Drain Current	I <sub>DSS</sub>			1	μA	V <sub>DS</sub> = 40 V, V <sub>GS</sub> = 0 V
Gate Leakage Current	I <sub>GSS</sub>			±100	nA	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0 V
Gate to Source Cut-off Voltage	V <sub>GS(off)</sub>	2.0		4.0	V	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA
Forward Transfer Admittance <sup>Note5</sup>	y <sub>fs</sub>	26			S	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 50 A
Drain to Source On-state Resistance <sup>Note5</sup>	R <sub>DS(on)</sub>		2.7	3.7	mΩ	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 50 A
Input Capacitance	C <sub>iss</sub>		5550		pF	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz
Output Capacitance	C <sub>oss</sub>		580		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>		320		pF	
Turn-on Delay Time	t <sub>d(on)</sub>		29.0		ns	V <sub>DD</sub> = 20 V, I <sub>D</sub> = 50 A, V <sub>GS</sub> = 10 V, R <sub>G</sub> = 0 Ω
Rise Time	t <sub>r</sub>		15.0		ns	
Turn-off Delay Time	t <sub>d(off)</sub>		64.0		ns	
Fall Time	t <sub>f</sub>		13.0		ns	
Total Gate Charge	Q <sub>G</sub>		100		nC	V <sub>DD</sub> = 32 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 100 A
Gate to Source Charge	Q <sub>GS</sub>		26		nC	
Gate to Drain Charge	Q <sub>GD</sub>		32		nC	
Body Diode Forward Voltage <sup>Note5</sup>	V <sub>F(S-D)</sub>			1.5	V	I <sub>F</sub> = 100 A, V <sub>GS</sub> = 0 V
Reverse Recovery Time	t <sub>rr</sub>		40		ns	I <sub>F</sub> = 50 A, V <sub>GS</sub> = 0 V, di/dt = 100 A/μs
Reverse Recovery Charge	Q <sub>rr</sub>		44		nC	

Notes: 5. Pulsed test

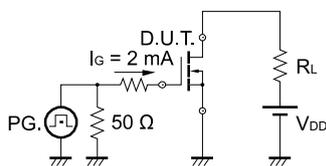
TEST CIRCUIT 1 AVALANCHE CAPABILITY



TEST CIRCUIT 2 SWITCHING TIME

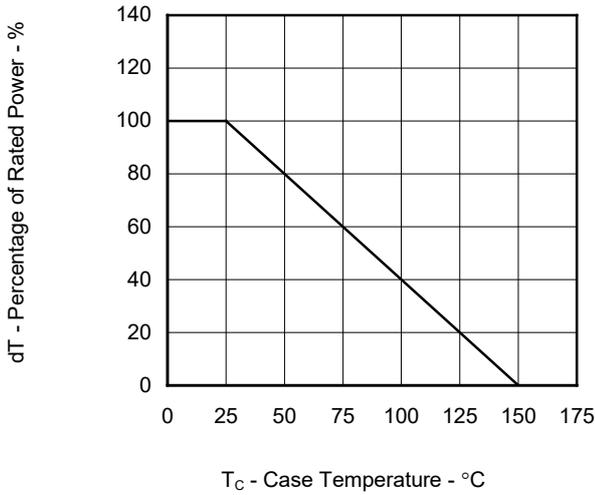


TEST CIRCUIT 3 GATE CHARGE

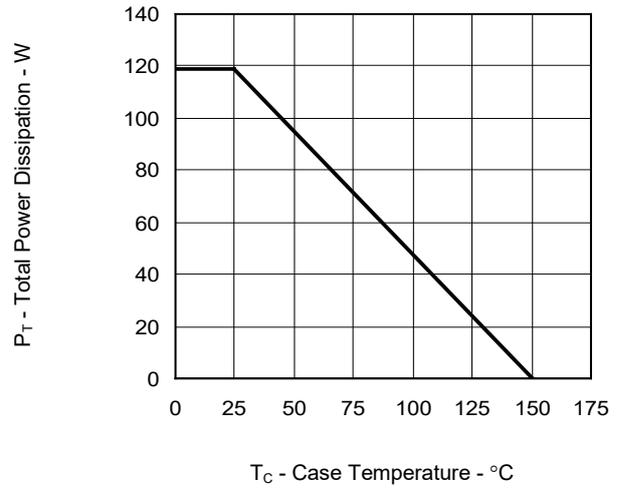


Typical Characteristics Note6

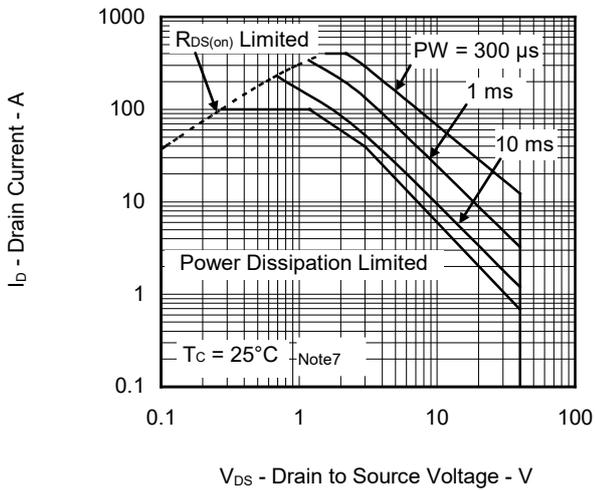
DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA



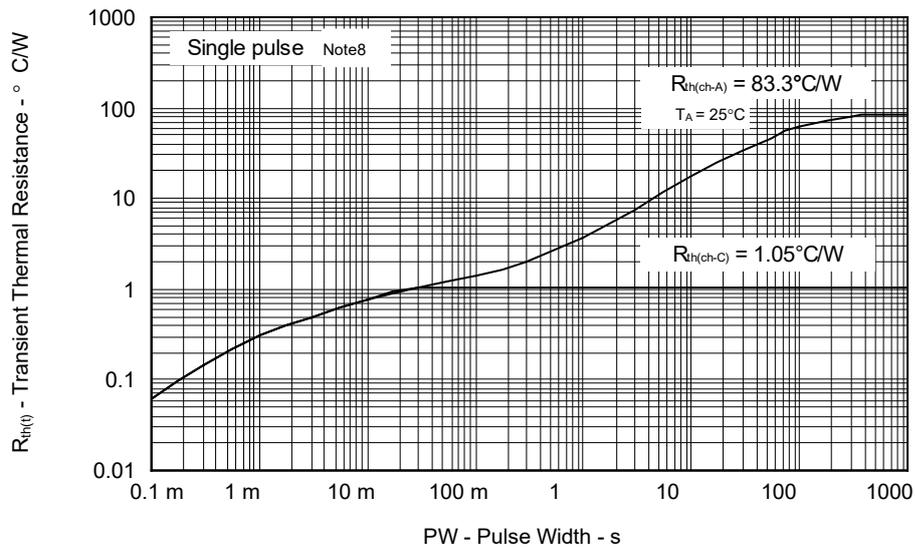
TOTAL POWER DISSIPATION vs. CASE TEMPERATURE



FORWARD BIAS SAFE OPERATING AREA

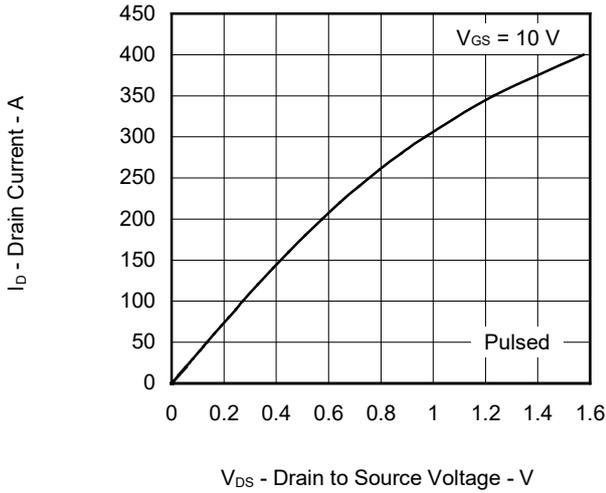


TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

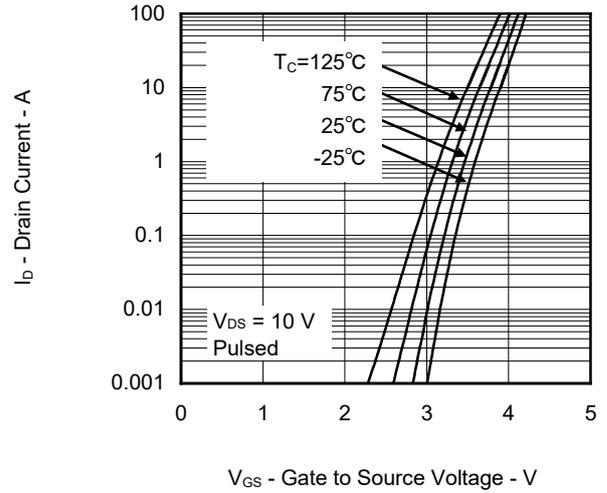


- Notes: 6. Designed target value on Renesas measurement condition. (T<sub>c</sub> = 25°C, unless otherwise specified)  
 7. This data is the designed value on Renesas's measurement condition. Renesas recommends that operating conditions are designed according to a document "Power MOSFET/IGBT Attention of Handling Semiconductor Devices (R07ZZ0010)".  
 8. This data is the designed target maximum value on Renesas's measurement condition.

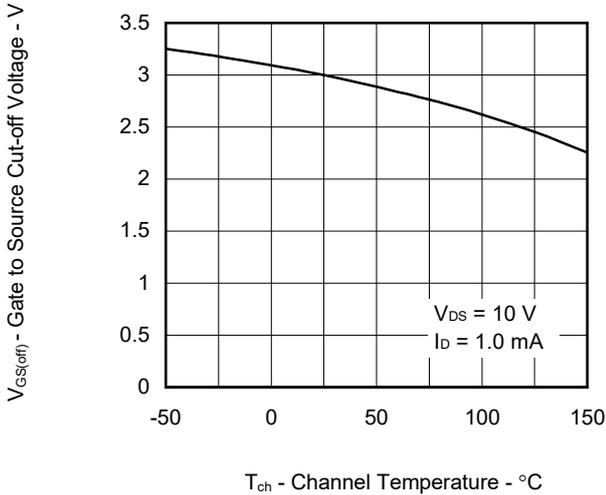
DRAIN CURRENT vs.  
DRAIN TO SOURCE VOLTAGE



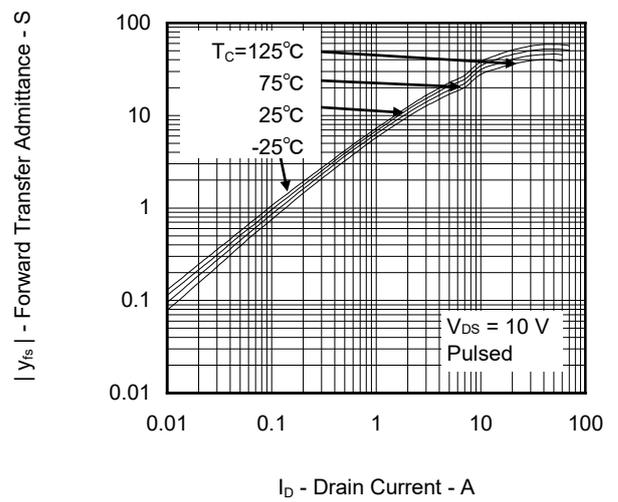
FORWARD TRANSFER  
CHARACTERISTICS



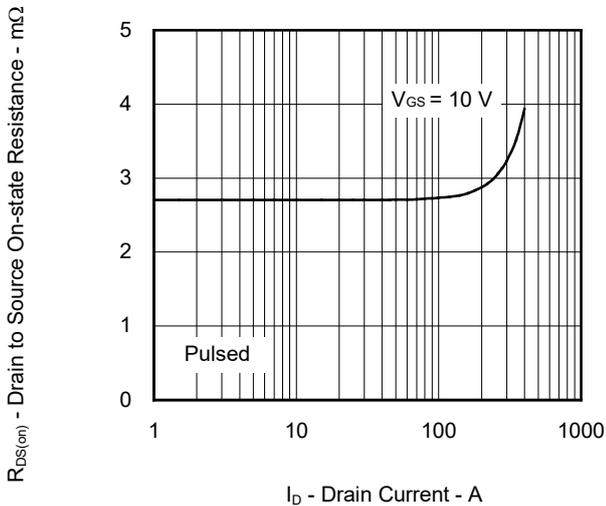
GATE TO SOURCE CUT-OFF VOLTAGE  
vs. CHANNEL TEMPERATURE



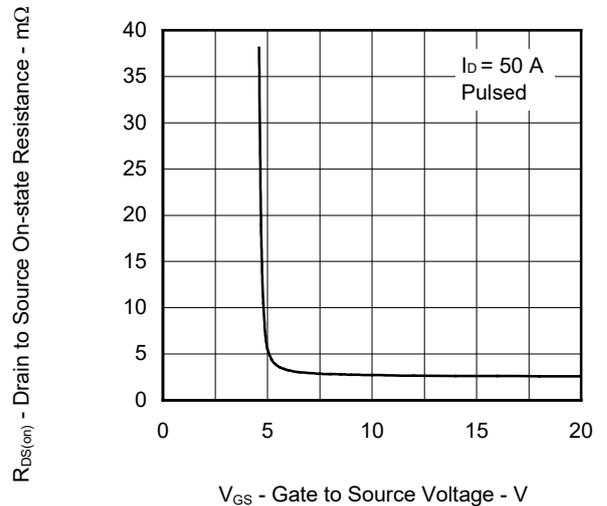
FORWARD TRANSFER ADMITTANCE vs.  
DRAIN CURRENT



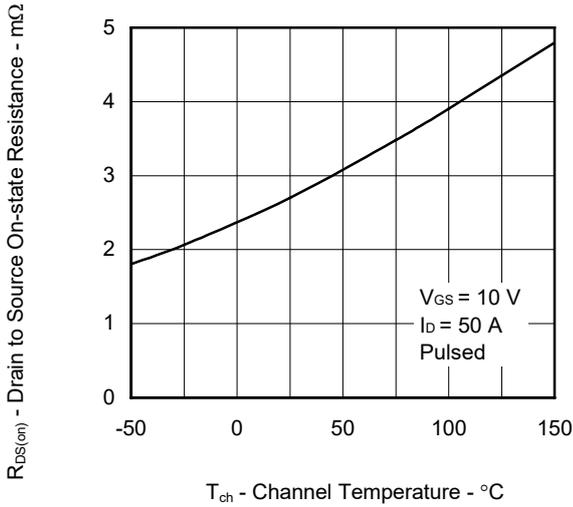
DRAIN TO SOURCE ON-STATE RESISTANCE  
vs. DRAIN CURRENT



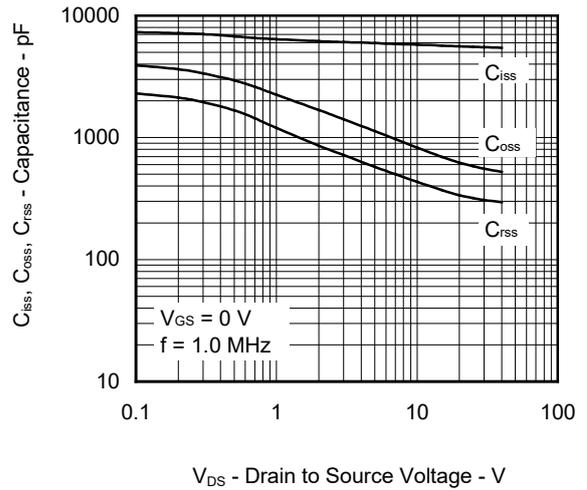
DRAIN TO SOURCE ON-STATE RESISTANCE  
vs. GATE TO SOURCE VOLTAGE



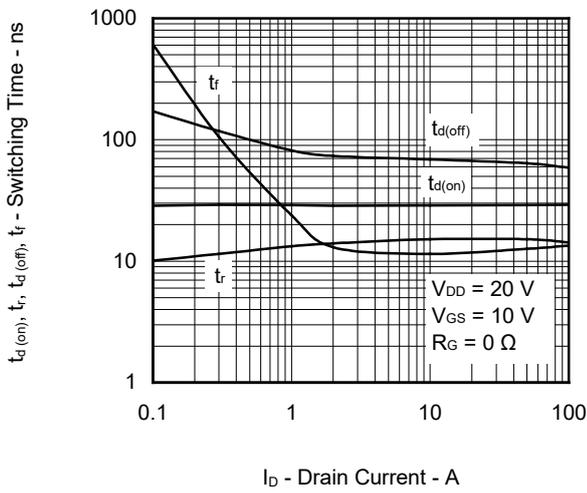
DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



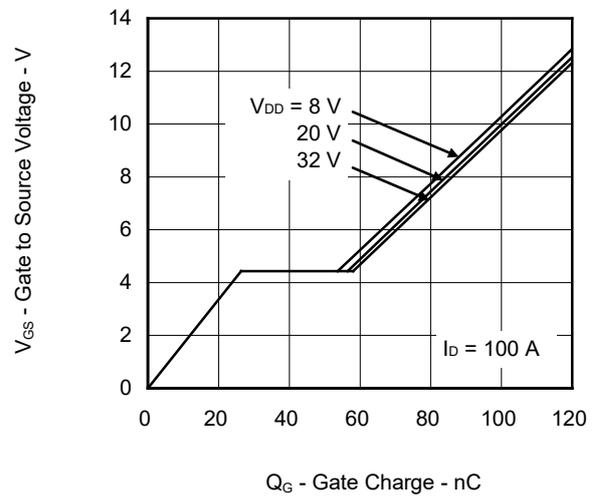
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



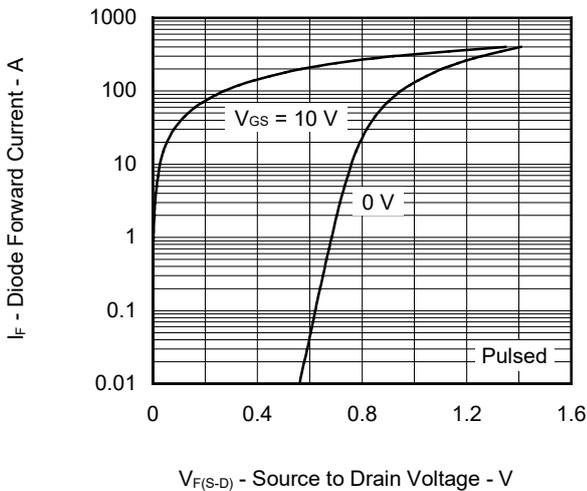
SWITCHING CHARACTERISTICS



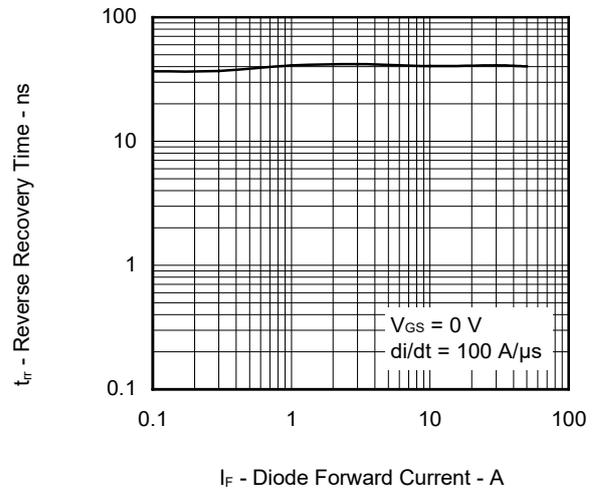
DYNAMIC INPUT CHARACTERISTICS



SOURCE TO DRAIN DIODE FORWARD VOLTAGE



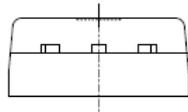
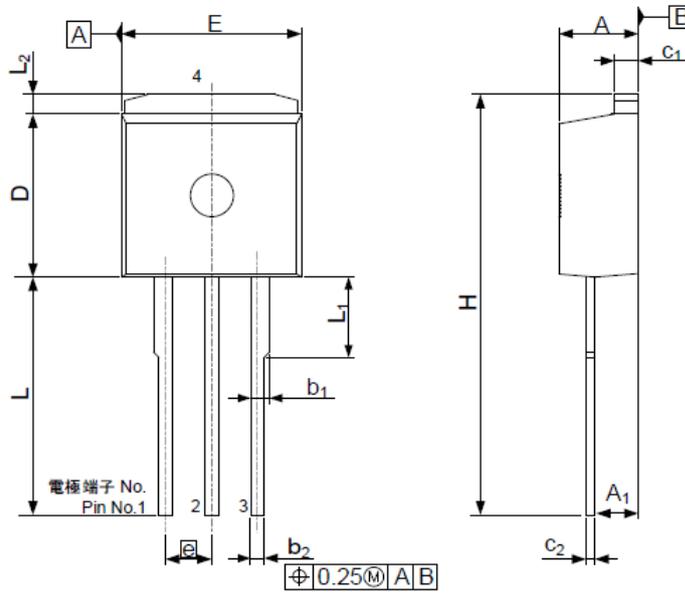
REVERSE RECOVERY TIME vs. DIODE FORWARD CURRENT



Package Drawing (Unit: mm)

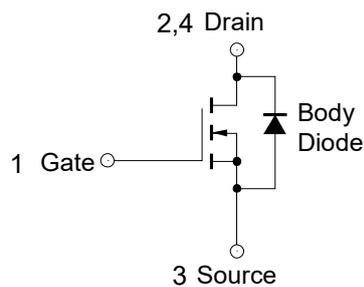
Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS (Typ) [g]
TO-262	—	PRSS0004AR-A	TO-262A	1.4

Unit: mm



Reference Symbol	Dimensions in mm		
	MIN.	NOM.	MAX.
A	4.20	4.40	4.60
A <sub>1</sub>	2.05	2.40	2.75
b <sub>1</sub>	0.635	1.05	1.40
b <sub>2</sub>	0.64	0.75	0.88
c <sub>1</sub>	1.14	1.30	1.40
c <sub>2</sub>	0.33	0.50	0.60
D	8.50	9.25	9.65
E	9.65	10.00	10.37
e	2.54 BSC		
H	—	23.85	—
L	12.90	13.50	14.10
L <sub>1</sub>	—	4.55	4.80
L <sub>2</sub>	—	1.10	1.727

Equivalent Circuit



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9. 不可将瑞萨电子产品和技术用于或者嵌入日本国内或海外相应的法律法规所禁止生产、使用及销售的任何产品或系统中。也不可将瑞萨电子产品或技术用于(1)与大规模杀伤性武器（例如核武器、化学武器、生物武器或运送上等武器的导弹，包括无人机(UAV)）的开发、设计、制造、使用、存储等相关的任何目的；(2)与常规武器的开发、设计、制造或使用相关的任何目的；(3)扰乱国际和平与安全的任何其他目的，并且不可向任何第三方销售、出口、租赁、转让、或让与瑞萨电子产品或技术，无论直接或间接地或者有理由知悉该第三方或任何其他方将从上述活动。用户必须遵守对各方或交易行使司法管辖权的任意国家/地区政府所公布和管理的任何适用出口管制法律法规。
10. 瑞萨电子产品的买方或分销商，或者分销、处置产品、或以其他方式向第三方出售或转让产品的任何其他方有责任先向所述第三方通知本文件规定的内容和条件。
11. 在事先未得到瑞萨电子书面认可的情况下，不得以任何形式部分或全部再版、转载或复制本文件。
12. 如果对本文件中记载的信息或瑞萨电子产品有任何疑问，请向瑞萨电子的营业部门咨询。  
(注1) 瑞萨电子：在本文件中指瑞萨电子株式会社及其控股子公司。  
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