TFT MODULE SPECIFICATION

RVT101HVBNWC00

EVE4 IPS 10.1" LCD TFT DATASHEET

Rev.1.6
2022-11-30

ITEM	CONTENTS	UNIT
LCD Type	TFT/Transmissive/Normally Black/IPS	/
Size	10.1	Inch
Viewing Direction	Free	/
Outside Dimensions (W x H x D)	257.96 x 168.60 x 13.17	mm
Active Area (W x H)	216.96 x 135.60	mm
Pixel Pitch (W x H)	0.1695 x 0.1695	mm
Resolution	1280 x 800	/
Brightness	800	cd/m^2
Color Depth	16.7 M	/
Pixel Arrangement	RGB Vertical Stripe	/
Driver IC of Board	BT817Q	/
Interface	SPI/QSPI	/
QSPI Flash Memory size	512	Mb
Host Connector	RiBUS, ZIF 20 pin, 0.5mm pitch, down-side contact	/
With/Without Touch	With Projected Capacitive Touch Panel	/
CTP Driver	ILI2132A	/
Supply Voltage for Module	3.3	V
Supply Voltage for Backlight	7.0 ÷ 14.0	V
Audio amplifier	Build in class-D 1.5W audio amplifier	/
Weight	430	g

Note 1. RoHS3 compliant

Note 2. LCM weight tolerance: ± 5%.

1. REVISION RECORD

REV NO.	REV DATE	CONTENTS	REMARKS
1.0	2021-04-19	Initial Release	
1.1	2021-06-03	Adding assembly guide Updating the drawing	
1.2	2021-07-16	Updating the drawing (dimensions in inches, adding the speaker), New template	
1.3	2021-10-27	Correct Supply Voltage for Backlight From 5.0 V to 7.0V	
1.4	2021-12-06	Add the accessory link of Riverdi louder speaker: RVA-SPK1.5W-C150, which is matched with Riverdi's all EVE4 series displays.	
1.5	2022-04-13	Correction on figure of simplified audio circuit design from R4, 1K resistor to R4, 100K resistor. R4 100K is the actual resistor value on PCB.	
1.6	2022-11-30	• Update the backlight electrical parameters • Add more detailed info like QSPI flash memory size, Audio amplifier etc. • Drawing update with adding grounding tape and dimensions overhaul	

2. CONTENTS

3. MODULE CLASSIFICATION INFORMATION

RV	T	101	H	V	B	N	W	C	00
1.	2.	3.	4.	5.	6.	7.	8.	9.	10.

NO.	PARAMETER	SYMBOL
1.	BRAND	RV – Riverdi
2.	PRODUCT TYPE	T – TFT Standard
3.	DISPLAY SIZE	101 – 10.1”
4.	MODEL SERIAL NO.	H – High Brightness, IPS
5.	RESOLUTION	V – 1280 x 800 px
6.	INTERFACE	B – SPI/QSPI
7.	FRAME	N – Without Mounting Metal Frame
8.	BACKLIGHT TYPE	W – LED White
9.	TOUCH PANEL	C – With Capacitive Touch Panel
10.	VERSION	00 – (00-99)

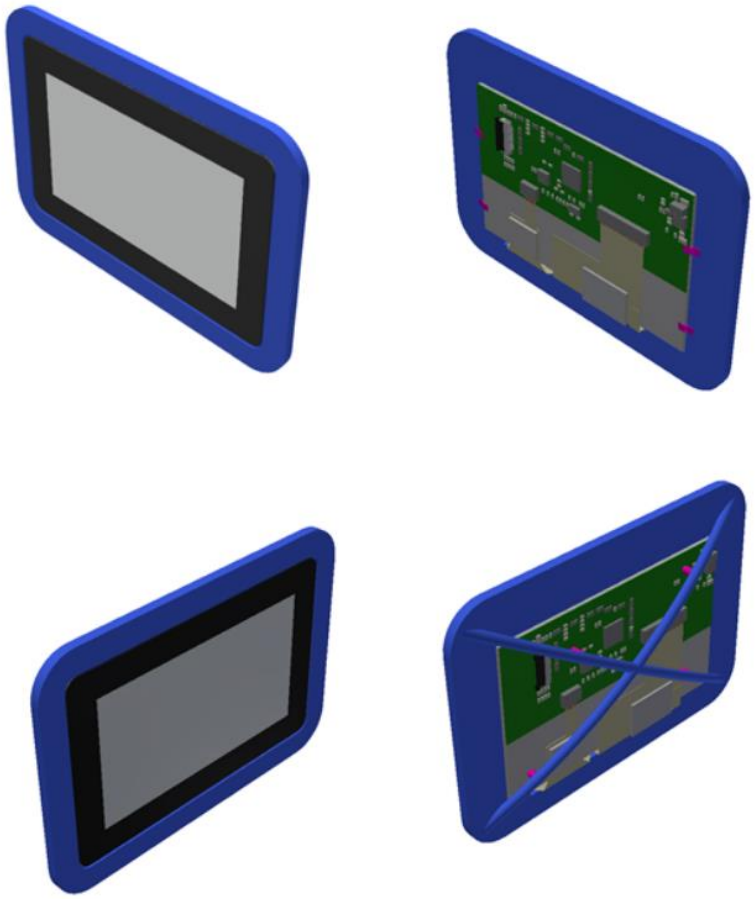
4. ASSEMBLY GUIDE

4.1 uxTouch ASSEMBLY

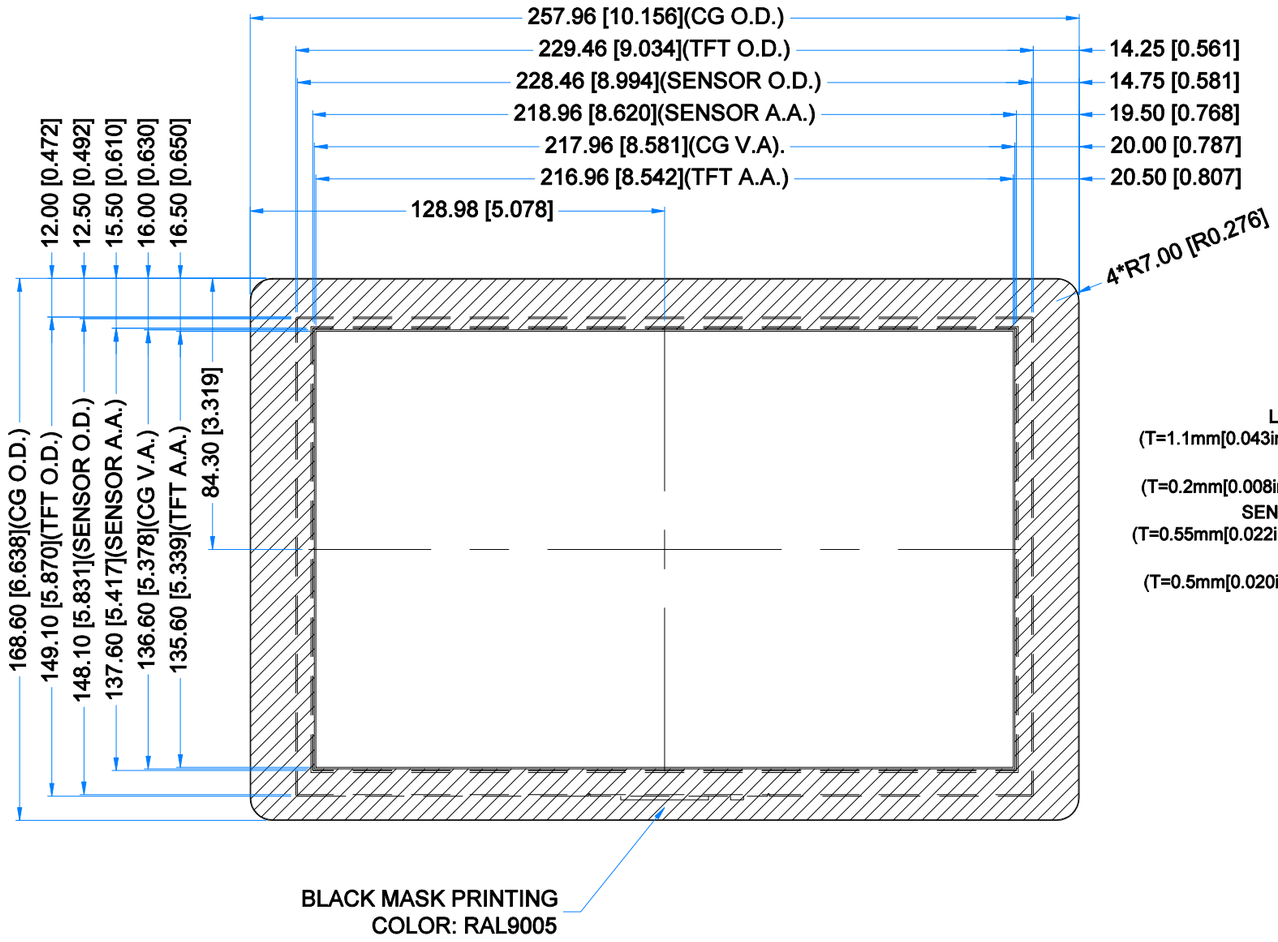
uxTouch are LCD TFT displays with specially designed projected capacitive touch panels. uxTouch display can be mounted without any holed in the housing. Our standard uxTouch displays include double-sided adhesive tape (DST) to stick TFT easily to the housing. Basic series include 4.3”, 5.0”,7.0” and 10.1” display sizes.

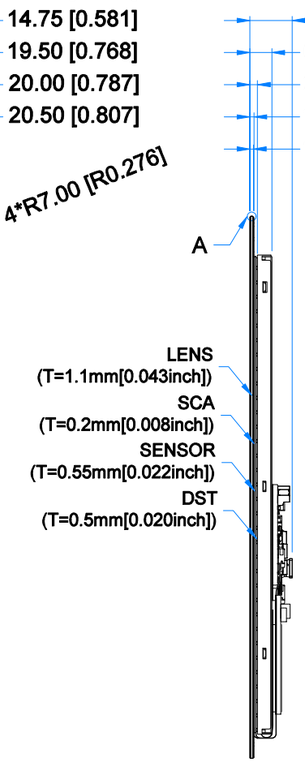
uxTouch models with double-side adhesive tape can be mounted by connecting the glass to the housing. Riverdi recommends using support brackets assembled to display’s back. An additional support will stiffen the whole structure and minimize the influence of external factors such as vibration. Figure 1 below show examples of using support elements.

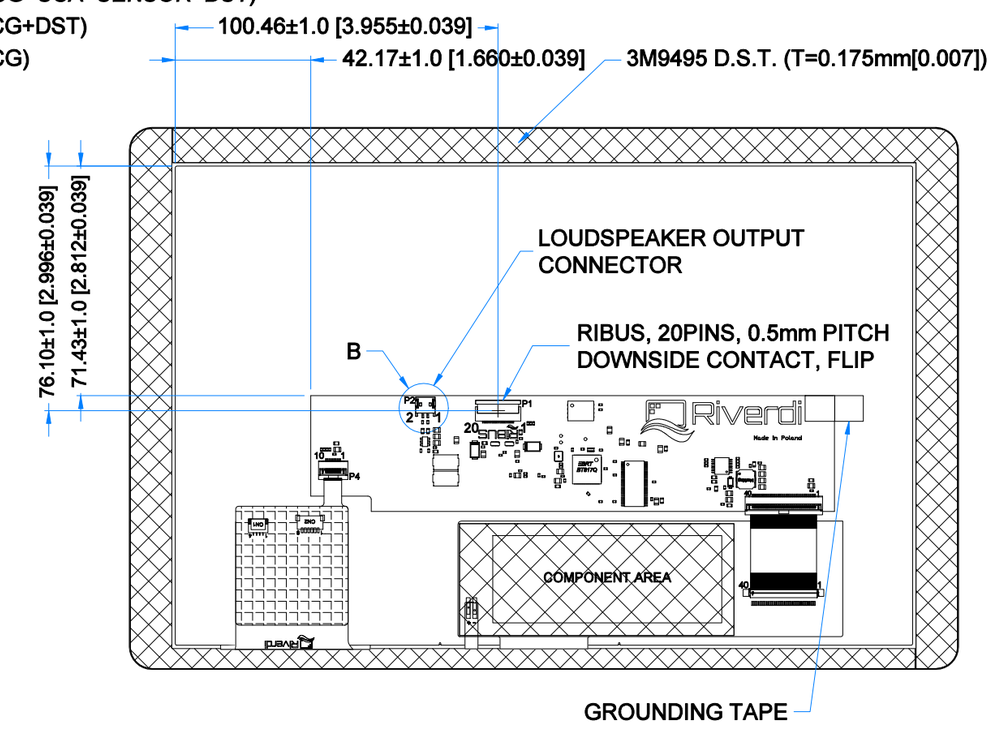
Figure 1. Example of using support brackets

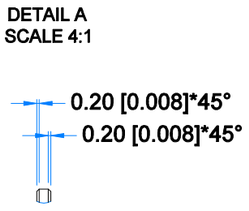


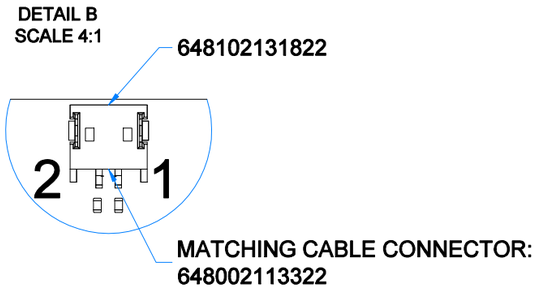
Revision:	Changes:	Date:
1.0	Initial Case	2020.12.09
1.1	PCB Update And Dimension Overhaul	2021.04.20
1.2	Adding Speaker PN And Matching Connector PN	2021.07.08
1.3	PCB Update And Added Grounding Tape And Dimensions Overhaul	2022.08.31

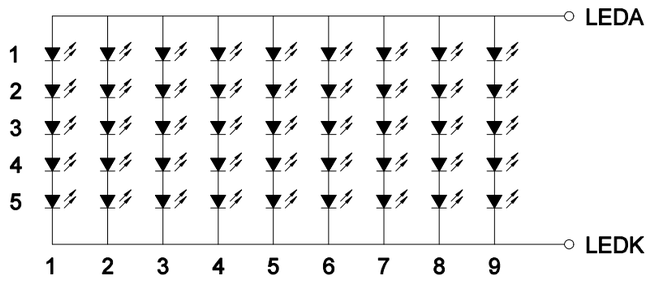












LED Diagram Circuit

LCD NOTES:
1. LCD TYPE: TRANSMISSIVE, NORMALLY BLACK, IPS
2. RESOLUTION: 1280x800
3. VIEWING ANGLE: FREE
4. SURFACE LUMINANCE: 800 cd/m^2
5. DRIVING IC ON THE BOARD: BT817Q
6. INTERFACE: SPI/QSPI
7. SUPPLY VOLTAGE FOR MODULE: 3.3 V
8. SUPPLY VOLTAGE FOR BACKLIGHT: 7.0 ÷ 14.0 V, BUILT-IN LED INVERTER

TP NOTES:
1. TP STRUCTURE: G+G
2. CG THICKNESS: 1.10mm[0.043inch]
3. SURFACE HARDNESS: 7H
4. DRIVER IC: ILI2132A
5. INTERFACE: SPI/QSPI VIA RIBUS AND BT817Q

GENERAL NOTES:
1. ZERO BAD PIXEL FOR TFT
2. AIR BONDING BETWEEN TFT AND CTP
3. OPERATING TEMPERATURE: -20°C ~ 70°C
4. STORAGE TEMPERATURE: -30°C ~ 80°C
5. WITHOUT INDIVIDUAL TOLERANCE: ±0.3mm[0.012inch]
6. RoHS3 COMPLIANT

PN: RVT101HVBNWC00				
SN:				
DRAWN:	M.Natywa	2022.08.31	1:1.91	
CHECKED:	Carol Gao	2022.09.13	[mm]	
APPR:			ISO A3	P. 1 of 1

6. ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	MIN	MAX	UNIT	NOTE
Supply Voltage for Module	VDD	0	3.6	V	Note 1
Digital I/O signals (SPI/QSPI/GPIO) Voltage	-	-0.5	3.3		Note 1, 2
Supply voltage for Backlight	BLVDD	-0.3	16.0		Note 1
Operating Temperature	T_{OP}	-20	70	°C	
Storage Temperature	T_{ST}	-30	80	°C	
Storage Humidity (@ 25 ± 5°C)	H_{ST}	10	-	% RH	
Operating Ambient Humidity (@ 25 ± 5°C)	H_{OP}	10	-	% RH	

Note 1. Exceeding maximum values may cause improper operation or permanent damage to the unit.

Note 2. Digital I/O signals are to be connected to pins 3 ÷ 9, 11 and 12 pins at RiBUS connector (P1).

7. ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Supply Voltage for Module	VDD	3.0	3.3	3.6	V	
Input Voltage “H” Level	V_{IH}	2.0	-	3.3	V	
Input Voltage “L” Level	V_{IL}	-	-	0.8	V	

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Current drawn from VDD@3.3V	I_{VDD}	-	399	731	mA	Note 1

Note 1. Animated pictures are displayed on the screen and there is no QSPI communication during the measurement of TYP and MAX values.

TYP value is measured when the audio is off.

MAX value is measured when the audio is on, and volume is set to maximum.

Riverdi loudspeaker RVA-SPK1.5W-C150 is applied during the measurement.

8. BACKLIGHT ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Supply Voltage for Backlight	BLVDD	7.0	9.0	14.0	V	
Lifetime	-	-	50,000	-	hours	Note 1

Note 1. Operating life means the period in which the LED brightness goes down to 50% of the initial brightness. Typical operating lifetime is the estimated parameter.

PARAMETER	SYMBOL	MIN BL	50% BL	100% BL	UNIT	NOTE
Current drawn from BLVDD @7.0V	I_{BLVDD}	20	408	880	mA	Note 2
Current drawn from BLVDD @9.0V		14	308	650		
Current drawn from BLVDD @14.0V		-	191	394		

Note 2. To control the backlight dimming, please refer to subchapter 11.4.

MIN BL is when REG_PWM_DUTY = 1

50% BL is when REG_PWM_DUTY = 64

100% BL is when REG_PWM_DUTY = 128

9. ELECTRO-OPTICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT	RMK	NOTE
Response Time	Tr+Tf	θ=0° ø=0° Ta=25 °C	-	25	35	ms	FIG 2.	4
Contrast Ratio	Cr		800	1000	-	---	FIG 3.	1
Luminance Uniformity	δ WHITE		-	75	-	%		3
Surface Luminance	Lv		-	800	-	cd/m²		2
Viewing Angle Range	θ	ø = 90°	75	85	-	deg	FIG 4.	6
		ø = 270°	75	85	-	deg		
		ø = 0°	75	85	-	deg		
		ø = 180°	75	85	-	deg		
CIE (x, y) Chromaticity	Rx	θ=0° ø=0° Ta=25 °C	0.22	0.26	0.30	-	FIG 3.	5
	Ry		0.20	0.24	0.28	-		
	Gx		0.34	0.38	0.42	-		
	Gy		0.50	0.54	0.58	-		
	Bx		0.10	0.14	0.18	-		
	By		0.09	0.13	0.17	-		
	Wx		0.28	0.32	0.36	-		
	Wy		0.29	0.33	0.37	-		

Note 1. Contrast Ratio (CR) is defined mathematically as below, for more information see Figure 3.

$$\text{Contrast Ratio} = \frac{\text{Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Average Surface Luminance with all black pixels (P1, P2, P3, P4, P5)}}$$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see Figure 3.

Lv = Average Surface Luminance with all white pixels (P1, P2, P3, P4, P5)

Note 3. The uniformity in surface luminance δ WHITE is determined by measuring luminance at each test position 1 through 5, and then dividing the minimum luminance of

5 points luminance by maximum luminance of 5 points luminance. For more information see Figure 3.

$$\delta \text{ WHITE} = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5)}}$$

Note 4. Response time is the time required for the display to transition from white to black (Rise Time, Tr) and from black to white (Decay Time, Tf). For additional information see Figure 2. The test equipment is BM-7A.

Note 5. CIE (x, y) chromaticity, the x, y value is determined by measuring luminance at each test position 1 through 5, and then calculating the average value.

Note 6. For TFT module the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to LCD surface. For more information see Figure 4.

Note 7. Viewing angle is measured at the center point of the LCD by CONOSCOPE (ergo-80). For response time testing, the testing data is based on BM-7A. Instruments for Contrast Ratio, Surface Luminance, Luminance Uniformity, Chromaticity the test data is based on SR-3A.

Figure 2. The definition of response time

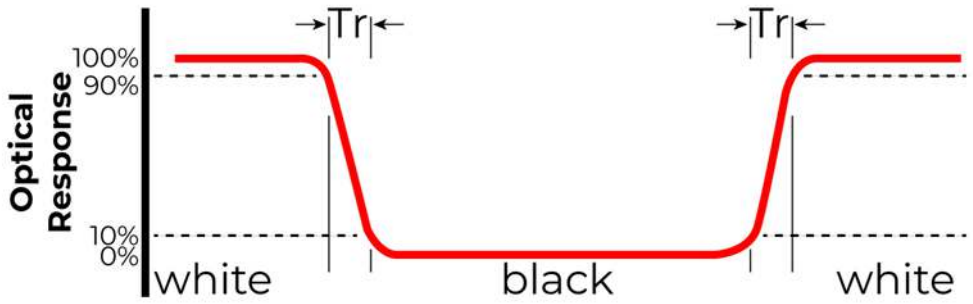
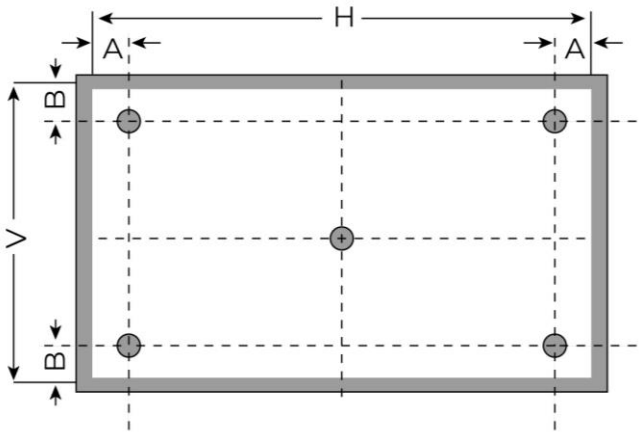


Figure 3. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x, y) chromaticity



A: 5mm

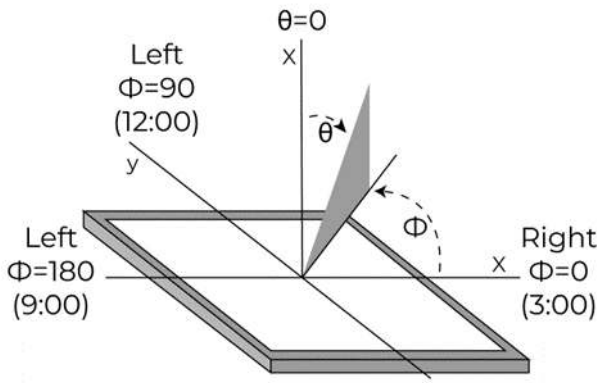
B: 5mm

H, V: Active Area

Light spot size Ø=5mm, 500mm distance from the LCD surface to detector lens.

Measurement instrument is SR-3A

Figure 4. The definition of viewing angle



10. INTERFACES DESCRIPTION

10.1 P1 connector - RiBUS description

PIN NO.	CONNECTOR	DESCRIPTION	NOTE
1	VDD	Supply voltage for module; TYP 3.3 V	
2	GND	Ground	
3	SPI_SCLK	SPI SCK signal	
4	MISO/ IO.1	SPI MISO signal / SPI Quad mode: SPI data line 1	
5	MOSI/ IO.0	SPI MOSI signal / SPI Quad mode: SPI data line 0	
6	CS	SPI chip select signal	
7	INT	Interrupt signal from device to the system, Active Low, internally 47k Pull UP	
8	RST/PD	Reset / Power down signal, Active Low, Internally Pulled UP 47k	
9	GPIO.0	GPIO.0	
10	DISP_AUDIO	Display audio in/out	
11	GPIO.1/IO.2	SPI Single/Dual mode: General purpose IO0. QSPI mode: SPI data line 2	
12	GPIO.2/IO.3	SPI Single/Dual mode: General purpose IO1. QSPI mode: SPI data line 3	
13	NC	Not connected	
14	NC	Not connected	
15	NC	Not connected	
16	NC	Not connected	
17	BLVDD	Supply voltage for backlight	
18	BLVDD	Supply voltage for backlight	
19	BLGND	Backlight Ground, internally connected to GND	
20	BLGND	Backlight Ground, internally connected to GND	

Note. Matched 20 pins, 0.5 mm pitch, 150mm long FFC accessory: FFC0520150

10.2 P2 connector description- Audio interface description

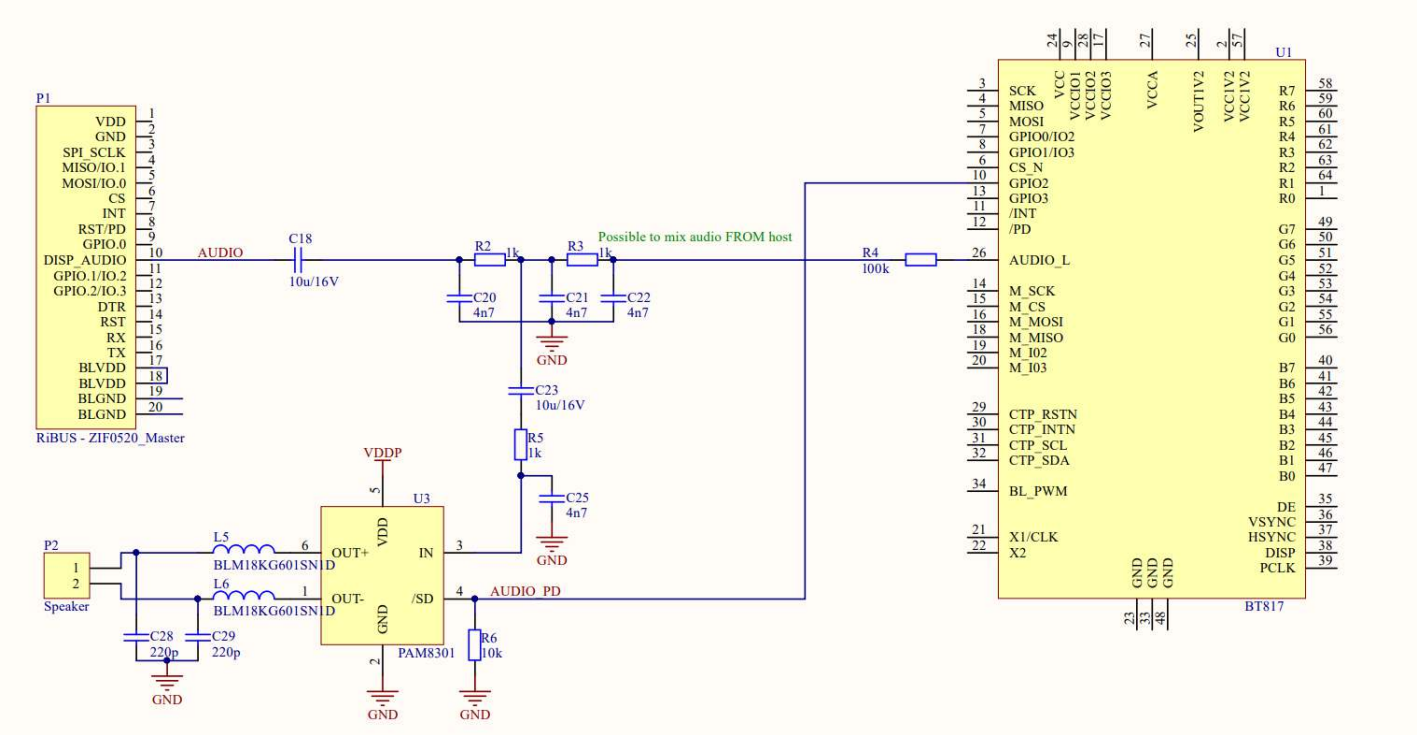
PIN NO.	SYMBOL	DESCRIPTION	NOTE
1	SPEAKER +	Speaker coil “+” terminal	Note 1
2	SPEAKER -	Speaker coil “-” terminal	

The audio circuit allows for the following 3 modes:

1. To play sounds from BT817Q on internal amplifier U3.
2. To play sounds from host on internal amplifier U3.
3. To play sounds from BT817Q on external amplifier.

Note 1. Matched Riverdi louder speaker for all EVE4 series displays: RVA-SPK1.5W-C150

Figure 5. The simplified audio circuit design



11. BT817Q CONTROLLER SPECIFICATION

BT817Q or EVE4 (Embedded Video Engine 4) simplifies the system architecture for advanced human machine interfaces (HMIs) by providing functionality for display, audio, and touch as well as an object-oriented architecture approach that extends from display creation to the rendering of the graphics.

11.1 Serial host interface

Figure 6.SPI single/dual interface connection

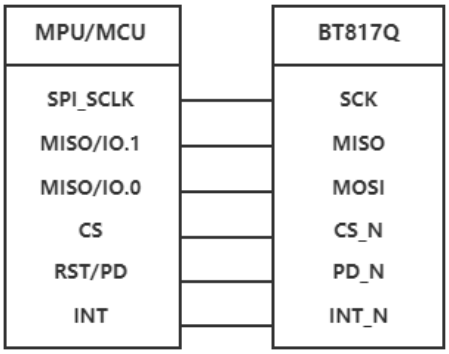
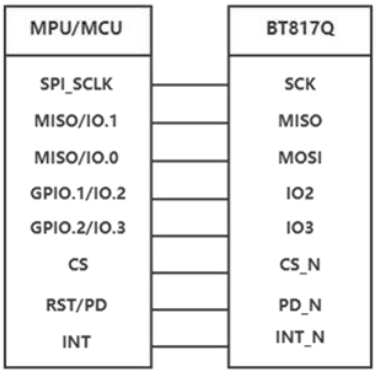


Figure 7. QSPI interface connection



SPI Interface – the SPI slave interface operates up to 30MHz.

Only SPI mode 0 is supported. The SPI interface is selected by default.

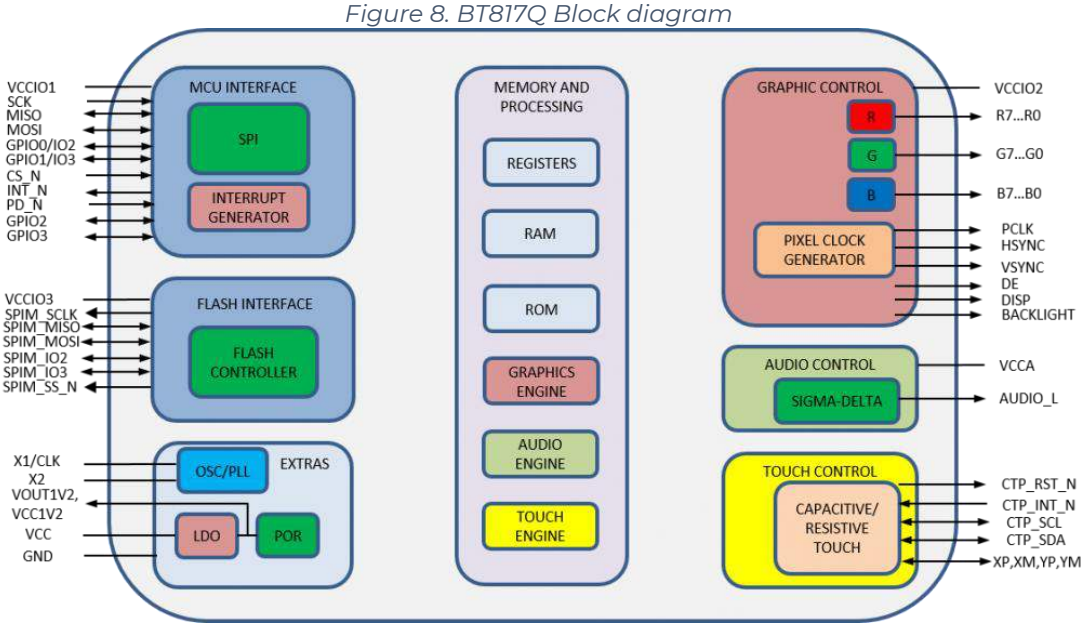
QSPI Interface – the QSPI slave interface operates up to 30MHz. Only SPI mode 0 is supported.

The QSPI can be configured as a SPI slave in SINGLE, DUAL or QUAD channel modes.

By default, the SPI slave operates in the SINGLE channel mode with MOSI as input from the master and MISO as output to the master. DUAL and QUAD channel modes can be configured through the SPI slave itself. To change the channel modes, write to register REG_SPI_WIDTH.

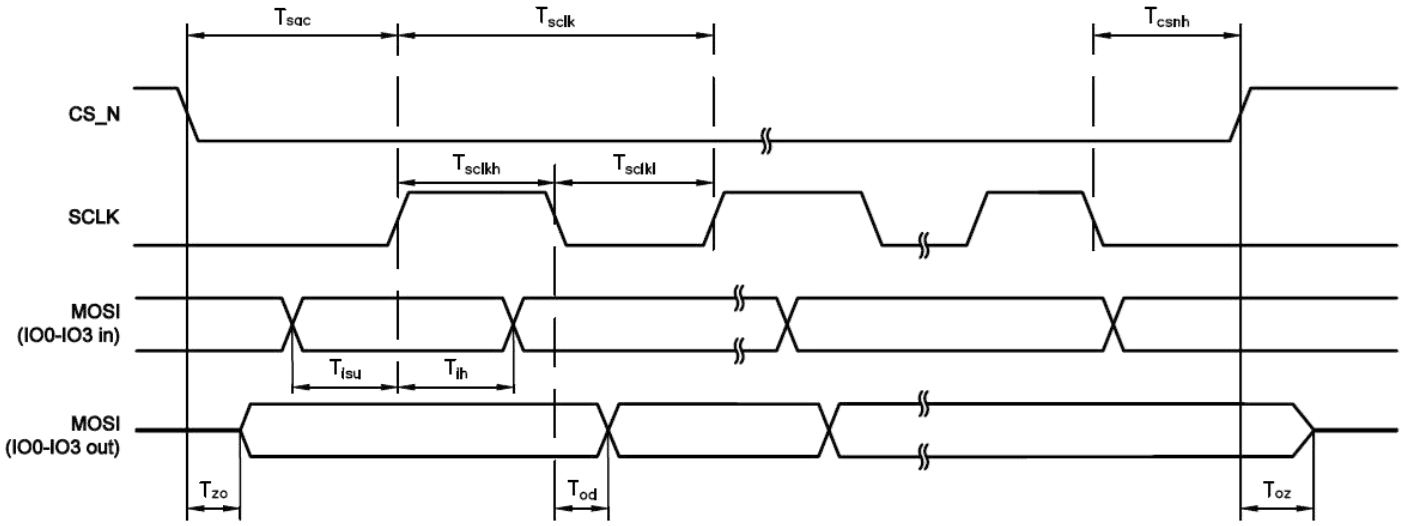
11.2 Block diagram

Figure 8. BT817Q Block diagram



11.3 Host interface SPI mode 0

Figure 9. SPI timing diagram



The meanings of the timings in the Figure 9 are defined in the table below.

PARAMETER	DESCRIPTION	VCCIO=1.8V		VCCIO=2.5V		VCCIO=3.3V		UNIT
		Min	Max	Min	Max	Min	Max	
T_{sclk}	SPI clock period	33.3	-	33.3	-	33.3	-	ns
T_{sclkl}	SPI clock low duration	13	-	13	-	13	-	
T_{sclkh}	SPI clock high duration	13	-	13	-	13	-	
T_{sac}	SPI access time	4	-	3.5	-	3	-	
T_{isu}	Input Setup	4	-	3.5	-	3	-	
T_{ih}	Input Hold	0	-	0	-	0	-	
T_{zo}	Output enable delay	-	16	-	13	11	-	
T_{oz}	Output disable delay	-	13	-	11	10	-	
T_{od}	Output data delay	-	15	-	12	11	-	
T_{csnh}	CSN hold time	0	-	0	-	0	-	

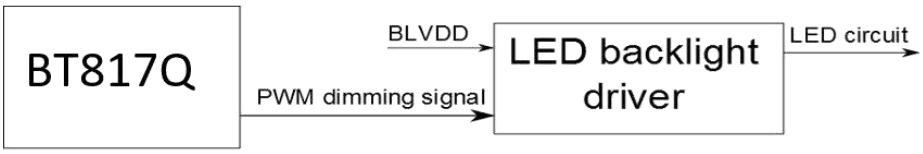
For more information about BT817Q controller please go to official BT81x website.
https://brtchip.com/product/bt817/

11.4 Backlight driver block diagram

Backlight enable signal is internally connected to BT817Q backlight control pin. This pin is controlled by two BT817Q's registers.

REG_PWM_HZ specifies the PWM output frequency. **REG_PWM_DUTY** specifies the duty cycle. Refer to BT817Q datasheet for more information.

Figure 10. Backlight driver block diagram



The LED backlight driver used in this module does not burst the LED current. Therefore, it does not generate audible noises on the output capacitor. It is equipped with soft start subsystem, which increases LED lifetime, as LED current peaks are reduced significantly.

12.512Mb NOR FLASH MEMORY

The Riverdi EVE4 10.1" series modules are built with a 512Mb NOR flash memory chip. Graphics assets such as fonts, audio, and images can be stored in the flash memory. Up to 110 full resolution (1280 * 800 pixels, JPG) images can be stored. If you need to change the memory size, please contact: contact@riverdi.com

There is an additional port P3 for programming the flash memory directly from an external source. This port is designed to be used during production if the customer wants to order pre-programmed EVE4 boards with graphic content of their own choice.

Cable TC2050-IDC-NL is compatible with P3 programming port.

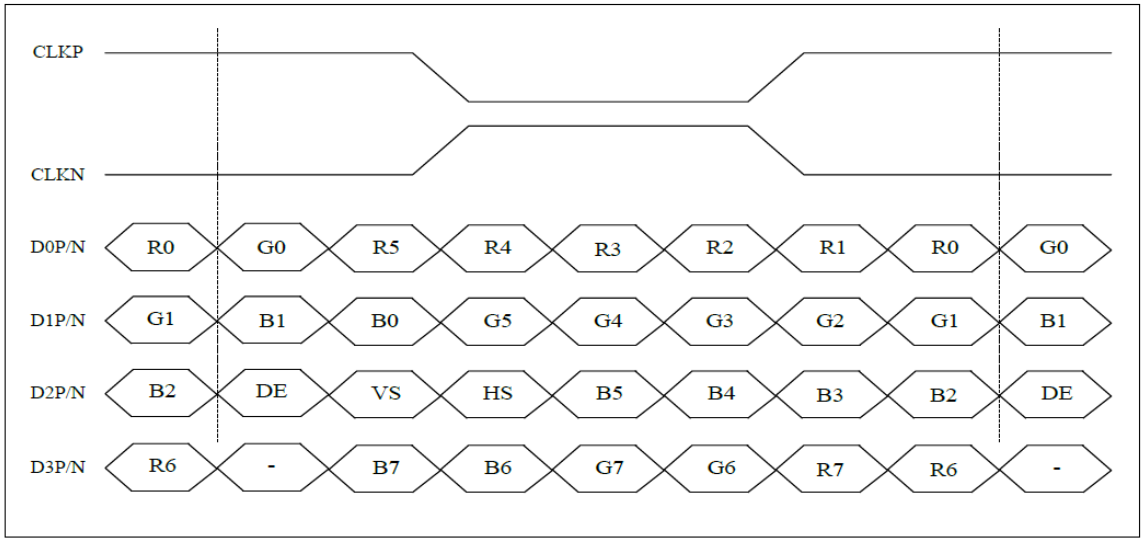
13.TFT TIMING CHARACTERISTICS

The TFT of the module applies Riverdi high brightness, IPS, 10.1" LVDS TFT: RVT101HVLNWC00

For detailed information of the display, please refer to datasheet of display.

13.1 LVDS interface characteristic

VESA Format: 8-bit LVDS input, (LVBIT=H, LVFMT=H)



Note 1. Control signals DE VS HS: Active Low

13.2 Timing table

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Clock Frequency (Rate=60Hz (LVDS))	F_{DCLK}	66.3	72.4	78.9	MHz
HSYNC Period Time	T_H	1380	1440	1500	DCLK
Horizontal Display area	T_{HD}		1280		DCLK
Hsync Pulse Width	T_{HPW}	1	-	40	Tc
Hsync Back Porch (With pulse width)	T_{HBP}	88	88	88	DCLK
Hsync Front Porch	T_{HFP}	12	72	132	DCLK

VSYNC Period Time	T_V	824	838	872	H
Vertical Display area	T_{VD}		800		
Vsync Pulse Width	T_{VW}	1	-	20	
Vsync Back Porch (With pulse width)	T_{VBP}	23	23	23	
Vsync Front Porch	T_{VFP}	1	15	49	

14. CAPACITIVE TOUCH SCREEN PANEL SPECIFICATIONS

14.1 Mechanical characteristics

DESCRIPTION	SPECIFICATION	REMARK
Touch Panel Size	10.1 inch	uxTouch
Outline Dimension of CTP	257.96 mm x 168.60 mm	
Product Thickness	2.35 mm	
Glass Thickness	1.1 mm	
CTP View Area	217.96 mm x 136.60 mm	
Sensor Active Area	218.96mm x 137.60 mm	
Surface Hardness	7H	

14.2 Electrical characteristics

DESCRIPTION	SPECIFICATION	REMARK
Power Consumption (IDD)	90 mA	
Linearity	+/- 1.5mm	
Controller	ILI2132A	
Resolution	1280 x 800	

15. MODULE INITIALIZATION

1. There is no need to set touch calibration matrix (REG_TOUCH_TRANSFORM_A-F registers in BT817Q) as touch panel resolution and orientation are the same as display, so default values in BT817Q are correct.
2. Initialization data, timings and example codes are available on the Riverdi GitHub, at address: https://github.com/riverdi/riverdi-eve
3. REGISTER VALUES:

REGISTER NAME	REGISTER VALUE (DEC.)
REG_HSIZE	1280
REG_VSIZE	800
REG_HCYCLE	1440
REG_HOFFSET	88
REG_HSYNC0	0
REG_HSYNC1	20
REG_VCYCLE	838
REG_VOFFSET	23
REG_VSYNC0	0
REG_VSYNC1	10
REG_PCLK	1
REG_SWIZZLE	0
REG_PCLK_POL	1
REG_CSPREAD	0
REG_DITHER	0
REG_PCLK_FREQ	2241 (0x8C1)
REG_PCLK_2X	1

16. INSPECTION

Standard acceptance/rejection criteria for TFT module

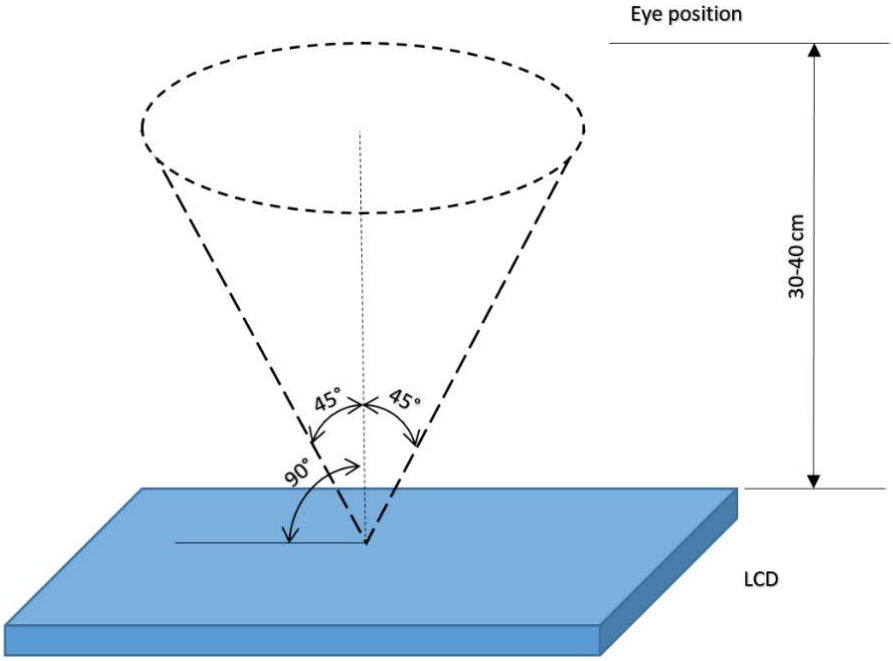
16.1 Inspection condition

Ambient conditions:

- Temperature: 25 ± 2°C
- Humidity: (60 ± 10) %RH
- Illumination: Single fluorescent lamp non-directive (300 to 700 lux)

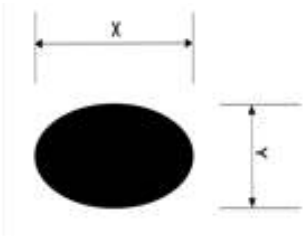
Viewing distance: 35 ± 5cm between inspector bare eye and LCD.

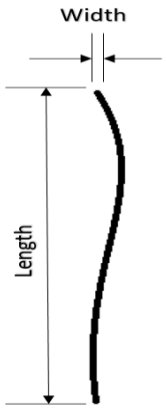
Viewing Angle: U/D: 45°/45°, L/R: 45°/45°



16.2 Inspection standard

The LCD TFT has zero bad pixels. Please refer the item “Bright/Dark dots”.

<table>
<tr><th>ITEM</th><th colspan="4">CRITERION</th></tr>
<tr><td rowspan="5">Black spots, white spots, light leakage, Foreign Particle (round Type)</td><td rowspan="5"> 

D=(x+y)/2
Spots density: 10 mm</td><td colspan="3">Size =10.1”</td></tr>
<tr><td colspan="2">Average Diameter</td><td>Qualified Qty</td></tr>
<tr><td colspan="2">D ≤ 0.2 mm</td><td>Ignored</td></tr>
<tr><td colspan="2">0.2 mm < D ≤ 0.3 mm</td><td>N≤4</td></tr>
<tr><td colspan="2">0.5mm < D</td><td>N = 0</td></tr>
<tr><td rowspan="5">LCD black spots, white spots, light leakage (line Type)</td><td rowspan="5"> 

Spots density: 10 mm</td><td colspan="3">Size =10.1”</td></tr>
<tr><td>Length</td><td>Width</td><td>Qualified Qty</td></tr>
<tr><td>-</td><td>W ≤ 0.05</td><td>Ignored</td></tr>
<tr><td>L ≤ 5.0</td><td>0.05< W ≤ 0.1</td><td>N ≤ 3</td></tr>
<tr><td>5.0 < L</td><td>0.10< W
5.0 < L</td><td>N = 0</td></tr>
<tr><td rowspan="5">Bright/Dark Dots</td><td colspan="3">Size =10.1”</td></tr>
<tr><td>Item</td><td colspan="2">Qualified Qty</td></tr>
<tr><td>Bright dots</td><td colspan="2">0</td></tr>
<tr><td>Dark dots</td><td colspan="2">0</td></tr>
<tr><td>Cluster Bright Dots or Dark Dots</td><td colspan="2">0</td></tr>
<tr><td rowspan="6">Clear spots</td><td>Total Bright and Dark Dots</td><td colspan="2">0</td></tr>
<tr><td colspan="3">Size ≥ 5”</td></tr>
<tr><td>Average Diameter</td><td colspan="2">Qualified Qty</td></tr>
<tr><td>D < 0.2 mm</td><td colspan="2">Ignored</td></tr>
<tr><td>0.2 mm < D < 0.3 mm</td><td colspan="2">4</td></tr>
<tr><td>0.3 mm < D < 0.5 mm</td><td colspan="2">2</td></tr>
<tr><td>0.5 mm < D</td><td colspan="2">0</td></tr>
<tr><td colspan="3">Spots density: 10 mm</td></tr>
<tr><td rowspan="4">Touch panel spot</td><td colspan="3">Size ≥ 5"</td></tr>
<tr><td>Average Diameter</td><td colspan="2">Qualified Qty</td></tr>
<tr><td>D < 0.25 mm</td><td colspan="2">Ignored</td></tr>
<tr><td>0.25 mm < D < 0.5 mm</td><td colspan="2">4</td></tr>
<tr><td></td><td>0.5 mm < D</td><td colspan="2">0</td></tr>
<tr><td rowspan="2"></td><td colspan="3">Size ≥ 5"</td></tr>
<tr><td>Length</td><td>Width</td><td>Qualified Qty</td></tr>
</table>

Touch panel White line Scratch	- L < 5.0 -	W < 0.03 0.03 < W < 0.05 0.05 < W	Ignored 2 0

17.RELIABILITY TEST

NO.	TEST ITEM	TEST CONDITION	Note
1	High Temperature Storage	80°C/120 hours	Note 1
2	Low Temperature Storage	-30°C/120 hours	
3	High Temperature Operating	70 °C /120 hours	
4	Low Temperature Operating	-20°C/120 hours	
5	High Temperature and High Humidity	Humidity 40℃, 90%RH, 120Hrs	
6	Thermal Cycling Test (No operation)	-20℃ for 30min, 70℃ for 30 min. 100 cycles. Then test at room temperature after 1 hour	Note 2
7	Vibration Test	Frequency: 10 ÷ 55 Hz. Stroke: 1.5 mm. Sweep: 10Hz ÷ 55Hz ÷ 10 Hz. 2 hours for each direction of X, Y, Z (Total 6 hours)	
8	Package Drop Test	Height: 60 cm 1 corner, 3 edges, 6 surfaces	

Note 1. Sample quantity for each test item is 5 ÷ 10 pcs.
Note 2. Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

18.LEGAL INFORMATION

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