SHARP GP2W0110YPS

 $(T_0=25^{\circ}C)$ 

# GP2W0110YPS

#### **■** Features

- 1. Compliant with IrDA1.2 low power
- 2. Integrated package of transmitter/receiver. (7.9×2.85×height 2.15mm)
- 3. General purpose
- 4. Low dissipation current due to shut-down function (Dissipation current at shut-down mode:Max.  $0.1\mu A$ )
- 5. Soldering reflow type
- 6. Shield type

## ■ Applications

- 1. Cellular phones, PHS
- 2. Personal information tools

### ■ Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Supply voltage	$V_{CC}$	0 to 6.0	V
LED Supply voltage	$V_{LEDA}$	0 to 7.0	V
*1 Peak forward current	$I_{FM}$	60	mA
Operating temperature	$T_{opr}$	-40 to +85	°C
Storage temperature	$T_{stg}$	-40 to +85	°C
*2 Soldering temperature	$T_{sol}$	240	°C

<sup>\*1</sup> Pulse width 78.1µs, Duty ratio:3/16

# ■ Recommended Operating Conditions

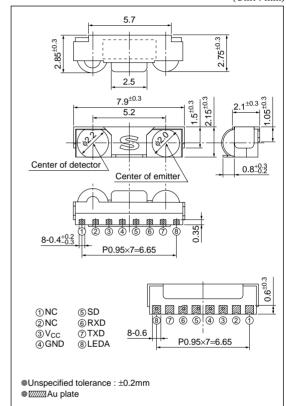
Parameter	Symbol	Rating	Unit
Supply voltage	$V_{CC}$	2.0 to 3.6	V
Transmission rate	BR	2.4 to 115.2	kb/s
High level input voltage (SD terminal)	$V_{IHSD}$	V <sub>CC</sub> ×0.67 to V <sub>CC</sub>	V
Low level input voltage (SD terminal)	$V_{ILSD}$	0 to V <sub>CC</sub> ×0.1	V
*3 High level input voltage (TXD)	$V_{IHTXD}$	V <sub>CC</sub> ×0.8 to V <sub>CC</sub>	V
*3 Low level input voltage (TXD)	$V_{ILTXD}$	0 to V <sub>CC</sub> ×0.2	V
LED Supply Voltage	$V_{LEDA}$	2.0 to 6.0	V

<sup>\*3</sup> Refer to Fig.8

# IrDA Transceiver Module Compliant with IrDA1.2 Low Power

#### ■ Outline Dimensions

(Unit: mm)



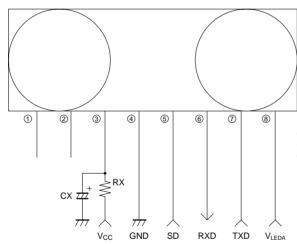
<sup>\*2</sup> For MAX. 10s

#### ■ Electrical Characteristics

	Electrical Characteristics					$(T_a=25^{\circ}C,$	V <sub>CC</sub> =3.3 V)
	Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Receiver side	Dissipation current at no input signal	$I_{CC}$	No input light, output terminal open, V <sub>ILSD</sub> =0V	_	90	120	μΑ
	S/D dissipation current	I <sub>CC-S</sub>	V <sub>CC</sub> =3.3V, V <sub>IHSD</sub> =V <sub>CC</sub> -0.5, No input light output terminal open	-	0.001	0.1	μΑ
	High level output voltage	V <sub>OH</sub>	$I_{OH} = -200 \mu A^{*4}$ $V_{CC} = 0$		-	-	V
	Low level output voltage	V <sub>OL</sub>	I <sub>OL</sub> =200μA*4	İ	ı	0.45	V
	Low level pules width	$t_{\rm w}$	BR=115.2kb/s*4, ¢≤15°	1.28	-	6.0	μs
Rec	Rise time	t <sub>r</sub>	BR=115.2kb/s*4, C <sub>L</sub> =10pF	_	_	0.06	μs
	Fall time	$t_{\rm f}$	BR=115.2kb/s*4, C <sub>L</sub> =10pF	ļ	-	0.06	μs
	Maximum communication distance	L	Voh, Vol., tw, tr, tf *4 shall be satisfied at $\phi$ ≤15°	21	-	_	cm
Transmitter side	Radiant intensity	$I_E$	BR=115.2kb/s, φ≤15°, V <sub>IHTXD</sub> =2.8V *5	4.0	_	25	mW/sr
Trans	Peak emission wavelength	$\lambda_p$	BR=113.2kb/s, ψ≤13 , VIHTXD=2.8 V	850	870	900	nm

<sup>\*4</sup> Refer to Fig.4, 5, 6

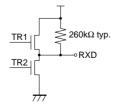
Fig.1 Recommended External Circuit



\*I/O Truth table

SD	TXD	LED	Receiver	TR1	TR2	RXD
	High ON		Don't care	-	-	Not valid
Low	Low	OFF	IrDA signal	OFF	ON	Low
			No signal	ON	OFF	High
High	Don't care	OFF	Don't care	OFF	OFF	Pull-up

<sup>\*</sup>RXD Eruivalent circuit



1	NC
(a)	NIC

- ② NC
- ③ V<sub>CC</sub> ④ GND
- (5) SD
- (5) SD (6) RXD
- ⑦ TXD
- ® LEDA

Components	Recommended values	
CX	1µF/6.3V (Note)	

(Note) Please choose the most suitable CX according to the noise level and noise frequency of power

Depend on noise level and noise frequency of power supply, CX does not work well.

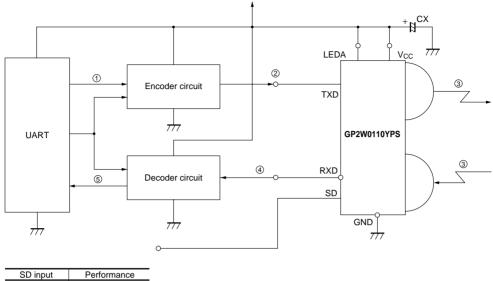
There are cases that some pulse noises from RXD other than signal will occur in certain communication area. Please check by finish product that there are no problem at all communication area and data rate.

If there are any problem, please check by inserting RX (1 to  $10\Omega$ ) in the circuit drawing.

Pin ① and ② are not connected internally.

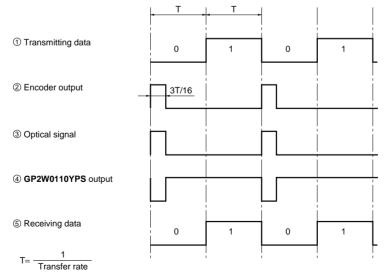
<sup>\*5</sup> Refer to Fig.7, 8, 9

Fig.2 System Configuration



SD input	Performance	
Low	Normal mode	
High	Shut down mode	

Fig.3 Example of Signal Waveform



Transfer rate; 2.4kb/s,9.6kb/s,19.2kb/s,38.4kb/s,57.6kb/s,115.2kb/s

Fig.4 Input Signal Waveforrm (Receiver side)

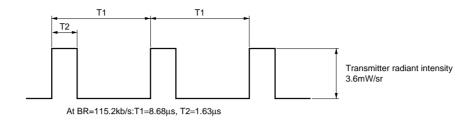


Fig.5 Output Waveform Specification (Receiver side)

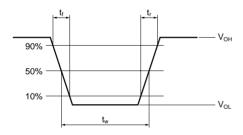
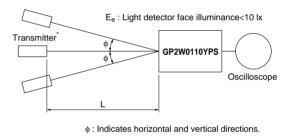


Fig.6 Standard Optical System (Receiver side)



\* Transmitter shall use **GP2W0110YPS** (\(\hat{\p}=870nm\) TYP.) which is adjusted the radiation intensity at 40mW/sr

Fig.7 Output Waveform Specification (Transmitter side)

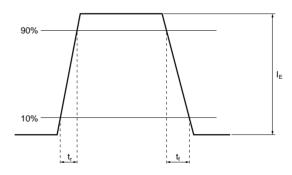


Fig.8 Standard Optical System (Transmitter side)

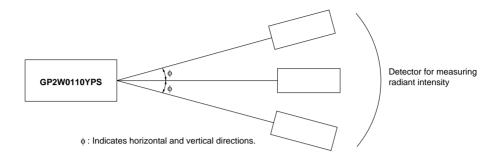
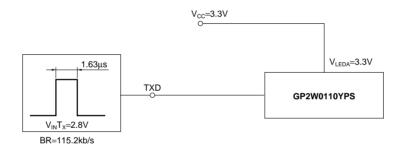
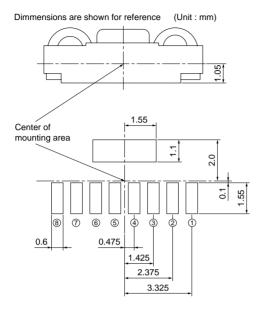


Fig.9 Recommended Circuit of Transmitter side



# Fig.10 Recommended PCB Foot Pattern

Dimensions are shown for reference

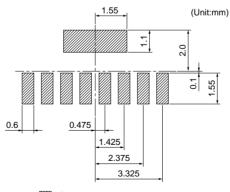


	Terminal	Symbol
1	NC	NC
2	NC	NC
3	V <sub>cc</sub>	V <sub>cc</sub>
4	Ground	GND
⑤	Shutdown	SD
6	Receiver data output	RXD
7	Transmitter data input	TXD
8	LED anode	LEDA

<sup>\*</sup> connect foot pattern of shield case to GND pattern

# Fig.11 Recommended Size of Solder Creamed Paste (Reference)

Please open the solder mask as below so that the size of solder creamed paste for this device before reflow soldering must be as large as one of the foot pattern land indicated Fig.10



Solder paste area

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