#### SEIKO EPSON CORPORATION

## **CRYSTAL OSCILLATOR (SPXO) OUTPUT : CMOS, TTL**

:

## SG - 645 / SG - 636 series

•Supply voltage Function

•Frequency range : 2.21675 MHz to 135 MHz : 2.5 V Typ. / 3.3 V Typ. / 5.0 V Typ. Output enable(OE) or Standby( $\overline{ST}$ ) •External dimensions : 7.1 × 5.1 × 1.5 mm (t: Max.)····SG-645

10.5 × 5.8 × 2.7 mm (t: Max.)…SG-636



### Specifications (characteristics)

			Specifications			
	Symbol	Specifications			Conditions / Remarks	
	Symbol	SG-636 PTF	SG-636 SCE		Conditions / Remarks	
		2.21675 MHz	2.21675 MHz	2.21675 MHz		
Output frequency range	fo	to 41.000 MHz	to 40.000 MHz	to 40.000 MHz	Please contact us about available frequencies	
Supply voltage	Vcc	5.0 V ±0.5 V	3.3 V ±0.3 V	2.5 V ±0.25 V		
Storage temperature	T_stg		-55 °C to +100 °C		Storage as single product.	
Operating temperature	T use		-20 °C to +70 °C			
Frequency tolerance	f tol		C: ±100 × 10 <sup>-6</sup>		-20 °C to +70 °C	
Current consumption	Icc	17 mA Max.	9 mA Max.	5 mA Max.	No load condition	
Disable current	l dis	10 mA Max.	5 mA Max.	3 mA Max.	OE=GND	
Stand-by current	I_std		2 μA Max.	—	ST =GND(SCE)	
Symmetry	SYM	40 % to 60 % 45 % to 55 %		CMOS load:50 % Vcc level		
Symmetry	3110	45 % to 55 %	-	-	TTL load: 1.4 V level	
Output voltogo	Vон		Vcc-0.4 V Min.		loн=-8 mA(PTF) / -4 mA(SCE,PCE) / -3.2 mA(PDE)	
Output voltage	Vol		0.4 V Max.		IoL=16 mA(PTF) / 4 mA(SCE,PCE) / 3.2 mA(PDE)	
Output load condition (TTL)	L_TTL	10 TTL Max.			$L_CMOS \le 15 \text{ pF}$	
Output load condition (CMOS)	L_CMOS	50 pF Max.	30 pF Max.	15 pF Max.		
Input voltage	Vih	2.0 V Min.	80 % Vcc Min. 20 % Vcc Max.		OF Terminal ar Terminal (SCF)	
	VIL	0.8 V Max.			OE Terminal or ST Terminal (SCE)	
Rise time / Fall time	tr / tr	7 ns Max.	5 ns Max.		CMOS load:20 % Vcc to 80 % Vcc level	
use time / i dil time	u / U	5 ns Max.	—		TTL load:0.4 V to 2.4 V level	
Start-up time	t_str	4 ms Max.	4 ms Max.		Time at minimum supply voltage to be 0 s	
Frequency aging	f aging		±5 × 10 <sup>-6</sup> / year Max.		+25 °C, Vcc=5.0 V/3.3 V/2.5 V, First year	

#### Specifications (characteristics)

	Symbol	Specifications				
ltem		SG-636 PTG	SG-636 PHG	SG-636 PCG SG-636 SCG	Conditions / Remarks	
Output frequency range	fo	2	2.21675 MHz to 33.000 MHz	*1	Please contact us about available frequencies.	
Supply voltage	Vcc	4.5 V	to 5.5 V	2.7 V to 3.6 V		
Storage temperature	T_stg		-55 °C to +100 °C		Storage as single product.	
Operating temperature	T_use		-20 °C to +70 °C			
Frequency tolerance	f_tol		B: $\pm 50 \times 10^{-6}$ C: $\pm 100 \times 10^{-6}$	10-6	-20 °C to +70 °C	
Current consumption	lcc	25 m	A Max.	12 mA Max.	No load condition	
Disable curren	l_dis	20 m	A Max.	10 mA Max.	OE=GND (PTG,PHG,PCG)	
Stand-by current	Lstd	— 50 μA Max.			$\overline{ST} = GND (SCG)$	
Symmetry	SYM	— 45 % to 55 %		50 % Vcc level, L_CMOS=25 pF		
Symmetry	STIVI	40 % to 60 % —		1.4 V level, L_CMOS=25 pF		
	Voн	2.4 V Min.	_	Vcc-0.4 V Min.	Iон=-8 mA	
Output voltage	VOH		Vcc-0.4 V Min.		Iон=-16 mA	
Output voltage	Vol			0.4 V Max.	IoL=8 mA	
		0.4 V Max. —		loL=16 mA		
Output load condition	L_CMOS	25 pF Max.				
Input voltage	Vih	2.0 V Min.		70 % Vcc Min.	OE Terminal or ST Terminal	
	Vil	0.8 V Max.		20 % Vcc Max.		
Rise time / Fall time	tr / tf		3.4 ns Max.	4 ns Max.	20 % Vcc to 80 % Vcc level, L_CMOS $\leq$ 25 pF	
hise time / I all time		2.4 ns Max. —		TTL load:0.4 V to 2.4 V level, L_CMOS $\leq$ 25 pF		
Start-up time	t_str		12 ms Max.		t=0 at 90 % Vcc	
Frequency aging	f_aging		$\pm5 imes10^{-6}$ / year Max.		+25 °C, Vcc=5.0 V/ 3.3 V, First year	

\*1 4.1250 MHz < fo < 4.4336 MHz, 8.2500 MHz < fo < 8.8672 MHz, 16.500 MHz < fo < 17.7344 MHz : Unavailable



#### Specifications (characteristics)

		Specifications			
Symbol	SG-636 PTW / STW	SG-636 PHW / SHW	SG-636 PCW / SCW	Conditions / Remarks	
	SG-645 PTW / STW	SG-645 PHW / SHW	SG-645 PCW / SCW		
fo	32	2.001 MHz to 135.000 MH	Ηz	Please contact us about available frequencies.	
Vcc	5.0 V :	±0.5 V	3.3 V ±0.3 V		
T_stg	SG-636***:-55 °C	to +100 °C / SG-645***:	-55 °C to +125 °C	Storage as single product.	
T_use	-20 °C to +70 °C				
	— -40 °C to +85 °C		SG-645PCW / SCW Only		
f_tol	B: -	$50 \times 10^{-6}$ C <sup>2</sup> : ±100 ×	10-6	-20 °C to +70 °C	
	-	_	M: ±100 × 10 <sup>-6</sup>	-40 °C to +85 °C : SG-645PCW / SCW Only	
lcc	45 mA	Max.	28 mA Max.	No load condition( Max. frequency range )	
I_dis	30 mA		16 mA Max.	OE=GND (PTW,PHW,PCW)	
I_std		50 μA Max.		ST =GND (STW,SHW,SCW)	
SVM	— 40 % to 60 %		50 % Vcc level, L_CMOS=Max.		
0110	40 % to 60 %		1.4 V level, L_CMOS=Max.		
Vон	Vcc-0.4 V Min.		IOH=-16 mA(PTW, STW, PHW, SHW)		
			/-8 mA(PCW , SCW)		
Vol	0.4 V Max.		IOL= 16 mA(PTW , STW , PHW , SHW)		
_		/ 8 mA(F		/ 8 mA(PCW , SCW)	
L_TTL	5 TTL Max.	—	—	fo≤ 90 MHz, Max.Supply voltage.	
L_CMOS	15 pF Max.		Max.frequency, Max.Supply voltage.		
Vін	2.0 V Min. 70 % Vcc Min.				
VIL	0.8 V Max. 20 % Vcc Max.		OE Terminal or ST Terminal		
<b>A</b> / <b>A</b>	—	4 ns	Max.	20 % Vcc to 80 % Vcc level, L_CMOS ≤ Max.	
u / U	4 ns Max.	_	—	0.4 V to 2.4 V level	
t_str		10 ms Max.		Time at minimum supply voltage to be 0 s	
f_aging		$\pm 5 \times 10^{-6}$ / year Max.		+25 °C, Vcc=5.0 V / 3.3 V, First year	
	f0           Vcc           T_stg           T_use           f_tol           Icc           I_dis           I_std           SYM           VoH           VoL           L_TTL           L_CMOS           VIH           VIL           tr / tr           t_str	SG-645 PTW / STW           fo         32           Vcc         5.0 V           T_stg         SG-636***:-55 °C           T_use	Symbol         SG-636 PTW / STW SG-645 PTW / STW C / SG-645 ***: T_use           T_stg         SG-636 ***: 55 °C to +100 °C / SG-645 ***: T_use           T_use         -20 °C to +70 °C           f_tol         B: ±50 × 10 °C / SG-645 ***: T_use           I_use         -20 °C to +70 °C           I_tol         B: ±50 × 10 °C / SG-645 ***: T_use           I_cc         45 mA Max.           I_dis         30 mA Max.           I_std	Symbol         SG-636 PTW/STW SG-645 PTW/STW SG-645 PTW/STW SG-645 PTW/SHW SG-645 PCW/SCW SG-645 PCW/SCW SG-	

\*2 SG-636 series "C" tolerance : 40 MHz<fo≤135 MHz

Product Name (Standard form) SG-645 P T W 135.000000MHz B () 23 () 4 (5) () Model @Function (P: Output enable, S:Standby) () Supply voltage @Frequency () Frequency tolerance

3Su	pply voltage	© Frec	1
D	2.5 V Typ.	B	
С	3.3 V Typ.	C	
T,H	5.0 V Typ.	M	•

(Unit:mm)

© Frequency tolerance				
В	±50 × 10 <sup>-6</sup> / -20 to +70°C			
С	±100 × 10 <sup>-6</sup> / -20 to +70°C			
М	±100 × 10 <sup>-6</sup> / -40 to +85°C			

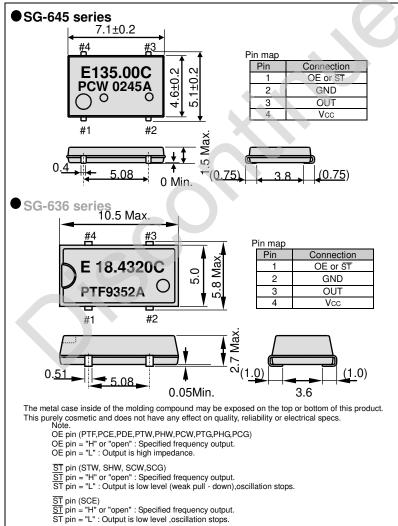
(Unit:mm)

1.8

Footprint (Recommended)

SG-645 series

## External dimensions



• SG-636 series • SG-6

# PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

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Explanation of the mark that are using it for the catalog

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ISO/TS16949 is the international standard that added the sector-specific supplemental requirements for automotive industry based on ISO9001.

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For Automotive	► Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.
Automotive Safety	► Designed for automotive applications related to driving safety (Engine Control Unit, Air Bag, ESC etc ).

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