

# **CBM-120**

**Mosaic Array Series** 

# Far Red Chip On Board LEDs



#### **Features:**

- Mosaic Array Far Red LED chipset with surface emitting area of 12 mm<sup>2</sup>, 4:3 aspect ratio
- Vertical chip LED technology for high power density and uniform emission
- High thermal conductivity copper coreboard package
- Can be operated at variable drive currents up to 18A

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### **Applications:**

- Inspection
- Machine Vision
- Fiber-coupled illumination
- Medical and Scientific Instrumentation



### **Technology Overview**

Luminus LEDs benefit from innovations in device technology, chip packaging and thermal management. This suite of technologies give engineers and system designers the freedom to develop solutions both high in power and efficiency.

#### Luminus Mosaic Array LED Technology

Luminus' Devices vertical chip technology enables LED chips with uniform brightness over the entire chip surface. The optical power and brightness produced by these densely packed arrays of devices enable solutions not possible with single chip packages that be used to replace arc and halogen lamps.

#### **Packaging Technology**

Thermal management is critical in high power LED applications. With a thermal resistance from junction to board of 0.64 °C/W, Luminus CBM-120 LEDs have the lowest thermal resistance of any packaged LED on the market. This will allow the LEDs to be driven at higher current densities while maintaining a low junction temperature, thereby resulting in brighter solutions and longer lifetimes.

#### Reliability

With designs based on years of chip and packaging development experience, Luminus LEDs are one of the most reliable light sources in the world today. Luminus LEDs pass a rigorous suite of environmental and mechanical stress tests, including mechanical shock, vibration, temperature cycling and humidity, and have been fully qualified for use in extreme high power and high current applications.

#### **Environmental Benefits**

Luminus LEDs help reduce power consumption and the amount of hazardous waste entering the environment. All LED products manufactured by Luminus are RoHS and Halogen compliant and free of hazardous materials, including lead and mercury.

### **Understanding Mosaic Array LED Test Specifications**

Every Luminus LED is fully tested to ensure that it meets the high quality standards expected from Luminus' products.

#### **Testing Temperature**

Luminus core board products are typically measured in such a way that the characteristics reported agree with how the devices will actually perform when incorporated into a system.

This method of measurement ensures that Luminus LEDs perform in the field just as they are specified.

#### **Multiple Operating Points**

The tables on the following pages provide typical optical and electrical characteristics for the standard drive conditions. Since the LEDs can be operated over a wide range of drive conditions and duty cycles from <1% to 100% there are many other potential values attainable. Driving devices beyond recommended driving conditions shortens lifetime.



### **CBM-120-FR Binning Structure**

All CBM-120-FR LEDs are tested for radiometric power / peak wavelength and placed into one of the following flux / wavelength bins.

Color	Radiometric Power Bin (FF)	Min Radiometric Power (Watts) @ 9A	Max Radiometric Power (Watts) @ 9A
	RA	4.6	5.8
Far Red (Radiometric Power)	RB	5.8	7.0
	RC	7.0	8.2

Note: Luminus maintains a +/-6% tolerance in Flux measurements.

Color	Peak Wavelength Bin	Min Peak Wavelength @ 9A	Max Peak Wavelength @ 9A
	P720	720	725
For Ded (Deals Wayalangth)	P725	725	730
rar Red (reak wavelength)	P730	730	735
	P735	735	740

Note: Luminus maintains a +/-1nm tolerance in Peak Wavelength measurements.



### **Ordering Information**

Products	Ordering Part Number	Description
CBM-120-FR	CBM-120-FR-C15-FF###	CBM-120 Mosaic Array chipset consisting of 12x1mm2 Far Red LEDs, a thermistor, connectors, and a square copper-core PCB.

### **Part Number Nomenclature**

CBM	— 120 —	СС	 C##	— FF###

Product Family	Chip Area	Color	Package Configuration	Bin Kit <sup>1,2</sup>
CBM: Copper- core PCB, Multi Chip Array, No Encapsulation	120: 12 mm²	FR= Far Red	C15: 28 mm x 26.75 mm - Common Anode Package See Mechanical Drawing section	See page 3 for complete bin definition table

Note 1: A Bin Kit represents a group of individual flux or power bins that are shippable for a given ordering part number. Individual flux bins are not orderable. Note 2: Flux Bin listed is minimum bin shipped - higher bins may be included at Luminus' discretion

### **CBM-120-FR Orderable Bin Kits**

Color	Radiometric Power			Deel. Marcelen eth Dire			
	Min. Flux Bin	Min Power	Max Power	Peak wavelength Bins	KIL NUMBER		
Fer Ded		9.2\\/	P730, P735	RA100			
Far Red	RA 4.6W		ed RA 4.6W 8.2W		8.277	P720, P725, P730, P735	RA200



## Reference Optical & Electrical Characteristics ( $T_{hs} = 40^{\circ}$ C)

FAR RED					
Parameter	Symbol	Values <sup>1</sup>	Unit		
Drive Conditions <sup>2</sup>	I	9	А		
Peak Wavelength Typ.	$\lambda_{p}$	730	nm		
Current Density	j	0.75	A/mm <sup>2</sup>		
	V <sub>F min</sub>	2.0	V		
Forward Voltage	V <sub>F</sub>	2.2	V		
	V <sub>F max</sub>	2.8	V		
Radiometric Flux	$\pmb{\Phi}_{typ}$	6.3	W		
FWHM- Spectral bandwidth at 50% of $\Phi$ v $^3$		32	nm		

Parameter	Symbol	Values	Unit
Absolute Minimum Current (CW or Pulsed) <sup>3</sup>	(CW or Pulsed) <sup>3</sup>		A
Absolute Maximum Current (CW) <sup>4</sup>		18.0	А
Maximum Junction Temperature⁵	T <sub>jmax</sub> 110		°C
Storage Temperature Range		-40 to +100	°C
Emitting Area Dimensions		4.4 × 3.3	mm × mm

Note 1: Unless otherwise noted, values listed are typical. Devices are production tested and specified at 9 A.

Note 2: Listed drive conditions are typical for common applications. CBM120-FR devices can be driven at currents ranging from 200 mA to 18 A and at duty cycles ranging from 1% to 100%. Drive current and duty cycle should be adjusted as necessary to maintain the junction temperature desired to meet application lifetime requirements.

Note 3: Special design considerations must be observed for operation under 1 A. Please contact Luminus for further information.

- Note 4: CBM-120-FR LEDs are designed for operation to an absolute maximum current as specified above. Sustained operation at or beyond absolute maximum currents will result in a reduction of device life time compared to recommended forward drive currents. Actual device lifetimes will also depend on junction temperature. In pulsed operation, rise time from 10-90% of forward current should be longer than 0.5 µseconds.
- Note 5: Lifetime dependent on LED junction temperature. Input power and thermal system must be properly managed to ensure lifetime.



### **Optical & Electrical Characteristics**



#### **Relative Peak Wavelength vs Forward Current**



**Relative Forward Voltage vs Forward Current** 



102% 101% Relative Radiometric Power (%) 100% 99% 98% 97% 96% 95% 94% 20 25 30 35 40 45 50 55 60 65 70 75 90 80 85 Ths (°C)

# Relative Radiometric Power vs Heat Sink Temp.

Relative Peak Wavelength vs Heat Sink Temp.



**Relative Forward Voltage vs Heat Sink Temp.** 



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### **CBM-120-FR Product Datasheet**

### **Thermal Resistance CBM-120**



#### **Typical Thermal Resistance**<sup>6</sup>

R <sub>θj-b</sub> <sup>7</sup>	0.64 °C/W
R <sub>θb-hs</sub> <sup>8</sup>	0.12 °C/W
R <sub>_{_{\theta_j-hs}}^8</sub>	0.76 °C/W
R <sub><math>\theta j</math>-ref<sup>7</sup></sub>	0.67 °C/W

- Note 6: Real thermal resistance data "Electrical" thermal resistance values available upon request
- Note 7: Thermal resistance values are based on engineering modeled data.

Note 8: Thermal Resistance is based on eGraf 1205 Thermal interface.

### **Electrical Pinout**





The thermistor used in CBT-120 devices mounted on coreboards is from Murata Manufacturing Co. The global part number is NCP18XH103J03RB. Please see http://www.murata.com/ for details on calculating thermistor temperature.

For more information on use of the thermistor, please contact Luminus directly.



### Mechanical Dimensions – CBM-120 Mosaic Array LED Emitter



DIMENSION NAME	DESCRIPTION	NOMINAL DIMENSION	TOLERANCE
"A"	TOP OF METAL SUBSTRATE TO TOP OF WINDOW	.93	±.13
"B"	TOP OF EMITTING AREA TO TOP OF WINDOW	.68	±.13
"C"	TOP OF METAL SUBSTRATE TO TOP OF EMITTING AREA	.25	±.03
			CONTRACTOR CONTRACTOR

DWG-002906

Recommended connector for Anode and Cathode: Panduit Disco Lok™ Series P/N: DNF14-250FIB-C or JST Manufacturing Co: SPS-61T-250 for AWG 16 to 14.

Panduit Disco Lok<sup>™</sup> Series P/N: DNF10-250FIB-L or JST Manufacturing Co: SPS-91T-250 for AWG 12 to 10. (Check NEC standards for ampacity of the power cable being used.)

Thermistor Connector: GCT P/N WTB08-021S-F.

Recommended Female: MOLEX P/N 51146-0200 (Not recommended for new designs), GCT P/N WTB06-020H-A or equivalent.



## **Shipping Tray Outline**

DIMENSIONS IN MILLIMETERS

#### 266.70 254 203.71 6.35 'n ט ס' "O O n ſ 1 А 127 139.70 57.15 n ٢ n п 1 41.40 PITCH 51.05 -7° 0.76 TYP TYP 19.81 SECTION A-A



TOP TRAY SHOWN TRANSPARENT FOR REFERENCE ONLY



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## Packing and Shipping Specification (CBM-120)

#### **Packing Specification**

Packing Configuration	Qty /Pack	Reel Dimensions (diameter x W, mm)	Gross Weight (kg)
Stack of 5 trays with 10 devices per tray Each pack is enclosed in ESD bag	50	150 x 280 x 85	2.7

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#### **Product Label Specification**

#### Label Fields (subject to change):

- 6-8 digit Box number (for Luminus internal use)
- Luminus ordering part number
- Quantity of devices in pack
- Part number revision (for Luminus internal use)
- Customer's part number (optional)
- Bin (FF-WW) as defined page 3
- 2D Bar code



### **Shipping Box**

	JS LEDS Solid Stat	e Filament™	
BT-012345	Qty: 50   <b>      </b>		nd attach
PT-120-G-L11-MPG	Rev 01		off label a
12345678			eability peel
5F			for trace
	RoHS C	omplian	t

Sample label – for illustration only

Shipping Box	Quantity	Material	Dimensions (L x W x H, mm)	
Carton Box	1 -20 packs (50 - 1000 Devices)	S4651	560 x 560 x 200	





### **History of Changes**

Rev		Description of Change		
А	08/03/2015	Initial Release - Preliminary Specifications for 740nm Parts		
В	01/29/2016	Updated Mechanical Drawing, Typical Parametric Values, Packaging Specifications, Ordering Information		
01	03/25/2016	Updated Thermal Resistance Drawing, Updated Binning		
02	09/09/2016	Updated Mechanical Drawing and Corrected Typos		
03	02/26/2020	Updated: - Rth junction to board value on page 2 - Binning Structures on page 3 (added P720 & P725) - Typical Performances on page 5 - Optical & Electrical Characteristics on page 6 - Mechanical Drawing on page 9 including connector information		

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This product is protected by U.S. Patents 6,831,302; 7,074,631; 7,083,993; 7,084,434; 7,098,589; 7,105,861; 7,138,666; 7,166,870; 7,166,871; 7,170,100; 7,196,354; 7,211,831; 7,262,550; 7,274,043; 7,301,271; 7,341,880; 7,344,903; 7,345,416; 7,348,603; 7,388,233; 7,391,059 Patents Pending in the U.S. and other countries.