

18-Line SCSI Terminator

FEATURES

- Complies with SCSI, SCSI-2, SCSI-3, SPI and FAST-20 Standards
- 2pF Channel Capacitance During Disconnect
- 50μA Supply Current in Disconnect Mode
- 110Ω Termination
- SCSI Hot Plugging Compliant, 10nA Typical
- +400mA Sinking Current for Active Negation
- –650mA Sourcing Current for Termination
- Trimmed Impedance to 5%
- Thermal Shutdown
- Current Limit

DESCRIPTION

The UCC5618 provides 18 lines of active termination for a SCSI (Small Computers Systems Interface) parallel bus. The SCSI standard recommends and Fast-20 (Ultra) requires active termination at both ends of the cable.

Pin for pin compatible with the UC5601 and UC5608, the UCC5618 is ideal for high performance 5V SCSI systems, Termpwr 4.0-5.25V. During disconnect the supply current is only $50\mu A$ typical, which makes the IC attractive for lower powered systems.

The UCC5618 is designed with a low channel capacitance of 2pF, which eliminates effects on signal integrity from disconnected terminators at interim points on the bus.

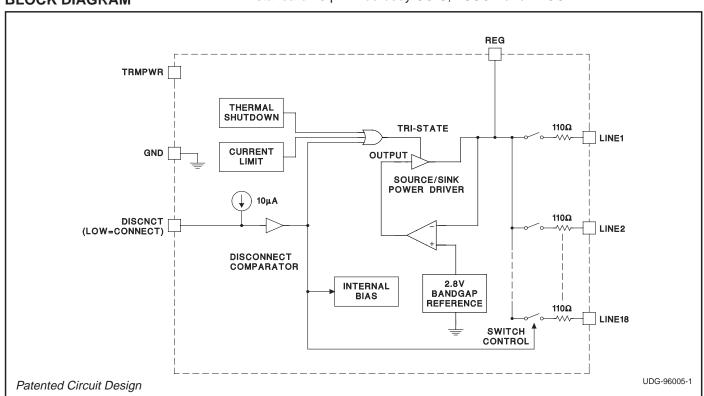
The power amplifier output stage allows the UCC5618 to source full termination current and sink active negation current when all termination lines are actively negated.

The UCC5618, as with all Unitrode terminators, is completely hot pluggable and appears as high impedance at the terminating channels with TRMPWR=0V or open.

Internal circuit trimming is utilized, first to trim the 110Ω impedance, and then most importantly, to trim the output current as close to the max SCSI-3 spec as possible, which maximizes noise margin in fast SCSI operation.

This device is offered in low thermal resistance versions of the industry standard 28 pin wide body SOIC, TSSOP and PLCC.

BLOCK DIAGRAM

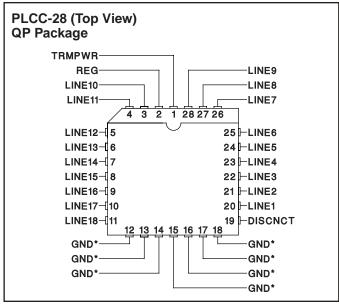


ABSOLUTE MAXIMUM RATINGS

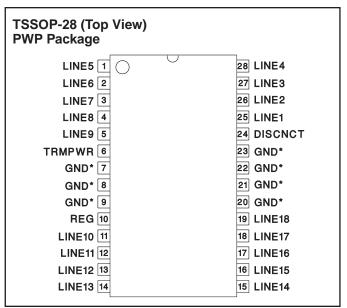
TEMPWR+	-7V
Signal Line Voltage	-7V
Regulator Output Current	1A
Storage Temperature65°C to +150)°C
Operating Junction Temperature –55°C to +150)°C
Lead Temperature (Soldering, 10 Seconds) 300)°C

All currents are positive into, negative out of the specified terminal. Consult Packaging Section of Databook for thermal limitations and considerations of packages.

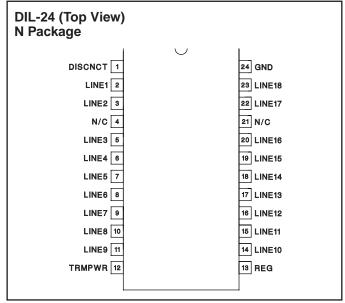
CONNECTION DIAGRAMS



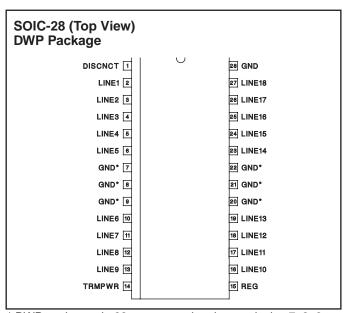
* DWP package pins 12–18 serve as both heatsink and signal ground.



* PWP package pin 23 serves as signal ground; pins 7, 8, 9, 20, 21, and 22 serve as heatsink ground.



Note: Drawings are not to scale.



* DWP package pin 28 serves as signal ground; pins 7, 8, 9, 20, 21, 22 serve as heatsink/ground.

ELECTRICAL CHARACTERISTICS: Unless otherwise stated these specifications apply for $T_A = 0$ °C to 70°C,

TRMPWR	= 4.75 V	DISCNCT	= 0V	$T_{\Lambda} = T$	

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNITS
Supply Current Section		•			
TERMPWR Supply Current	All Termination Lines = Open		1	2	mA
	All Termination Lines = 0.2V		420	440	mA
Power Down Mode	DISCNCT = TRMPWR		50	100	μΑ
Output Section (Termination Lines)					
Termination Impedance	See Figure 1	104.5	110	115.5	Ω
Output High Voltage	V _{TRMPWR} = 4V (Note 1)	2.6	2.8	3	V
Max Output Current	$V_{LINE} = 0.2V, T_{J} = 25^{\circ}C$	-22.1	-23.3	-24	mA
	$V_{LINE} = 0.2V$	-20.7	-23.3	-24	mA
	$V_{LINE} = 0.2V$, TERMPWR = 4V, $T_{J} = 25$ °C (Note 1)	-21	-23.3	-24	mA
	V _{LINE} = 0.2V, TRMPWR = 4V (Note 1)	-20	-23	-24	mA
	$V_{LINE} = 0.5V$			-22.4	mA
Output Leakage	DISCNCT = 2.4V, TRMPWR = 0V to 5.25V, REG = 0.2V, V_{LINE} = 5.25V		10	400	nA
Output Capacitance	DISCNCT = 2.4V (Note 2)		2	3.5	pF
Regulator Section					
Regulator Output Voltage		2.6	2.8	3	V
Drop Out Voltage	All Termination Lines = 0.2V		0.4	0.8	V
Short Circuit Current	V _{REG} = 0V	-475	-650	-950	mA
Sinking Current Capability	$V_{REG} = 3.5V$	200	400	800	mA
Thermal Shutdown			170		°C
Thermal Shutdown Hysteresis			10		°C
Disconnect Section		-			
Disconnect Threshold		0.8	1.5	2	V
Input Current	DISCNCT = 0V		-10	-30	μΑ

Note 1: Measuring each termination line while other 17 are low (0.2V).

Note 2: Guaranteed by design. Not 100% tested in production.

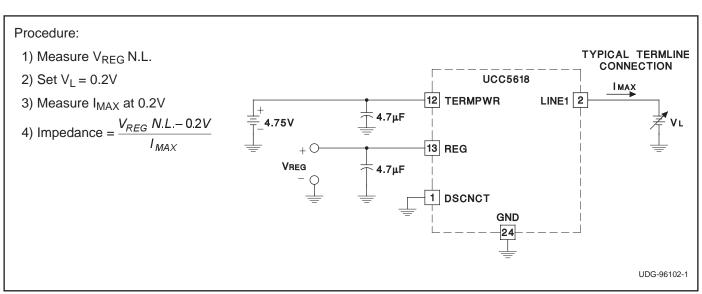


Figure 1. Termline Impedance Measurement Circuit

PIN DESCRIPTIONS

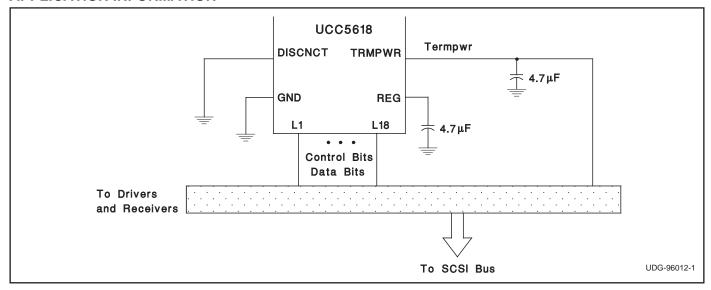
DISCNCT: Taking this pin high or leaving it open causes the 18 channels to become high impedance and the chip to go into low-power mode; a low state allows the channels to provide normal termination.

GND: Ground reference for the IC.

LINE1–LINE18: 110Ω termination channels. **REG:** Output of the internal 2.8V regulator.

TRMPWR: Power for the IC.

APPLICATION INFORMATION



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UCC5618, Lowest Capacitance 18-Line 5V SE Term for SCSI Through Ultra SCSI with Inv Sensing & Rev Disconnect Device Status: Active

- > Description
- > Features
- > Datasheets
- > Pricing/Samples/Availability
- > Application Notes
- > Applications

Parameter Name	UCC5618
Number of Lines	18
Driver Types Supported	SE
TERMPWR Voltage (max) (V)	5.25
TERMPWR Voltage (min) (V)	4.0
Disconnect Active State	High
Integrated SPI-3 Mode Switching Filter/Delay	No
Process	Bi-CMOS
Active Negation Support	Yes
Channel Capacitance (pF)	2.5
Resistor Tolerance (ppm)	500
Typical Sink Current (mA)	200
Current Tolerance (%)	3
Single-Ended Termination Impedance (ohms)	110
Single-Ended Tolerance (%)	5
Integrated TERMPWR Regulation	No

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Unitrode Products from Texas Instruments

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To view the following documents, Acrobat Reader 3.x is required.

To download a document to your hard drive, right-click on the link and choose 'Save'.

Datasheets

Full datasheet in Acrobat PDF: slus361.pdf (235 KB)

Pricing/Samples/Availability

Orderable Device	Package	Pins	Temp (°C)	<u>Status</u>	Price/unit USD (100-999)	Pack Qty	Availability / Samples
UCC5618DWP	<u>DW</u>	28	0 TO 70	ACTIVE	2.40	25	Check stock or order
UCC5618DWPTR	<u>DW</u>	28	0 TO 70	ACTIVE	2.20	1000	Check stock or order
UCC5618PWP	<u>PWP</u>	28	0 TO 70	ACTIVE	2.99	50	Check stock or order
UCC5618PWPTR	<u>PWP</u>	28	0 TO 70	ACTIVE	2.70	2000	Check stock or order
UCC5618QP	<u>FN</u>	28	0 TO 70	ACTIVE	5.24	37	Check stock or order
UCC5618QPTR	<u>FN</u>	28	0 TO 70	ACTIVE	4.63	750	Check stock or order

Application Reports

- Comparing Bus Solutions (SLLA067 Updated: 03/02/2000)
- Jitter Analysis (SLLA075 Updated: 03/30/2000)

Table Data Updated on: 8/30/2000

