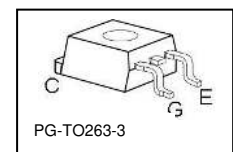
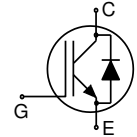


Low Loss DuoPack : IGBT in TRENCHSTOP™ and Fieldstop technology with soft, fast recovery anti-parallel Emitter Controlled HE diode



Features:

- Very low $V_{CE(sat)}$ 1.5V (typ.)
- Maximum Junction Temperature 175°C
- Short circuit withstand time 5 μ s
- Designed for frequency inverters for washing machines, fans, pumps and vacuum cleaners
- TRENCHSTOP™ technology for 600V applications offers :
 - very tight parameter distribution
 - high ruggedness, temperature stable behavior
 - very high switching speed
- Positive temperature coefficient in $V_{CE(sat)}$
- Low EMI
- Low Gate Charge
- Qualified according to JEDEC¹ for target applications
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models : <http://www.infineon.com/igbt/>



Type	V_{CE}	I_C	$V_{CE(sat), T_j=25^\circ C}$	$T_{j,max}$	Marking Code	Package
IKB15N60T	600V	15A	1.5V	175°C	K15T60	PG-TO263-3

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage, $T_j \geq 25^\circ C$	V_{CE}	600	V
DC collector current, limited by $T_{j,max}$	I_C	26	A
$T_C = 25^\circ C$, value limited by bondwire			
$T_C = 100^\circ C$			
Pulsed collector current, t_p limited by $T_{j,max}$	$I_{C,puls}$	45	
Turn off safe operating area, $V_{CE} = 600V$, $T_j = 175^\circ C$, $t_p = 1\mu s$	-	45	
Diode forward current, limited by $T_{j,max}$	I_F	26	A
$T_C = 25^\circ C$, value limited by bondwire			
$T_C = 100^\circ C$			
Diode pulsed current, t_p limited by $T_{j,max}$	$I_{F,puls}$	45	
Gate-emitter voltage	V_{GE}	± 20	V
Short circuit withstand time ²⁾	t_{SC}	5	μs
$V_{GE} = 15V$, $V_{CC} \leq 400V$, $T_j \leq 150^\circ C$			
Power dissipation $T_C = 25^\circ C$	P_{tot}	130	W
Operating junction temperature	T_j	-40...+175	°C
Storage temperature	T_{stg}	-55...+150	
Soldering temperature (reflow soldering, MSL1)	-	260	

¹ J-STD-020 and JESD-022

²⁾ Allowed number of short circuits: <1000; time between short circuits: >1s.

Thermal Resistance

Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic				
IGBT thermal resistance, junction – case	R_{thJC}		1.15	K/W
Diode thermal resistance, junction – case	R_{thJCD}		1.9	
Thermal resistance, junction – ambient	R_{thJA}	6cm ² Cu	40	

Electrical Characteristic, at $T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	Typ.	max.	
Static Characteristic						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE}=0\text{V}, I_C=0.2\text{mA}$	600	-	-	V
Collector-emitter saturation voltage	$V_{CE(sat)}$	$V_{GE} = 15\text{V}, I_C=15\text{A}$ $T_j=25^\circ\text{C}$ $T_j=175^\circ\text{C}$	- -	1.5 1.9	2.05 -	
Diode forward voltage	V_F	$V_{GE}=0\text{V}, I_F=15\text{A}$ $T_j=25^\circ\text{C}$ $T_j=175^\circ\text{C}$	- -	1.65 1.6	2.05 -	
Gate-emitter threshold voltage	$V_{GE(th)}$	$I_C=210\mu\text{A}, V_{CE}=V_{GE}$	4.1	4.9	5.7	
Zero gate voltage collector current	I_{CES}	$V_{CE}=600\text{V}, V_{GE}=0\text{V}$ $T_j=25^\circ\text{C}$ $T_j=175^\circ\text{C}$	- -	- -	40 1000	μA
Gate-emitter leakage current	I_{GES}	$V_{CE}=0\text{V}, V_{GE}=20\text{V}$	-	-	100	
Transconductance	g_{fs}	$V_{CE}=20\text{V}, I_C=15\text{A}$	-	8.7	-	S
Integrated gate resistor	R_{Gint}			-		Ω

Dynamic Characteristic

Input capacitance	C_{iss}	$V_{CE}=25\text{V}, V_{GE}=0\text{V}, f=1\text{MHz}$	-	860	-	pF
Output capacitance	C_{oss}		-	55	-	
Reverse transfer capacitance	C_{riss}		-	24	-	
Gate charge	Q_{Gate}	$V_{CC}=480\text{V}, I_C=15\text{A}, V_{GE}=15\text{V}$	-	87	-	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	L_E		-	7	-	nH
Short circuit collector current ¹⁾	$I_{C(SC)}$	$V_{GE}=15\text{V}, t_{SC}\leq 5\mu\text{s}$ $V_{CC} = 400\text{V}, T_j \leq 150^\circ\text{C}$	-	137.5	-	A

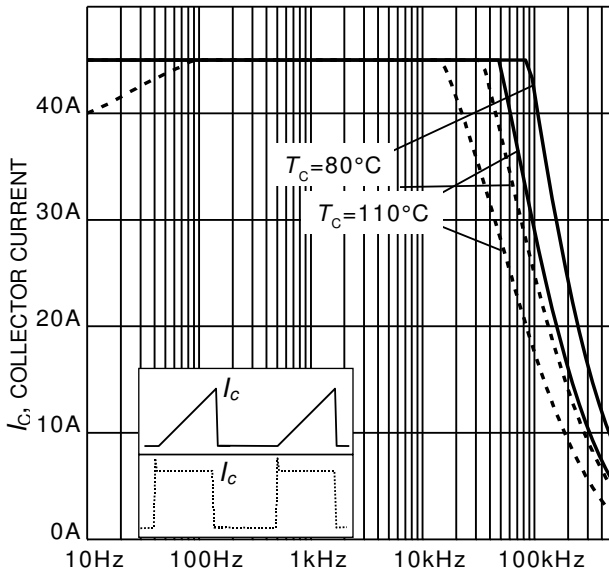
¹⁾ Allowed number of short circuits: <1000; time between short circuits: >1s.

Switching Characteristic, Inductive Load, at $T_j=25^\circ\text{C}$

Parameter	Symbol	Conditions	Value			Unit	
			min.	Typ.	max.		
IGBT Characteristic							
Turn-on delay time	$t_{d(on)}$	$T_j=25^\circ\text{C}$, $V_{CC}=400\text{V}$, $I_C=15\text{A}$, $V_{GE}=0/15\text{V}$, $r_G=15\Omega$, $L_\sigma=154\text{nH}$, $C_\sigma=39\text{pF}$	-	17	-	ns	
Rise time	t_r		-	11	-		
Turn-off delay time	$t_{d(off)}$		-	188	-		
Fall time	t_f		-	50	-		
Turn-on energy	E_{on}	L_σ , C_σ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	0.22	-	mJ	
Turn-off energy	E_{off}		-	0.35	-		
Total switching energy	E_{ts}		-	0.57	-		
Anti-Parallel Diode Characteristic							
Diode reverse recovery time	t_{rr}	$T_j=25^\circ\text{C}$, $V_R=400\text{V}$, $I_F=15\text{A}$, $di_F/dt=825\text{A}/\mu\text{s}$	-	34	-	ns	
Diode reverse recovery charge	Q_{rr}		-	0.24	-		μC
Diode peak reverse recovery current	I_{rrm}		-	10.4	-		
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	718	-		$\text{A}/\mu\text{s}$

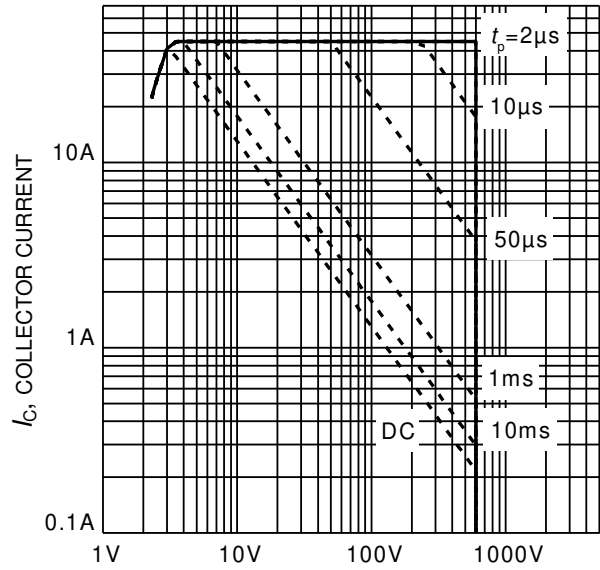
Switching Characteristic, Inductive Load, at $T_j=175^\circ\text{C}$

Parameter	Symbol	Conditions	Value			Unit	
			min.	Typ.	max.		
IGBT Characteristic							
Turn-on delay time	$t_{d(on)}$	$T_j=175^\circ\text{C}$, $V_{CC}=400\text{V}$, $I_C=15\text{A}$, $V_{GE}=0/15\text{V}$, $r_G=15\Omega$, $L_\sigma=154\text{nH}$, $C_\sigma=39\text{pF}$	-	17	-	ns	
Rise time	t_r		-	15	-		
Turn-off delay time	$t_{d(off)}$		-	212	-		
Fall time	t_f		-	79	-		
Turn-on energy	E_{on}	L_σ , C_σ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	0.34	-	mJ	
Turn-off energy	E_{off}		-	0.47	-		
Total switching energy	E_{ts}		-	0.81	-		
Anti-Parallel Diode Characteristic							
Diode reverse recovery time	t_{rr}	$T_j=175^\circ\text{C}$, $V_R=400\text{V}$, $I_F=15\text{A}$, $di_F/dt=825\text{A}/\mu\text{s}$	-	140	-	ns	
Diode reverse recovery charge	Q_{rr}		-	1.0	-		μC
Diode peak reverse recovery current	I_{rrm}		-	14.7	-		
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	495	-		$\text{A}/\mu\text{s}$



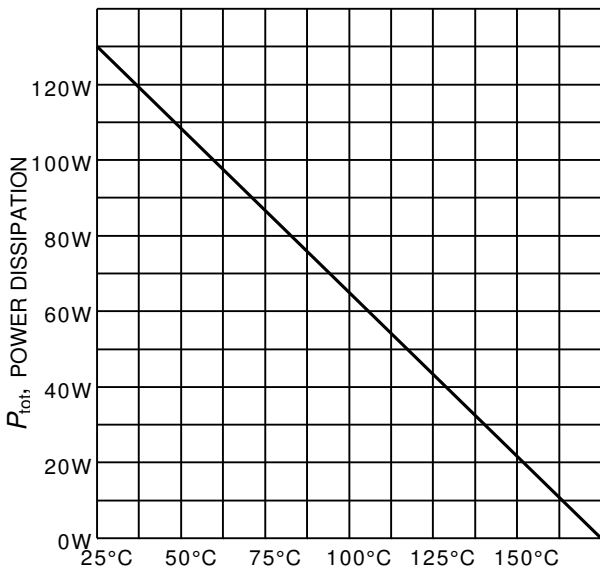
f , SWITCHING FREQUENCY

Figure 1. Collector current as a function of switching frequency
 ($T_j \leq 175^\circ\text{C}$, $D = 0.5$, $V_{CE} = 400\text{V}$,
 $V_{GE} = 0/15\text{V}$, $r_G = 15\Omega$)



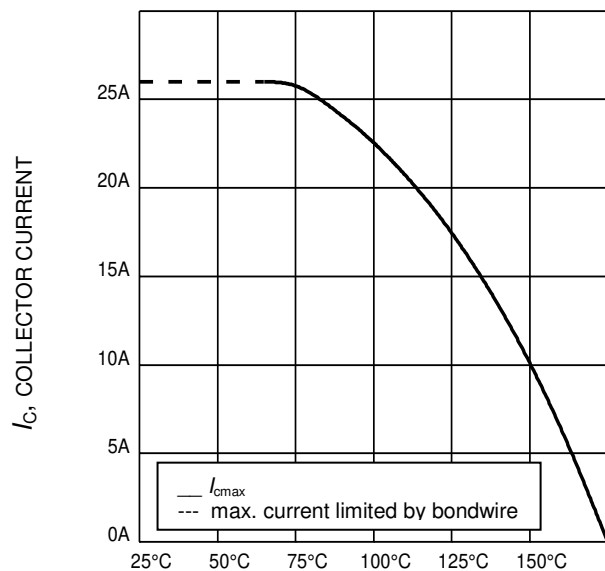
V_{CE} , COLLECTOR-EMITTER VOLTAGE

Figure 2. Safe operating area
 ($D = 0$, $T_C = 25^\circ\text{C}$, $T_j \leq 175^\circ\text{C}$;
 $V_{GE} = 0/15\text{V}$)



T_C , CASE TEMPERATURE

Figure 3. Power dissipation as a function of case temperature
 ($T_j \leq 175^\circ\text{C}$)



T_C , CASE TEMPERATURE

Figure 4. Collector current as a function of case temperature
 ($V_{GE} \geq 15\text{V}$, $T_j \leq 175^\circ\text{C}$)

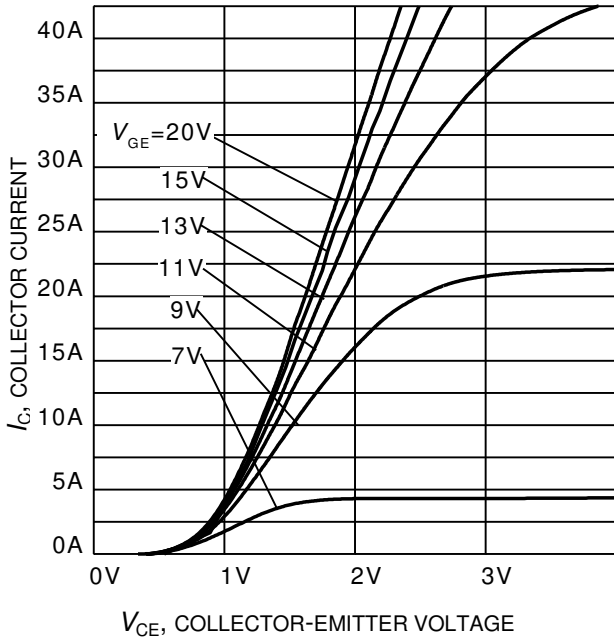


Figure 5. Typical output characteristic
($T_j = 25^\circ\text{C}$)

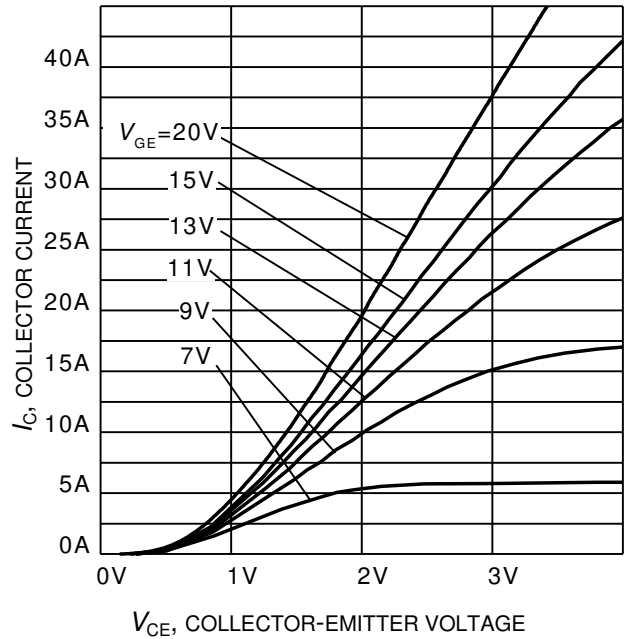


Figure 6. Typical output characteristic
($T_j = 175^\circ\text{C}$)

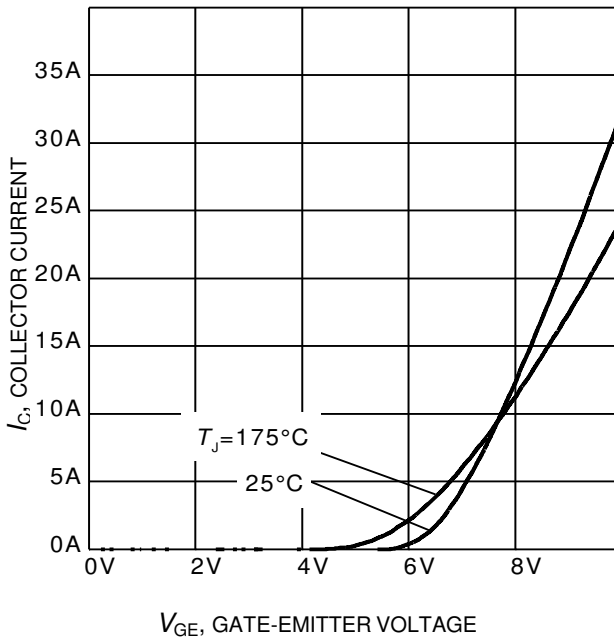


Figure 7. Typical transfer characteristic
($V_{CE} = 20\text{V}$)

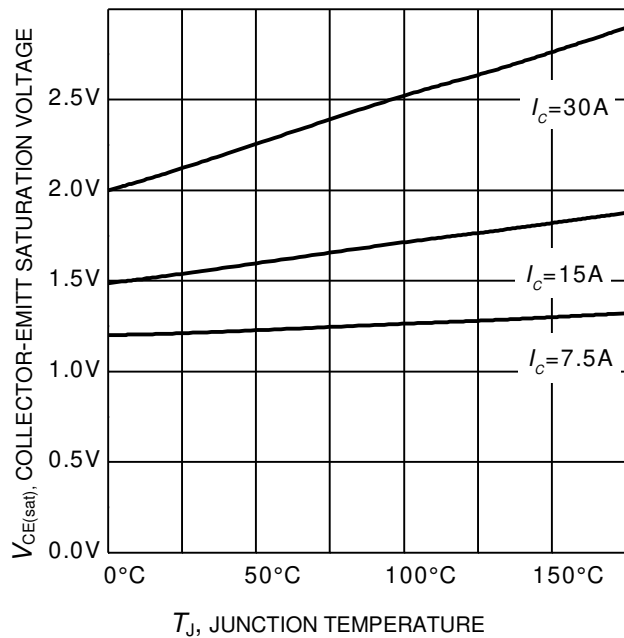
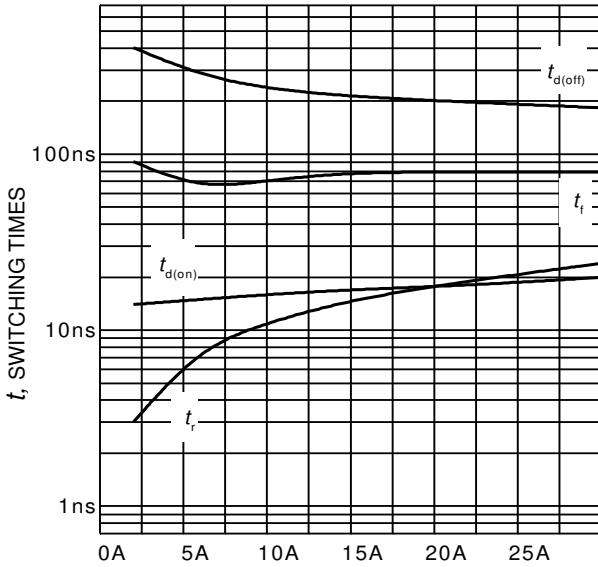
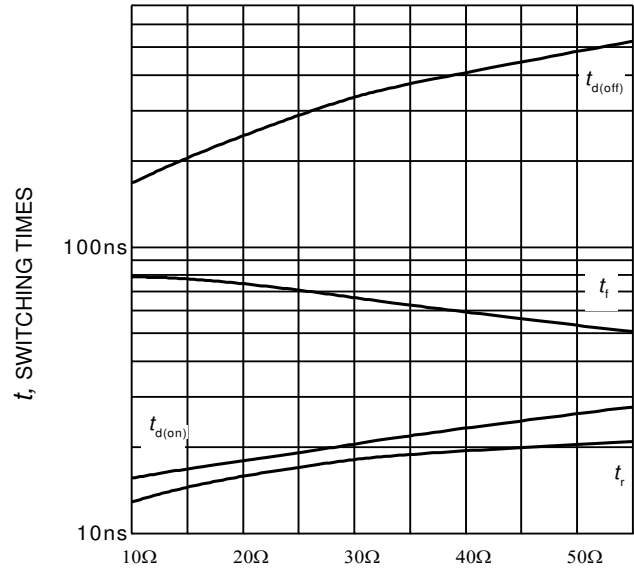


Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature
($V_{GE} = 15\text{V}$)



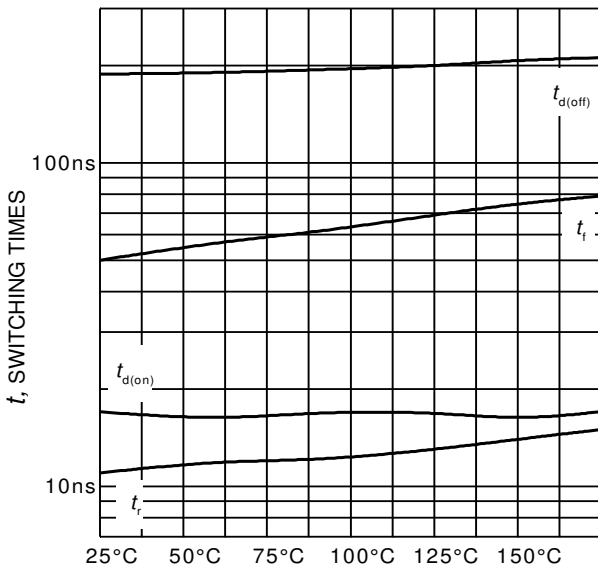
I_C , COLLECTOR CURRENT

Figure 9. Typical switching times as a function of collector current
(inductive load, $T_J=175^\circ\text{C}$,
 $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $r_G = 15\Omega$,
Dynamic test circuit in Figure E)



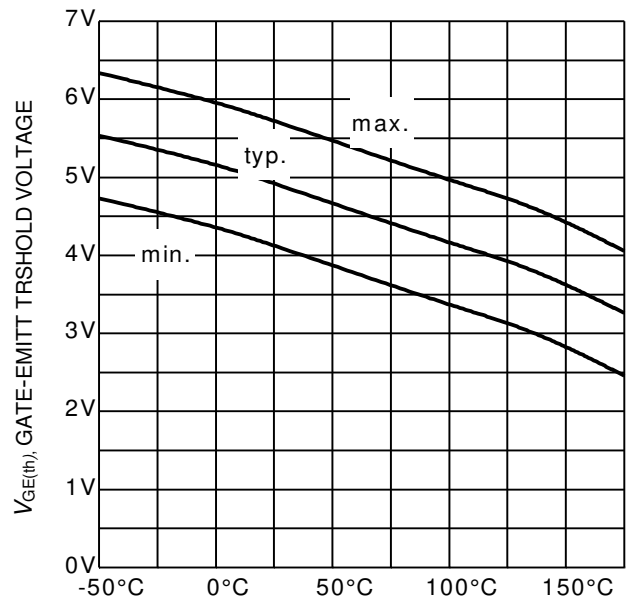
R_G , GATE RESISTOR

Figure 10. Typical switching times as a function of gate resistor
(inductive load, $T_J = 175^\circ\text{C}$,
 $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $I_C = 15\text{A}$,
Dynamic test circuit in Figure E)



T_J , JUNCTION TEMPERATURE

Figure 11. Typical switching times as a function of junction temperature
(inductive load, $V_{CE} = 400\text{V}$,
 $V_{GE} = 0/15\text{V}$, $I_C = 15\text{A}$, $r_G=15\Omega$,
Dynamic test circuit in Figure E)



T_J , JUNCTION TEMPERATURE

Figure 12. Gate-emitter threshold voltage as a function of junction temperature
($I_C = 0.21\text{mA}$)

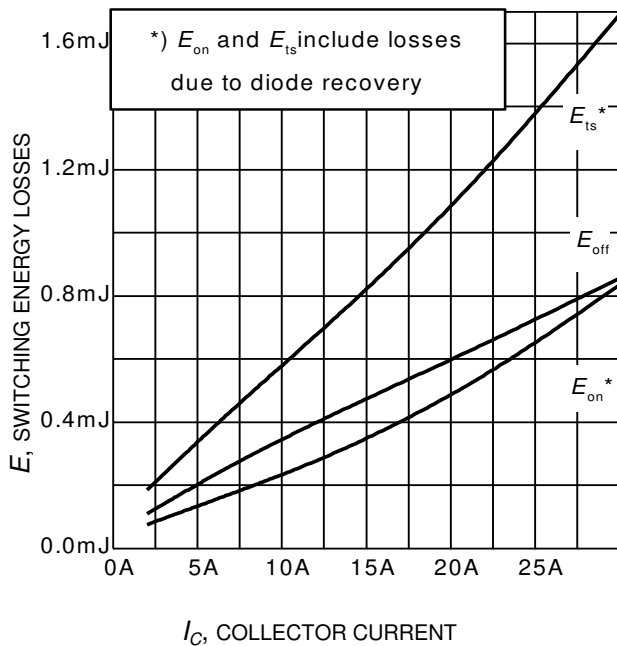


Figure 13. Typical switching energy losses as a function of collector current
 (inductive load, $T_J = 175^\circ\text{C}$, $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $r_G = 15\Omega$, Dynamic test circuit in Figure E)

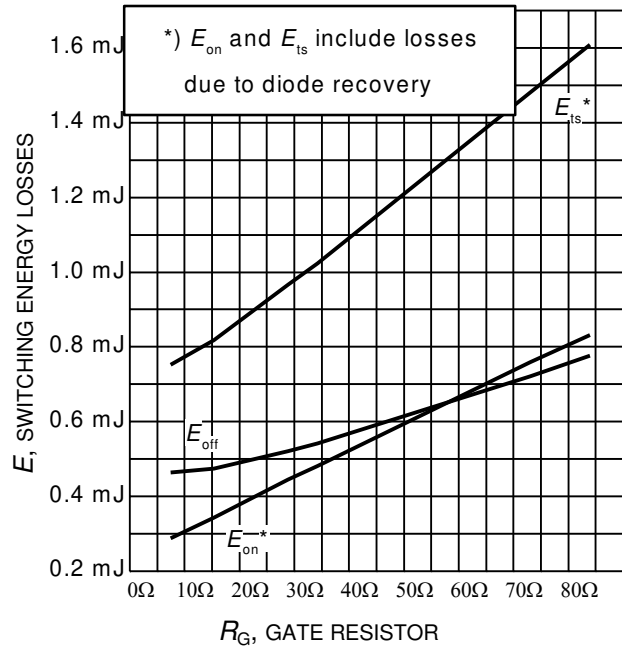


Figure 14. Typical switching energy losses as a function of gate resistor
 (inductive load, $T_J = 175^\circ\text{C}$, $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $I_C = 15\text{A}$, Dynamic test circuit in Figure E)

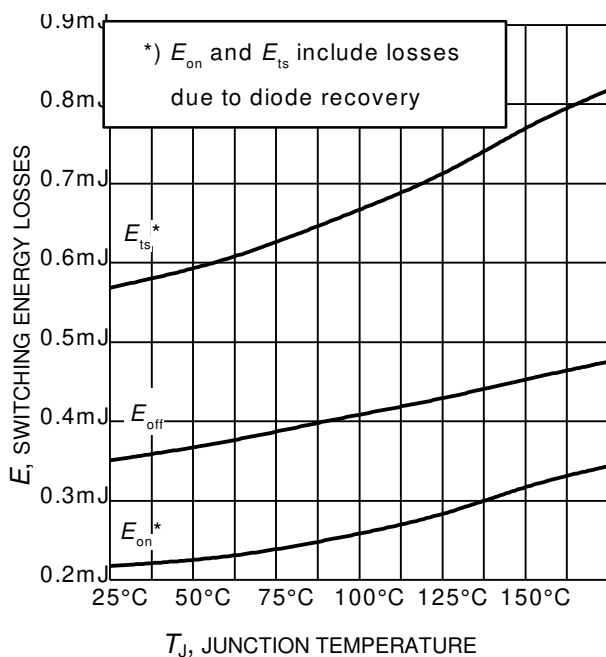


Figure 15. Typical switching energy losses as a function of junction temperature
 (inductive load, $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $I_C = 15\text{A}$, $r_G = 15\Omega$, Dynamic test circuit in Figure E)

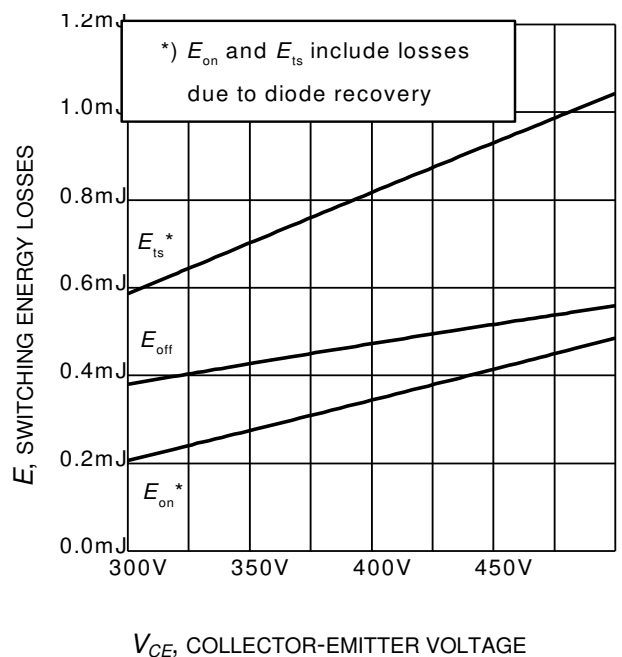


Figure 16. Typical switching energy losses as a function of collector emitter voltage
 (inductive load, $T_J = 175^\circ\text{C}$, $V_{GE} = 0/15\text{V}$, $I_C = 15\text{A}$, $r_G = 15\Omega$, Dynamic test circuit in Figure E)

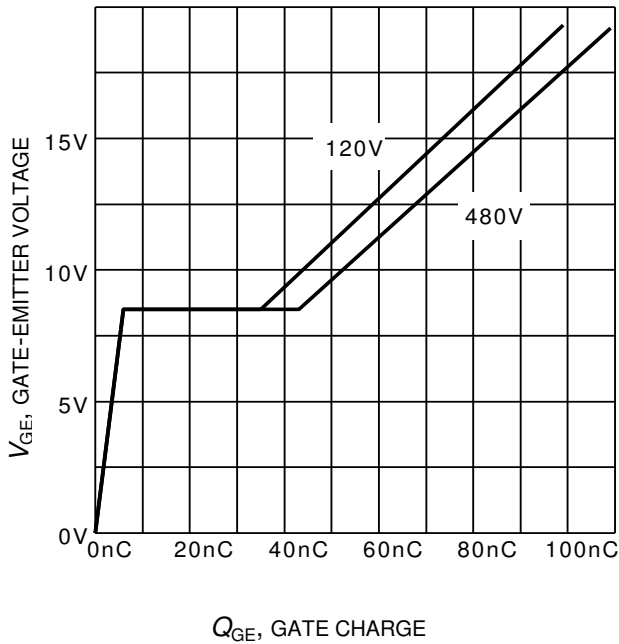


Figure 17. Typical gate charge
($I_C=15\text{ A}$)

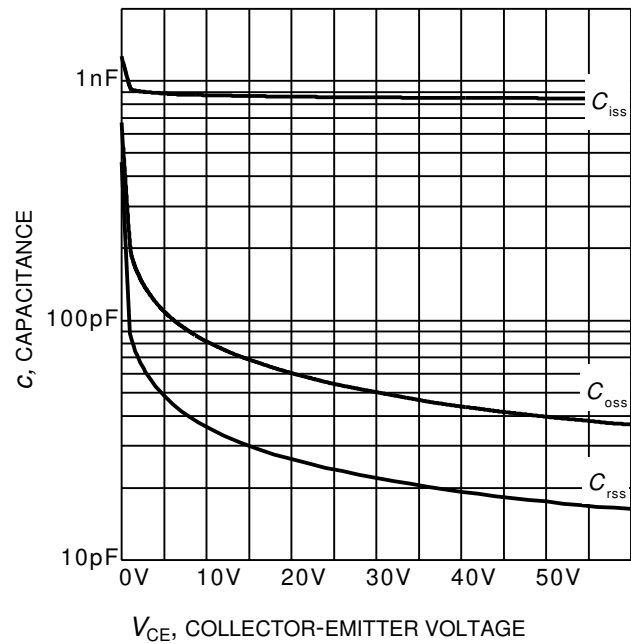


Figure 18. Typical capacitance as a function of collector-emitter voltage
($V_{GE}=0\text{V}$, $f = 1\text{ MHz}$)

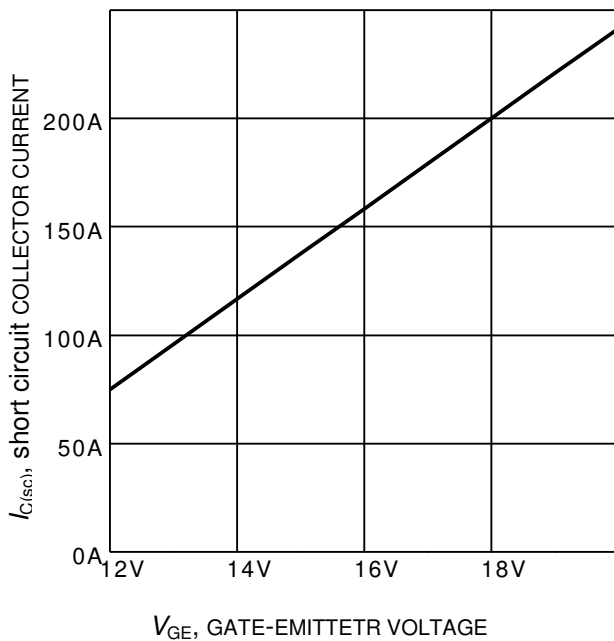


Figure 19. Typical short circuit collector current as a function of gate-emitter voltage
($V_{CE} \leq 400\text{V}$, $T_J \leq 150^\circ\text{C}$)

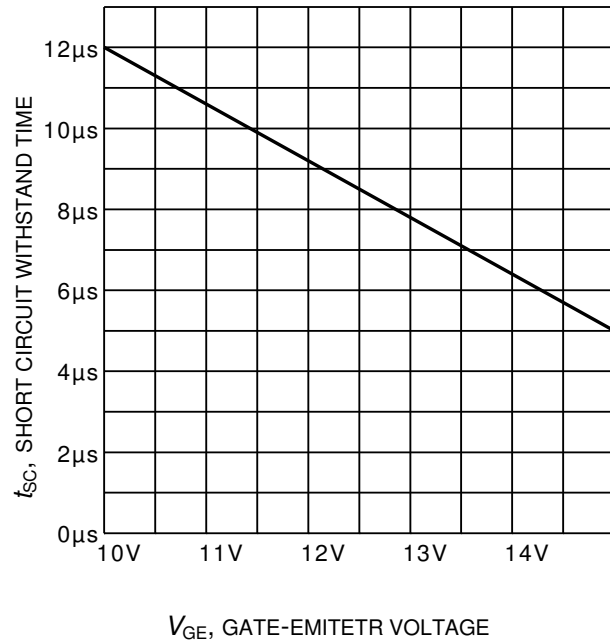


Figure 20. Short circuit withstand time as a function of gate-emitter voltage
($V_{CE}=400\text{V}$, start at $T_J=25^\circ\text{C}$, $T_{Jmax}<150^\circ\text{C}$)

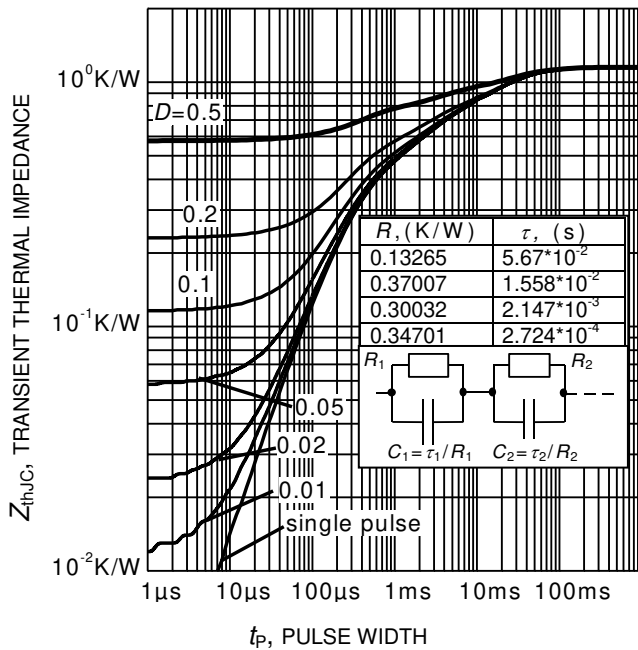


Figure 21. IGBT transient thermal impedance
($D = t_p / T$)

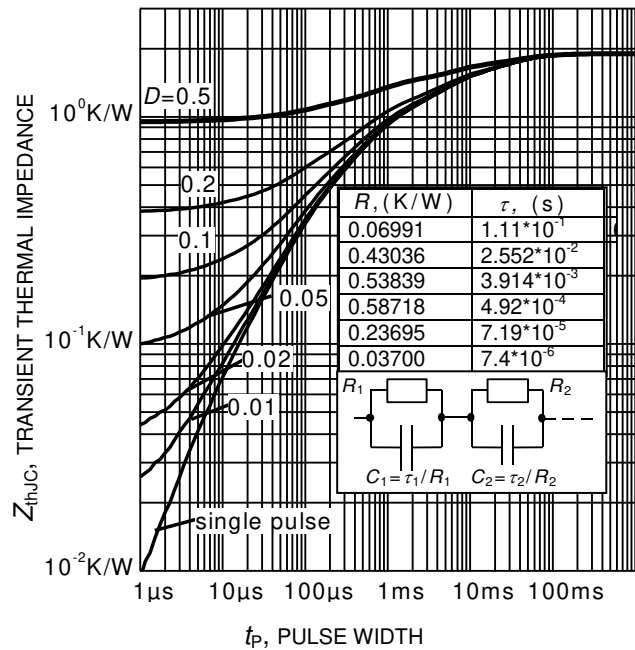


Figure 22. Diode transient thermal impedance as a function of pulse width
($D = t_p / T$)

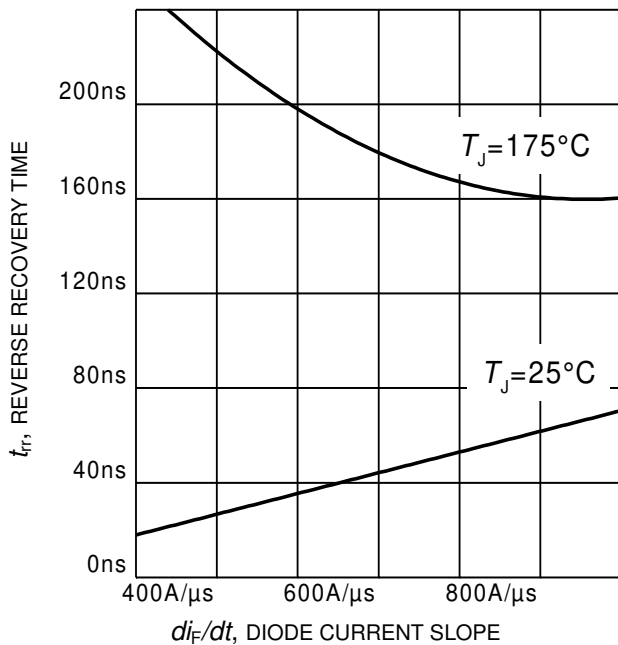


Figure 23. Typical reverse recovery time as a function of diode current slope
($V_R = 400V$, $I_F = 15A$,
Dynamic test circuit in Figure E)

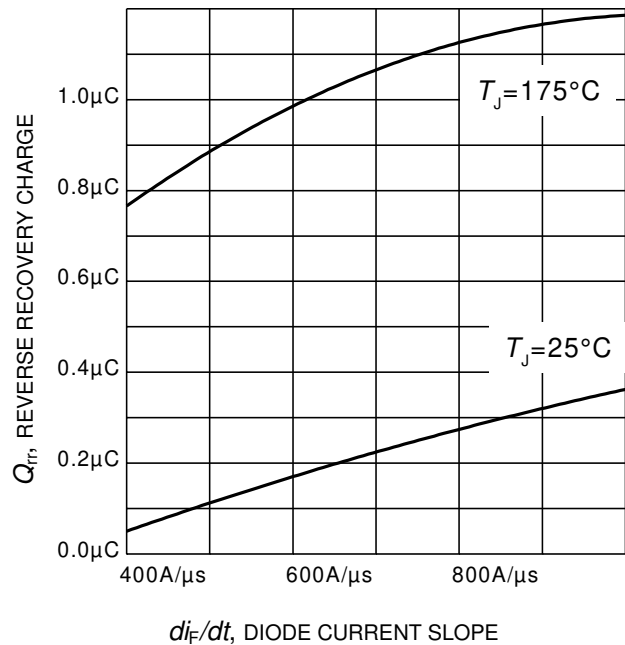
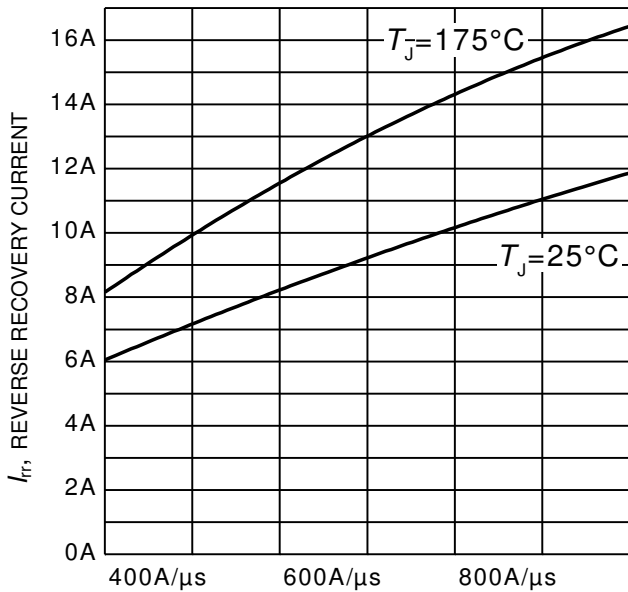


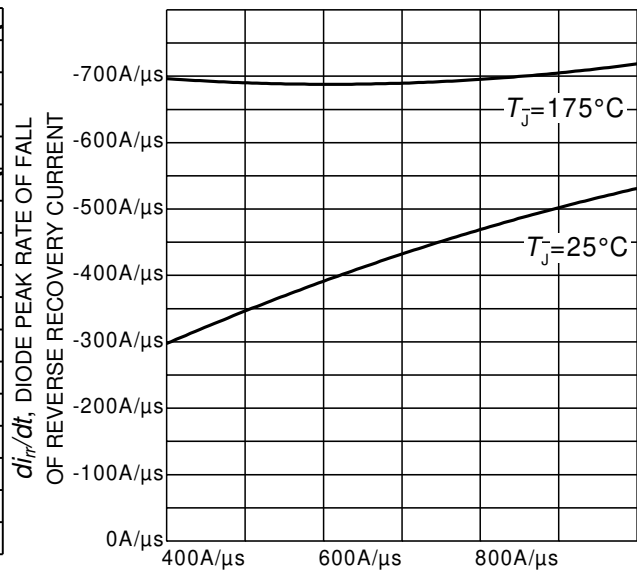
Figure 24. Typical reverse recovery charge as a function of diode current slope
($V_R = 400V$, $I_F = 15A$,
Dynamic test circuit in Figure E)



di_F/dt , DIODE CURRENT SLOPE

Figure 25. Typical reverse recovery current as a function of diode current slope

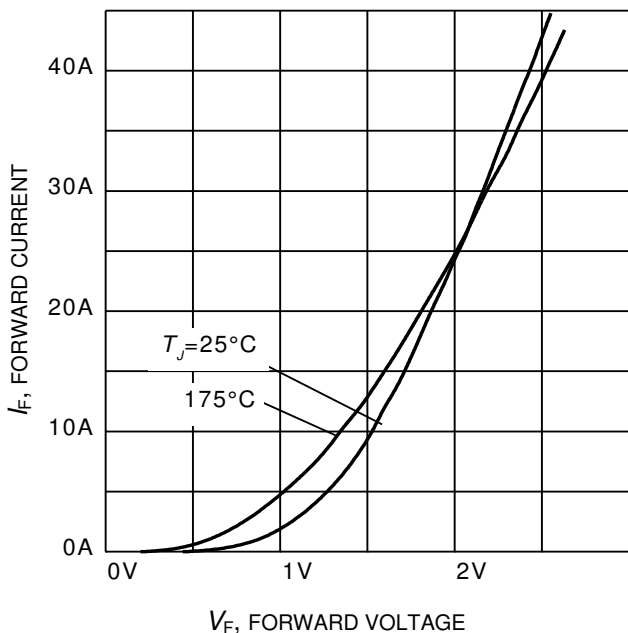
($V_R = 400V$, $I_F = 15A$,
Dynamic test circuit in Figure E)



di_F/dt , DIODE CURRENT SLOPE

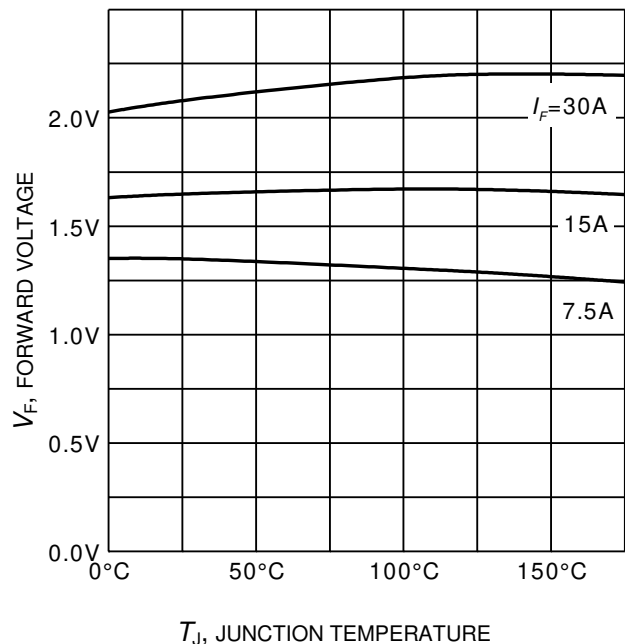
Figure 26. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope

($V_R = 400V$, $I_F = 15A$,
Dynamic test circuit in Figure E)



V_F , FORWARD VOLTAGE

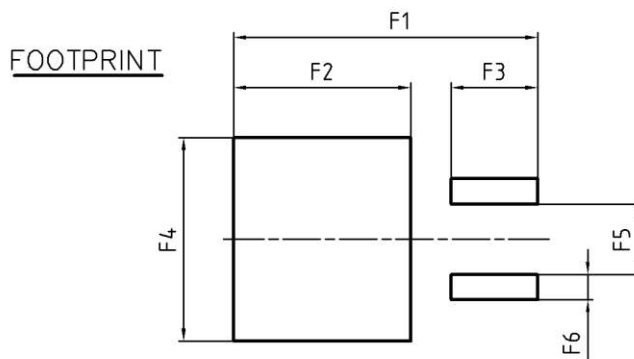
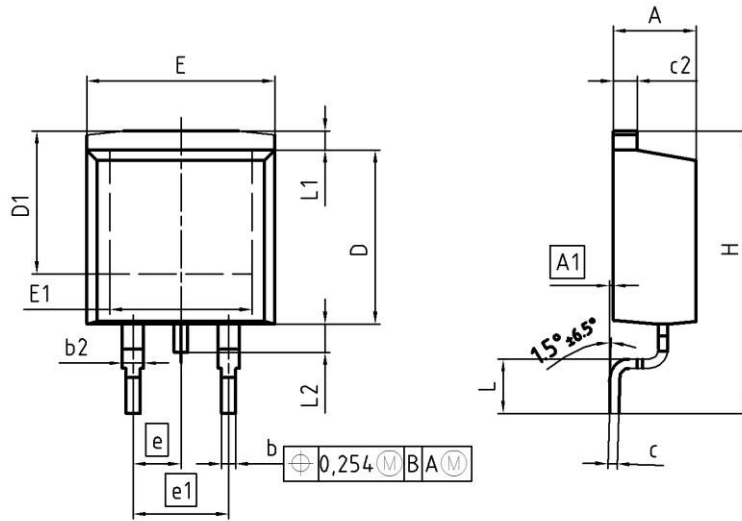
Figure 27. Typical diode forward current as a function of forward voltage



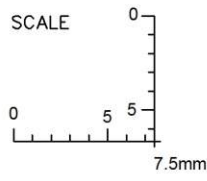
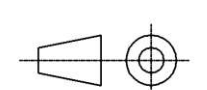
T_J , JUNCTION TEMPERATURE

Figure 28. Typical diode forward voltage as a function of junction temperature

PG-TO263-3



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.30	4.57	0.169	0.180
A1	0.00	0.25	0.000	0.010
b	0.65	0.85	0.026	0.033
b2	0.95	1.15	0.037	0.045
c	0.33	0.65	0.013	0.026
c2	1.17	1.40	0.046	0.055
D	8.51	9.45	0.335	0.372
D1	7.10	7.90	0.280	0.311
E	9.80	10.31	0.386	0.406
E1	6.50	8.60	0.256	0.339
e	2.54		0.100	
e1	5.08		0.200	
N	2		2	
H	14.61	15.88	0.575	0.625
L	2.29	3.00	0.090	0.118
L1	0.70	1.60	0.028	0.063
L2	1.00	1.78	0.039	0.070
F1	16.05	16.25	0.632	0.640
F2	9.30	9.50	0.366	0.374
F3	4.50	4.70	0.177	0.185
F4	10.70	10.90	0.421	0.429
F5	3.65	3.85	0.144	0.152
F6	1.25	1.45	0.049	0.057

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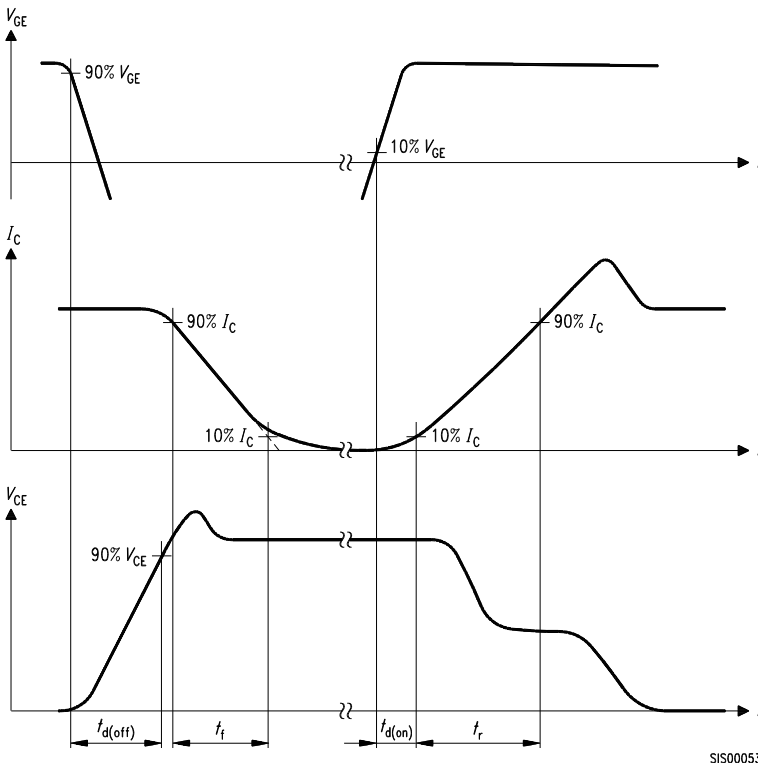


Figure A. Definition of switching times

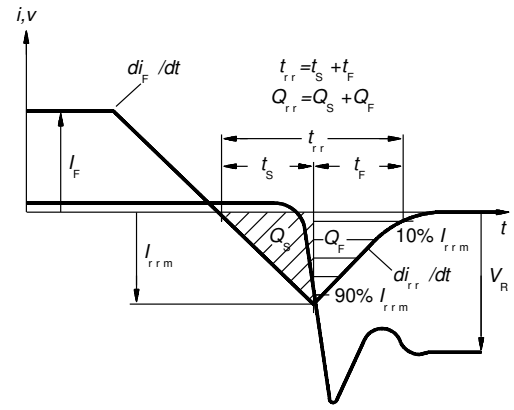


Figure C. Definition of diodes switching characteristics

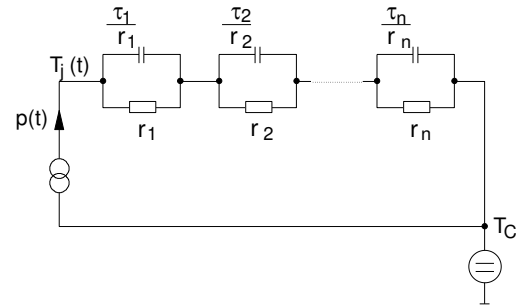


Figure D. Thermal equivalent circuit

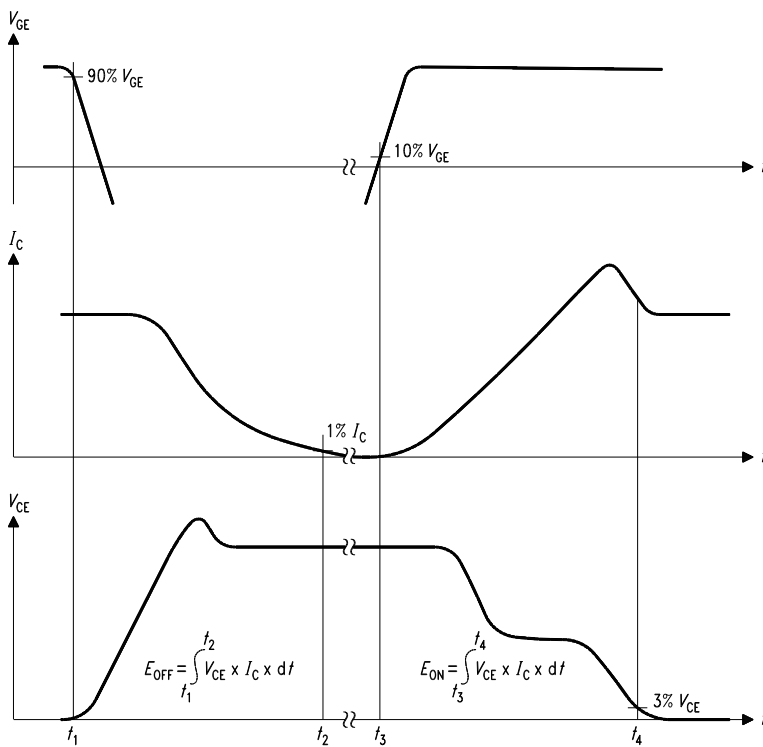


Figure B. Definition of switching losses

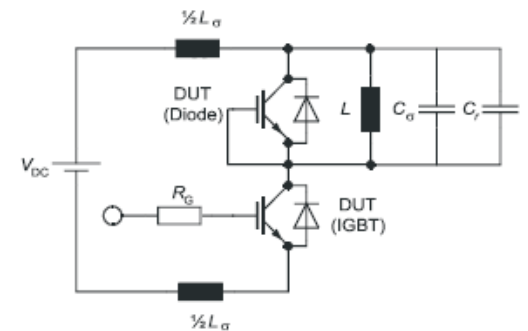


Figure E. Dynamic test circuit
Parasitic inductance L_σ ,
Parasitic capacitor C_σ ,
Relief capacitor C_r
(only for ZVT switching)

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Warnings

Due to technical requirements, components may contain dangerous substances. For information on the types in question, please contact the nearest Infineon Technologies Office.

The Infineon Technologies component described in this Data Sheet may be used in life-support devices or systems and/or automotive, aviation and aerospace applications or systems only with the express written approval of Infineon Technologies, if a failure of such components can reasonably be expected to cause the failure of that life-support, automotive, aviation and aerospace device or system or to affect the safety or effectiveness of that device or system. Life support devices or systems are intended to be implanted in the human body or to support and/or maintain and sustain and/or protect human life. If they fail, it is reasonable to assume that the health of the user or other persons may be endangered.