

VRF141 VRF141MP

28V, 150W, 175MHz

RF POWER VERTICAL MOSFET

The VRF141 is a gold-metallized silicon n-channel RF power transistor designed for broadband commercial and military applications requiring high power and gain without compromising reliability, ruggedness, or inter-modulation distortion.

FEATURES

- Improved Ruggedness V_{(BR)DSS} = 80 V
- 150W with 22dB Typical Gain @ 30MHz, 28V
- 150W with 13dB Typical Gain @ 175MHz, 28V
- Excellent Stability & Low IMD
- Common Source Configuration
- Available in Matched Pairs

- 30:1 Load VSWR Capability at Specified Operating Conditions
- Nitride Passivated
- Refractory Gold Metallization
- High Voltage Replacement for MRF141
- RoHS Compliant

Maximum Rat	ings All Ratings: T _c =2	All Ratings: T _c =25°C unless otherwise specifie			
Symbol	Parameter	VRF141(MP)	Unit		
V _{DSS}	Drain-Source Voltage	80	V		
I _D	Continuous Drain Current @ $T_c = 25^{\circ}C$	20	А		
V _{GS}	Gate-Source Voltage	±40	V		
P _D	Total Device dissipation @ T_c = 25°C	300	W		
T _{stg}	Storage Temperature Range	-65 to 150	°C		
TJ	Operating Junction Temperature	200			

Static Electrical Characteristics

Symbol	Parameter	Min	Тур	Max	Unit
V _{(BR)DSS}	Drain-Source Breakdown Voltage (V _{GS} = 0V, I _D = 100mA)	80			V
V _{DS(ON)}	On State Drain Voltage (I _{D(ON)} = 10A, V _{GS} = 10V)		1.0	1.3	v
I _{DSS}	Zero Gate Voltage Drain Current (V_{DS} = 60V, V_{GS} = 0V)			1.0	mA
I _{gss}	Gate-Source Leakage Current (V_{DS} = ±20V, V_{DS} = 0V)			1.0	μA
9 _{fs}	Forward Transconductance (V_{DS} = 10V, I_{D} = 5A)	5.0			mhos
V _{GS(TH)}	Gate Threshold Voltage (V_{DS} = 10V, I_{D} = 100mA)	2.9	3.6	4.4	V

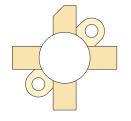
Thermal Characteristics

Symbol	Characteristic	Min	Тур	Max	Unit
R _{θJC}	Junction to Case Thermal Resistance			0.60	°C/W

Dynamic Characteristics

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
C _{ISS}	Input Capacitance	$V_{GS} = 0V$		400		
C _{oss}	Output Capacitance	V _{DS} = 28V		375		pF
C _{rss}	Reverse Transfer Capacitance	f = 1MHz		50		

CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.



Functional Characteristics

VRF141(MP)

Symbol	Parameter	Min	Тур	Max	Unit
G _{PS}	$f_1 = 30MHz, f_2 = 30.001MHz, V_{DD} = 28V, I_{DQ} = 250mA, P_{out} = 150W_{PEP}$	16	20		dB
G _{PS}	$f_1 = 175MHz, V_{DD} = 28V, I_{DQ} = 250mA, P_{out} = 150W$		13		uв
η	$f_1 = 30MHz, f_2 = 30.001MHz, V_{DD} = 28V, I_{DQ} = 250mA, P_{out} = 150W_{PEP}$	40	45		%
IMD _(d3)	f ₁ = 30MHz, f ₂ = 30.001MHz, V _{DD} = 28V, I _{DQ} = 250mA, P _{out} = 150W _{PEP} ¹		-30	-28	dB
IMD _(d11)	$f_1 = 30MHz, f_2 = 30.001MHz, V_{DD} = 28V, I_{DQ} = 250mA, P_{out} = 150W_{PEP}$		-60		uв
Ψ	$\rm f_1$ = 30MHz, $\rm f_2$ = 30.001MHz, $\rm V_{DD}$ = 28V, $\rm I_{DQ}$ = 250mA, $\rm P_{out}$ = 150W $_{\rm PEP}$ 30:1 VSWR - All Phase Angles	No Degradation in Output Power		Power	

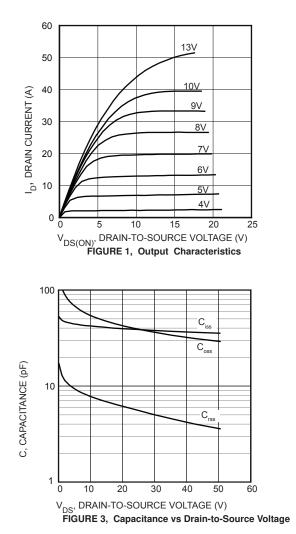
Class A Characteristics

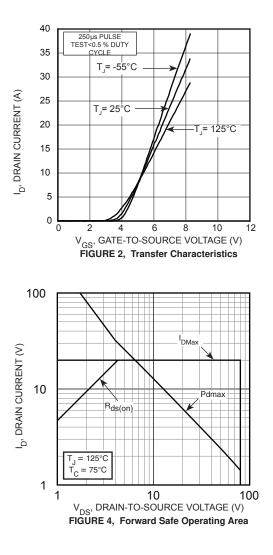
Symbol	Test Conditions	Min	Тур	Max	Unit
G _{PS}	$f_1 = 30MHz, f_2 = 30.001MHz, V_{DD} = 28V, I_{DQ} = 4.0A, P_{out} = 50W_{PEP}$		23		
IMD _(d3)	$f_1 = 30MHz$, $f_2 = 30.001MHz$, $V_{DD} = 28V$, $I_{DQ} = 4.0A$, $P_{out} = 50W_{PEP}$		-50		dB
IMD _(d9-d13)	$f_1 = 30MHz, f_2 = 30.001MHz, V_{DD} = 28V, I_{DQ} = 4.0A, P_{out} = 50W_{PEP}$		-75		

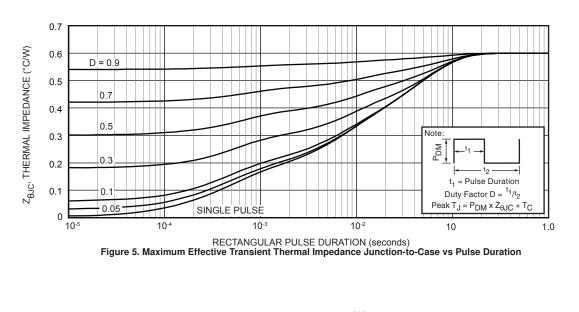
1. To MIL-STD-1311 Version A, test method 2204B, Two Tone, Reference Each Tone

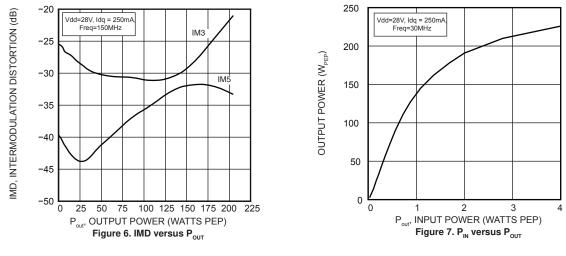
Microsemi reserves the right to change, without notice, the specifications and information contained herein.

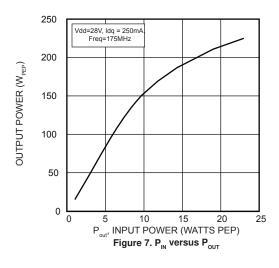
Typical Performance Curves

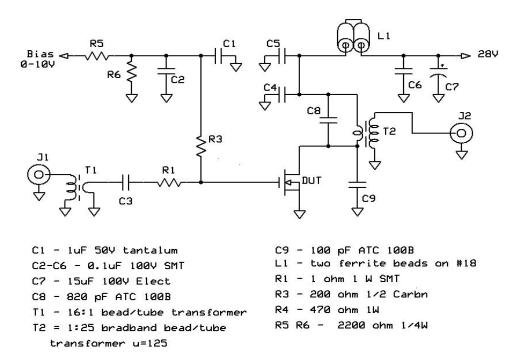




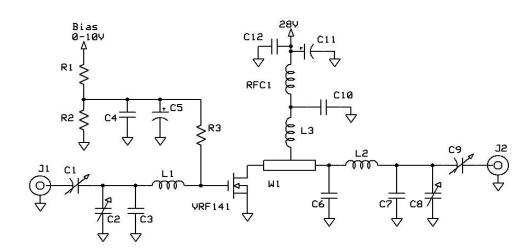








175 MHz test Circuit



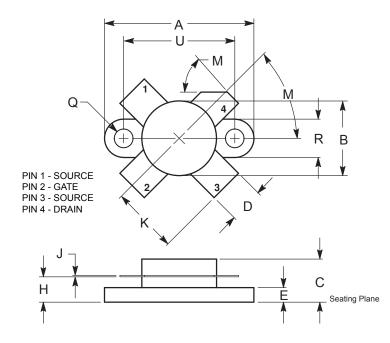
C1, 2, 8, 9 - ARCO 463 C3 C7 - 25 pF ATC 100B C4 C10 C12 - 0.1uF 100V SMT C5 - 1 uF 15WV tant C6 - 270 pF ATC 100B C10 - .05 100V 1206 SMT C11 - 15uF 100V Elect

L1 - 3/4" #18 ga into Hairpin W1 - printed line 0.23"W x 0.7" L L2 - 2t #16 ga .25" dia x .25" ~ 35nH L3 -2 turns #16 ga 5/16" ID tight. ~ 50nH R1 R2 - 2.2k ohm 1/4W R3 - 150 ohm 1/4W RFC1 Fair-Rite 2961666631 (VK200-4B) Adding MP at the end of P/N specifies a matched pair where $V_{GS(TH)}$ is matched between the two parts. V_{TH} values are marked on the devices per the following table.

Code	Vth Range	Code 2	Vth Range
А	2.900 - 2.975	М	3.650 - 3.725
В	2.975 - 3.050	N	3.725 - 3.800
С	3.050 - 3.125	Р	3.800 - 3.875
D	3.125 - 3.200	R	3.875 - 3.950
E	3.200 - 3.275	S	3.950 - 4.025
F	3.275 - 3.350	Т	4.025 - 4.100
G	3.350 - 3.425	W	4.100 - 4.175
Н	3.425 - 3.500	Х	4.175 - 4.250
J	3.500 - 3.575	Y	4.250 - 4.325
К	3.575 - 3.650	Z	4.325 - 4.400

 $V_{_{TH}}$ values are based on Microsemi measurements at datasheet conditions with an accuracy of 1.0%.

M174 Package Outline .5" SOE All Dimensions to be ±.005"



DIM	INC	INCHES		ETERS
DIM	MIN	MAX	MIN	MAX
А	0.096	0.990	24.39	25.14
В	0.465	0.510	11.82	12.95
С	0.229	0.275	5.82	6.98
D	0.216	0.235	5.49	5.96
Е	0.084	0.110	2.14	2.79
Н	0.144	0.178	3.66	4.52
J	0.003	0.007	0.08	0.17
к	0.435		11.0	
М	45° NOM		45° NOM	
Q	0.115	0.130	2.93	3.30
R	0.246	0.255	6.25	6.47
U	0.720	0.730	18.29	18.54



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