

NOT RECOMMENDED FOR NEW DESIGN **USE DMN33D8LDWQ**



DMN63D8LDWQ

DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BVDSS	RDS(ON)	I _D T _A = +25°C
30V	4.2Ω @V _{GS} = 4.5V	200mA
307	2.8Ω @V _{GS} = 10V	260mA

Description

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- DC-DC converters
- Power management functions
- Battery operated systems and solid-state relays
- Drivers: relays, solenoids, lamps, hammers, displays, memories, transistors, etc.

Features

- **Dual N-Channel MOSFET**
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Small Surface Mount Package
- **ESD Protected Gate**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DIODES™ DMN63D8LDWQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

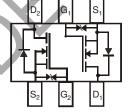
https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: SOT363
- Package Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish annealed over Alloy 42 leadframe (Lead Free Plating). Solderable per MIL-STD-202, Method 208 (3)
- Terminal Connections: See Diagram
- Weight: 0.006 grams (Approximate)







Top View Internal Schematic

Top View

Dorf Mumber	Deskara	Packing		
Part Number	Package	Qty.	Carrier	
DMN63D8LDWQ-7	SOT363	3,000	Tape & Reel	
DMN63D8LDWQ-13	SOT363	10,000	Tape & Reel	

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. Notes:

- See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and 1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/

Marking Information

Ordering Information (Note 4)



MM4 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: J = 2022)M = Month (ex: 8 = August)

Data Cada Kay

Date Code Key												
Year	2014		2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	В		J	K	L	М	N	0	Р	R	S	T
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

	Characteristic			Symbol	Value	Units
Drain-Source Voltage				V _{DSS}	30	V
Gate-Source Voltage				V _{GSS}	±20	V
Continuous Drain Current (Note 5)	V _{GS} = 10V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	220 170	mA
Continuous Drain Current (Note 6)	$V_{GS} = 10V$	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	260 210	mA
Pulsed Drain Current (10μs Pulse, Dι	uty Cycle = 1%)			I _{DM}	800	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units	
Total Power Dissipation	(Note 5)	P _D	300	mW	
Total Fower Dissipation	(Note 6)	FD	400	IIIVV	
Thermal Begistance, Junction to Ambient	(Note 5)	D	435		
Thermal Resistance, Junction to Ambient	(Note 6)	Reja	330	°C/W	
Thermal Resistance, Junction to Case	(Note 6)	$R_{ heta JC}$	139	•	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C	

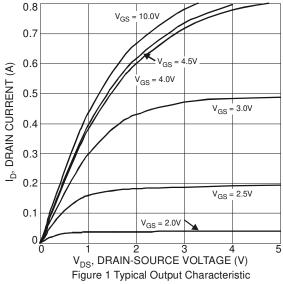
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

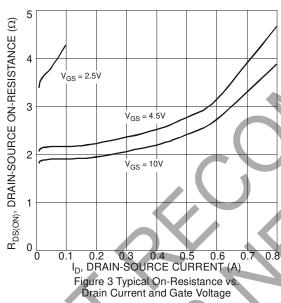
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	30			٧	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	1		1.0	μΑ	$V_{DS} = 30V, V_{GS} = 0V$
Gate-Body Leakage	I _{GSS}			±10.0	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(th)}$	8.0	_	1.5	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
			_	2.8		$V_{GS} = 10.0V, I_D = 250mA$
	~ ~	_	_	3.8		$V_{GS} = 5V, I_D = 250mA$
Static Drain-Source On-Resistance	R _{DS(ON)}		_	4.2	Ω	$V_{GS} = 4.5V, I_D = 250mA$
		_	_	4.5		$V_{GS} = 4.0V, I_D = 250mA$
		_	_	13		$V_{GS} = 2.5V, I_D = 10mA$
Forward Transconductance	g FS	80	_	_	mS	$V_{DS} = 10V, I_D = 0.115A$
Diode Forward Voltage	V_{SD}		8.0	1.2	٧	$V_{GS} = 0V, I_S = 115mA$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	Ciss		22.0	_		
Output Capacitance	Coss	_	3.2		рF	$V_{DS} = 25V, V_{GS} = 0V, f = 1.0MHz$
Reverse Transfer Capacitance	C_{rss}		2.0	_		
Gate Resistance	R_{G}	_	79.9	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge V _{GS} = 10V	Q_g	_	0.87	_		
Total Gate Charge V _{GS} = 4.5V	Q_g		0.43	_	nC	V 10V V 20V I- 150mA
Gate-Source Charge	Q_gs		0.11	_	ПС	$V_{GS} = 10V, V_{DS} = 30V, I_D = 150mA$
Gate-Drain Charge	Q_{gd}	_	0.11	_		
Turn-On Delay Time	t _{D(on)}	_	3.3	—		
Turn-On Rise Time	tr		3.2	_	nS	$V_{DD} = 30V$, $I_D = 0.115A$, $V_{GEN} = 10V$,
Turn-Off Delay Time	$t_{D(off)}$		12.0	_	110	$R_{GEN} = 25\Omega$
Turn-Off Fall Time	t _f	_	6.3	_		

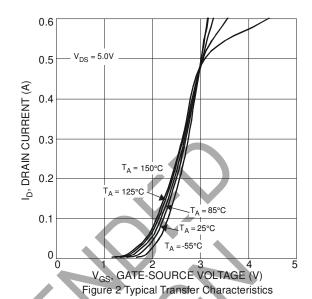
Notes:

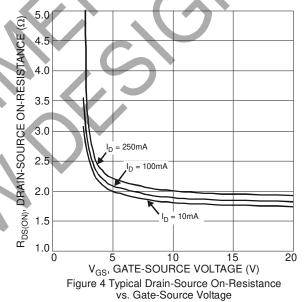
- 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.
 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to production testing.





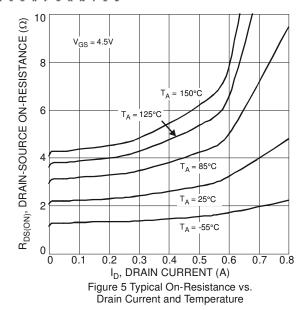


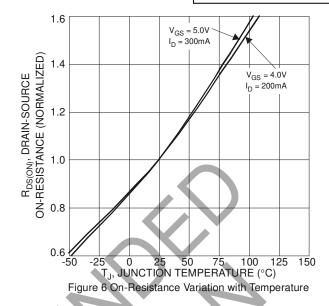


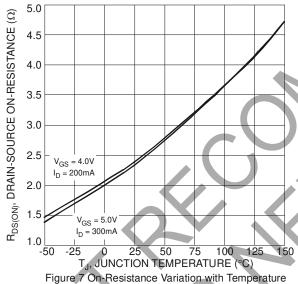


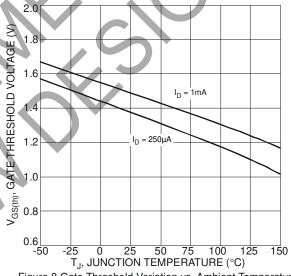












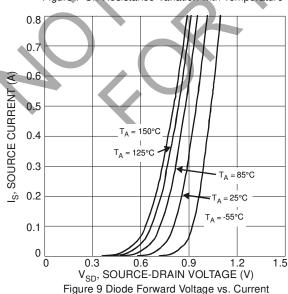


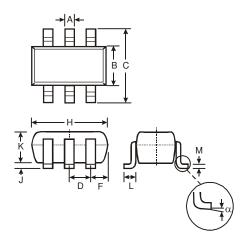
Figure 8 Gate Threshold Variation vs. Ambient Temperature



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT363

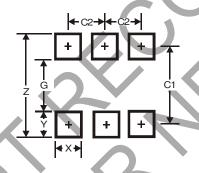


	SOT363							
Dim	Min	Max	Тур					
Α	0.10	0.30	0.25					
В	1.15	1.35	1.30					
С	2.00	2.20	2.10					
D		0.65 Ty	p .					
F	0.40	0.45	0.425					
Н	1.80	2.20	2.15					
J	0	0.10	0.05					
K	0.90	1.00	1.00					
L	0.25	0.40	0.30					
М	0.10	0.22	0.11					
α	0°	8°	-					
All	Dimen	sions i	n mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT363



Dimensions	Value (in mm)
Z	2.5
G	1.3
Х	0.42
Υ	0.6
C1	1.9
C2	0.65



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