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# FCI7N60 N-Channel SuperFET<sup>®</sup> MOSFET $600 V, 7 A, 600 m\Omega$

#### Features

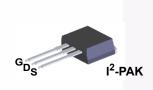
- 650V @ T<sub>J</sub> = 150°C
- Typ. R<sub>DS(on)</sub> = 530 mΩ
- Ultra Low Gate Charge (Typ. Q<sub>g</sub> = 23 nC)
- Low Effective Output Capacitance (Typ. C<sub>oss(eff.)</sub> = 60 pF)
- 100% Avalanche Tested
- RoHS compliant

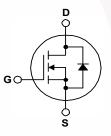
### Application

- Lighting
- Solar Inverter
- AC-DC Power Supply

## Description

SuperFET<sup>®</sup> MOSFET is Fairchild Semiconductor's first generation of high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low onresistance and lower gate charge performance. This technology is tailored to minimize conduction loss, provide superior switching performance, dv/dt rate and higher avalanche energy. Consequently, SuperFET MOSFET is very suitable for the switching power applications such as PFC, server/telecom power, FPD TV power, ATX power and industrial power applications.





#### MOSFET Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted.

Symbol		FCI7N60	Unit		
V <sub>DSS</sub>	Drain to Source Voltage	600	V		
I <sub>D</sub>	Drain Current	- Continuous (T <sub>C</sub> = 25 <sup>o</sup> C)	7	- A	
	Drain Current	- Continuous (T <sub>C</sub> = 100 <sup>o</sup> C)	4.4		
I <sub>DM</sub>	Drain Current	- Pulsed (Note 1)	21	A	
V <sub>GSS</sub>	Gate to Source Voltage	±30	V		
E <sub>AS</sub>	Single Pulsed Avalanche	Energy (Note 2)	230	mJ	
I <sub>AR</sub>	Avalanche Current	(Note 1)	7	A	
E <sub>AR</sub>	Repetitive Avalanche Ene	rgy (Note 1)	(Note 1) 8.3		
dv/dt	Peak Diode Recovery dv/	dt (Note 3)	4.5	V/ns	
P <sub>D</sub>	Dower Dissinction	(T <sub>C</sub> = 25°C)	83	W	
	Power Dissipation	- Derate Above 25°C	0.67	W/ºC	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Te	-55 to +150	°C		
TL	Maximum Lead Temperat	300	°C		
	1				

#### **Thermal Characteristics**

Symbol	Parameter	FCI7N60	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	1.5	°C/W
$R_{\thetaJA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	°C/W

November 2013

Part Nur	nber	Top Mark Pag		je	Packing Method	Reel Size	Тар	e Width	Qua	ntity
FCI7N60		FCI7N60	I <sup>2</sup> -PAK			N/A		50 units		
Electrica	l Char	acteristics T <sub>c</sub> = 2	25ºC unless	othe	rwise noted.					
Symbol	_	Parameter			Test Condition	ons	Min.	Тур.	Max.	Unit
Off Charac	teristic	S				¥				
5.7	Drain to Source Breakdown Voltage Breakdown Voltage Temperature Coefficient			V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 µA, T <sub>C</sub> = 25 <sup>o</sup> C			600	-	-	V
BV <sub>DSS</sub>			ltage	$V_{GS} = 0 V, I_D = 250 \mu A, T_C = 150^{\circ}C$			-	650	-	V
ΔΒV <sub>DSS</sub> /ΔΤ <sub>J</sub>			re	$I_D = 250 \ \mu\text{A}$ , Referenced to $25^{\circ}\text{C}$			-	0.6	-	V/ºC
BV <sub>DS</sub>	Drain-Source Avalanche Breakdown Voltage			V <sub>GS</sub> = 0 V, I <sub>D</sub> = 7 A			-	700	-	V
<b> </b>	Zero Gate Voltage Drain Current		nt	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V		-	-	1		
I <sub>DSS</sub>				V <sub>DS</sub> = 480 V, T <sub>C</sub> = 125 <sup>o</sup> C			-	-	10	μA
I <sub>GSS</sub>	Gate to Body Leakage Current			$V_{GS}$ = ±30 V, $V_{DS}$ = 0 V			-	-	±100	nA
On Charac	teristic	S								
V <sub>GS(th)</sub>	Gate Th	Gate Threshold Voltage		V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> = 250 μA			3.0	-	5.0	V
R <sub>DS(on)</sub>	Static Drain to Source On Resistance		stance	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 3.5 \text{ A}$			-	0.53	0.6	Ω
9 <sub>FS</sub>	Forward Transconductance			$V_{DS} = 40 \text{ V}, \text{ I}_{D} = 3.5 \text{ A}$			-	6	-	S
Dynamic C	haracte	eristics								
C <sub>iss</sub>	Input Capacitance						710	920	pF	
C <sub>oss</sub>	Output	Capacitance		V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1.0 MHz		-	380	500	pF	
C <sub>rss</sub>	Reverse	e Transfer Capacitance				-	34	-	pF	
C <sub>oss</sub>	Output	Capacitance		V <sub>DS</sub> = 480 V, V <sub>GS</sub> = 0 V, f = 1 MHz		/, f = 1 MHz	-	22	29	pF
Coss(eff.)	Effective	e Output Capacitance		$V_{DS} = 0 V$ to 400 V, $V_{GS} = 0 V$			-	60	-	pF
Switching	Charac	teristics								
t <sub>d(on)</sub>	1	Delay Time	-					35	80	ns
t <sub>r</sub>		Rise Time		VD	/ <sub>DD</sub> = 300 V, I <sub>D</sub> = 7 A,			55	120	ns
t <sub>d(off)</sub>	Turn-Of	ff Delay Time		$V_{GS}$ = 10 V, $R_{G}$ = 25 $\Omega$			75	160	ns	
t <sub>f</sub>		f Fall Time		(Note 4)		-	32	75	ns	
Q <sub>g(tot)</sub>	Total Ga	ate Charge at 10V		V <sub>DS</sub> = 480 V, I <sub>D</sub> = 7 A, V <sub>GS</sub> = 10 V			-	23	30	nC
Q <sub>gs</sub>	Gate to	Source Gate Charge					-	4.2	5.5	nC
Q <sub>gd</sub>	Gate to Drain "Miller" Charge			(Note 4)			-	11.5	-	nC
	rce Diod	de Characteristics	5							
I <sub>S</sub>	-	m Continuous Drain to		e Foi	ward Current		-	-	7	Α
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode						-	-	21	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage			$V_{GS} = 0 \text{ V}, \text{ I}_{SD} = 7 \text{ A}$			-		1.4	V
t <sub>rr</sub>		Recovery Time		$V_{GS} = 0 V, I_{SD} = 7 A,$ $U_{GS} = 0 V, I_{SD} = 7 A,$ $dI_{F}/dt = 100 A/\mu s$			-	360	-	ns
Q <sub>rr</sub>		Recovery Charge				-	4.5		μC	

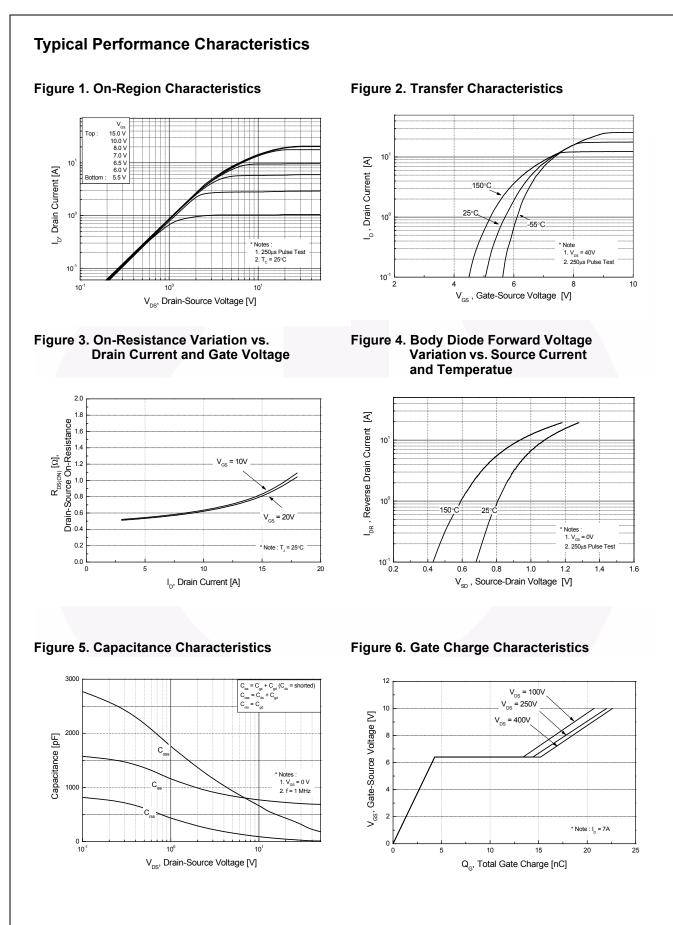
1. Repetitive rating: pulse-width limited by maximum junction temperature.

2. I\_{AS} = 3.5 A, V\_{DD} = 50 V, R\_G = 25 $\Omega$ , starting T\_J = 25°C.

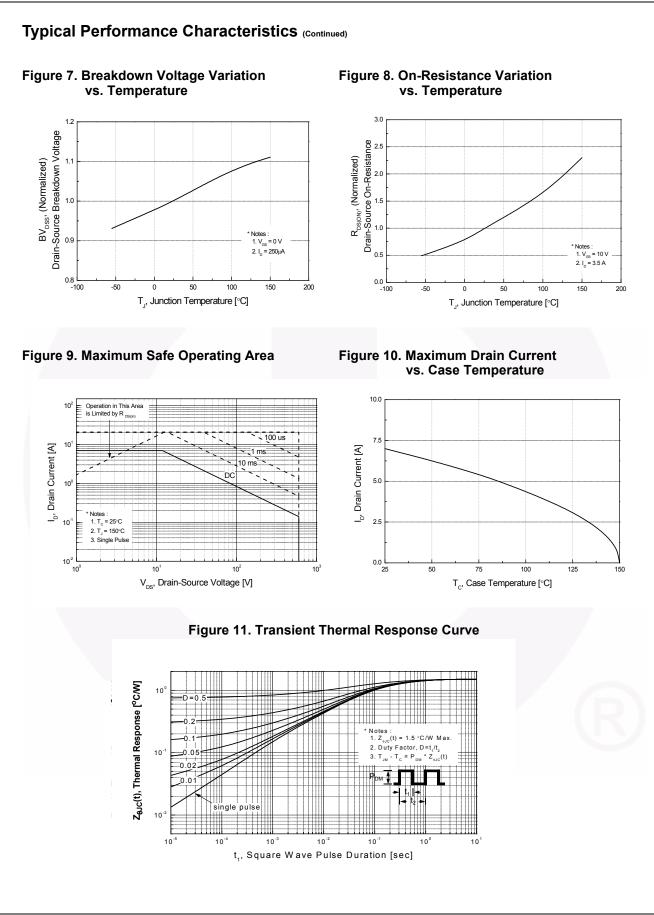
3. I\_{SD} \leq 7 A, di/dt  $\leq$  200 A/µs, V\_{DD}  $\leq$  BV\_{DSS}, starting T\_J = 25°C.

4. Essentially independent of operating temperature typical characteristics.

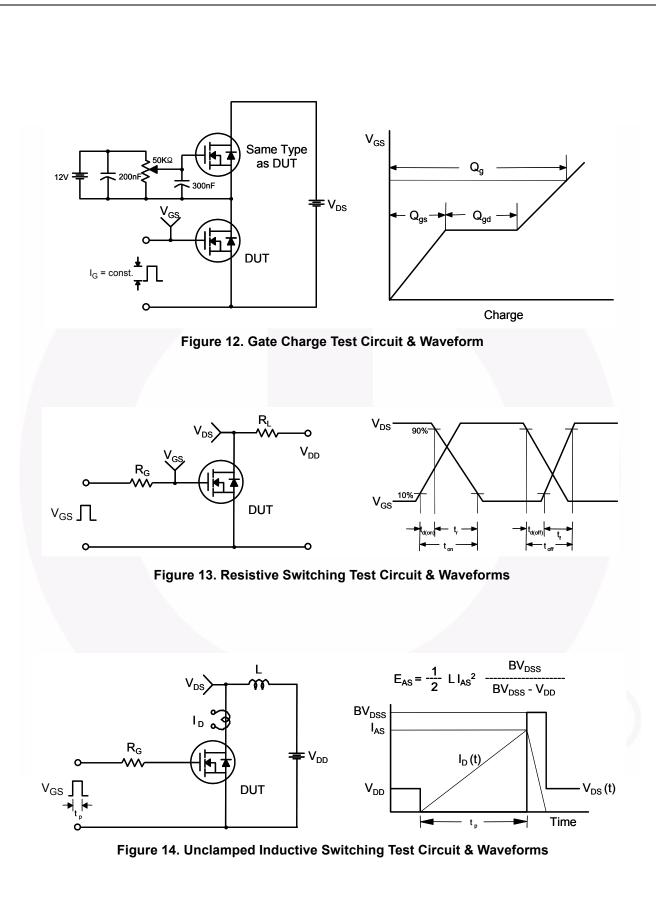
FCI7N60 — N-Channel SuperFET<sup>®</sup> MOSFET



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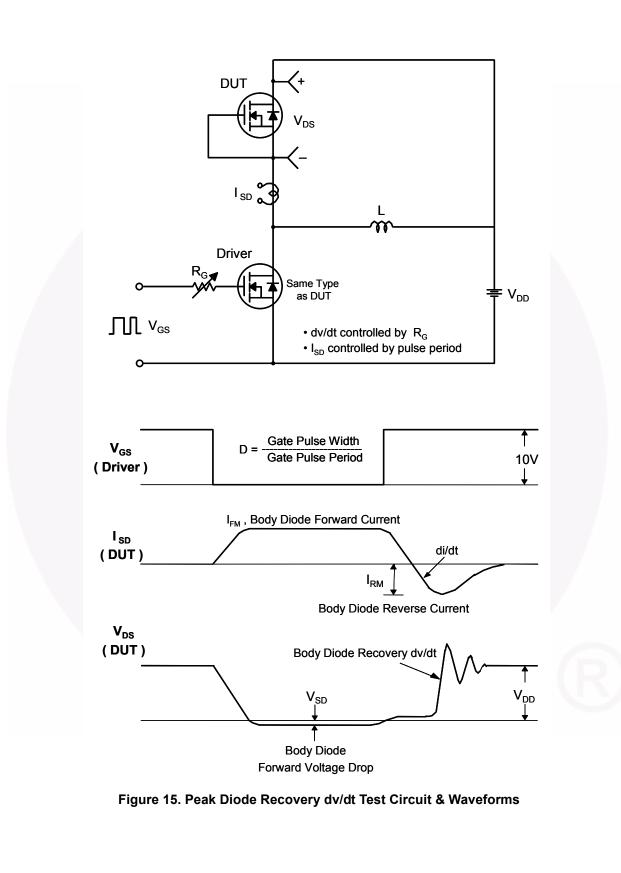


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FCI7N60 — N-Channel SuperFET<sup>®</sup> MOSFET

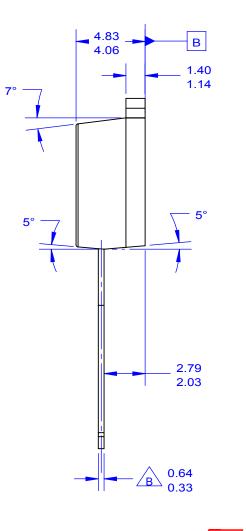
FCI7N60 — N-Channel SuperFET<sup>®</sup> MOSFET



10.29 Α 9.65 8.33 6.22 1.40 1.00 7.88 6.86 9.65 8.64 (+)2 3 1 3.96 B 2.80 (2.13)-14.73 12,70 1.78 <u>\_\_\_\_</u> 1.14 SEE NOTE "G' 2.54 0.90 ∕B∖ 0.64 5.08 ⊕ 0.254 A B

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