

### 100V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> Max	I <sub>D</sub> T <sub>C</sub> = +25°C
100V	8.9mΩ @ V <sub>GS</sub> = 10V	86A

### **Features**

- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production -Ensures More Reliable and Robust End Application
- Thermally Efficient Package-Cooler Running Applications
- High Conversion Efficiency
- Low R<sub>DS(ON)</sub> Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- <1.1mm Package Profile Ideal for Thin Applications (PowerDI®)
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DMTH10H009SPSQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

## **Description**

This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

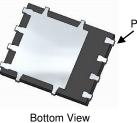
- Motor Control
- **DC-DC Converters**
- Power Management

# **Mechanical Data**

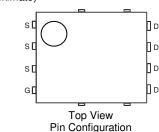
- Case: PowerDI5060-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminal Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.097 grams (Approximate)







Pin1 Internal Schematic



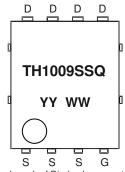
### Ordering Information (Note 4)

Part Number	Case	Packaging
DMTH10H009SPSQ-13	PowerDI5060-8	2,500 / Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

### **Marking Information**



 $\bigcap \prod = Manufacturer's Marking$ TH1009SSQ = Product Type Marking Code YYWW = Date Code Marking YY = Last Two Digits of Year (ex: 20 = 2020) WW = Week Code (01 to 53)

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# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V <sub>DSS</sub>	100	V		
Gate-Source Voltage	Vgss	±20	V		
Continuous Drain Current V <sub>GS</sub> = 10V (Note 6)	Steady State	$T_A = +25^{\circ}C$ $T_A = +100^{\circ}C$	lο	14 10	Α
Continuous Drain Current V <sub>GS</sub> = 10V (Note 7)	Steady State	$T_{C} = +25^{\circ}C$ $T_{C} = +100^{\circ}C$	lo	86 61	Α
Pulsed Drain Current (10µs Pulse, T <sub>C</sub> = +25°C, Package Li	I <sub>DM</sub>	340	Α		
Maximum Continuous Body Diode Forward Current (Note 7	Is	83	Α		
Pulsed Body Diode Current (10μs Pulse, T <sub>C</sub> = +25°C, Pack	I <sub>SM</sub>	340	Α		
Avalanche Current (Note 8), L = 3mH	las	11	Α		
Avalanche Energy (Note 8), L = 3mH	Eas	181.5	mJ		

### **Thermal Characteristics**

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T <sub>A</sub> = +25°C	PD	1.6	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	94	°C/W
Total Power Dissipation (Note 6)	T <sub>A</sub> = +25°C	PD	2.9	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Reja	51	°C/W
Total Power Dissipation (Note 7)	T <sub>C</sub> = +25°C	PD	100	W
Thermal Resistance, Junction to Case (Note 7)		Rejc	1.5	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +175	°C

# **Electrical Characteristics** (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 9)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	100	_	_	V	$V_{GS} = 0V$ , $I_D = 1mA$	
Zero Gate Voltage Drain Current	IDSS	_	_	1	μΑ	$V_{DS} = 80V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 9)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	2	_	4	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$	
Static Drain-Source On-Resistance	RDS(ON)	_	6.7	8.9	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A	
Diode Forward Voltage	V <sub>SD</sub>	_	0.8	1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 13A	
DYNAMIC CHARACTERISTICS (Note 10)	DYNAMIC CHARACTERISTICS (Note 10)						
Input Capacitance	Ciss		2085	_		V <sub>DS</sub> = 50V, V <sub>GS</sub> = 0V f = 1MHz	
Output Capacitance	Coss	_	609	_	рF		
Reverse Transfer Capacitance	Crss	_	13	_			
Gate Resistance	Rg	_	1.7	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge	Qg	_	30	_			
Gate-Source Charge	Qgs	_	9.5	_	nC	$V_{DD} = 50V, I_D = 13A,$ $V_{GS} = 10V$	
Gate-Drain Charge	Qgd	_	7.3	_			
Turn-On Delay Time	t <sub>D(ON)</sub>	_	9.7	_		$V_{DD} = 50V, V_{GS} = 10V,$ $I_{D} = 13A, R_{g} = 6\Omega$	
Turn-On Rise Time	tr		13.7	_			
Turn-Off Delay Time	tD(OFF)	_	25.1	_	ns		
Turn-Off Fall Time	ÌF	_	17.3	_			
Reverse Recovery Time	trr	_	45	_	ns	1 404 41/-14 4004/	
Reverse Recovery Charge	Qrr	_	68	_	nC	I <sub>F</sub> = 13A, di/dt = 100A/μs	

- 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
  7. Thermal resistance from junction to soldering point (on the exposed drain pad).
- 8.  $I_{AS}$  and  $E_{AS}$  ratings are based on low frequency and duty cycles to keep  $T_J = +25$ °C.
- 9. Short duration pulse test used to minimize self-heating effect.
- 10. Guaranteed by design. Not subject to product testing.





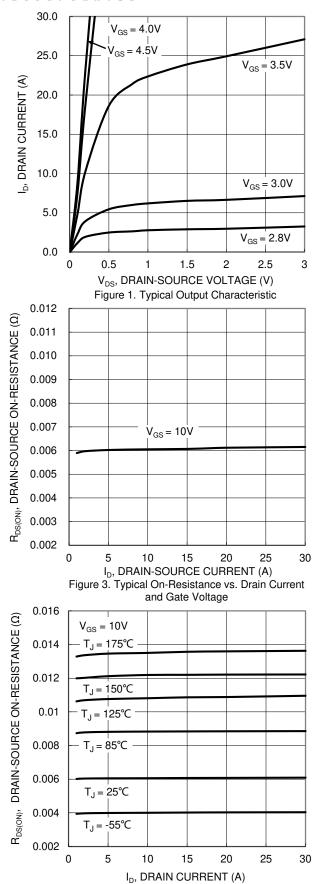
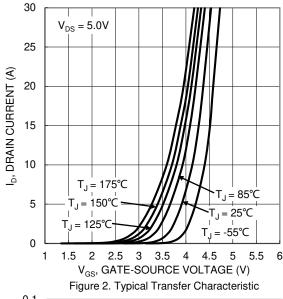
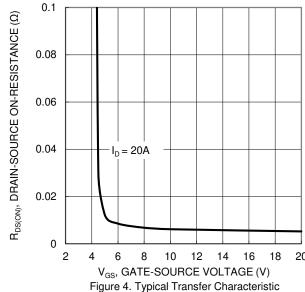


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





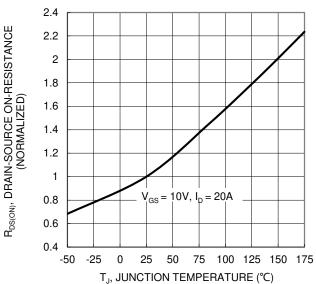
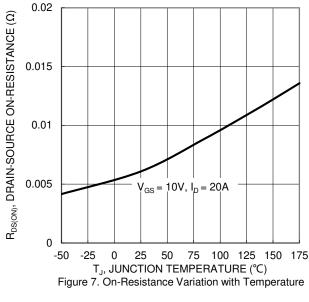
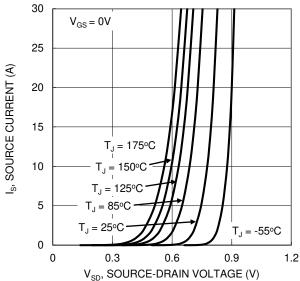


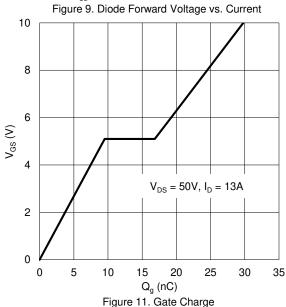
Figure 6. On-Resistance Variation with Temperature

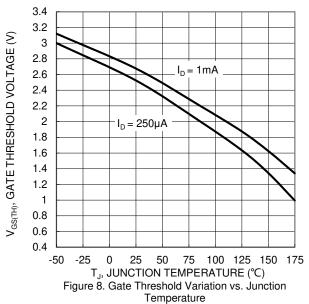


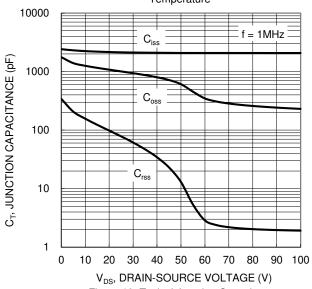


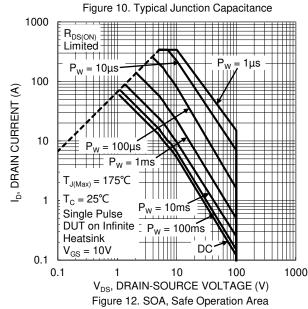














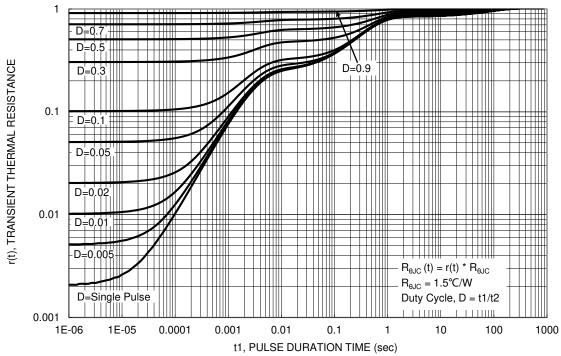


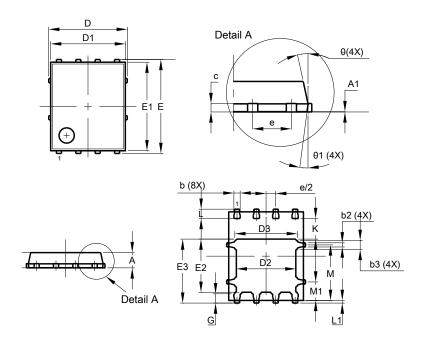
Figure 13. Transient Thermal Resistance



## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI5060-8

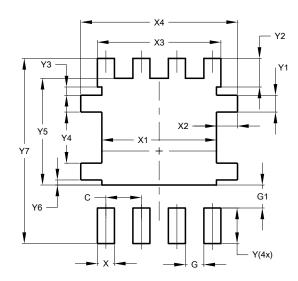


PowerDI5060-8					
Dim	Min	Max	Тур		
Α	0.90	1.10	1.00		
A1	0.00 0.05		-		
b	0.33	0.51	0.41		
b2	0.200	0.350	0.273		
b3	0.40	0.80	0.60		
C D	0.230	0.330	0.277		
_		5.15 BSC	,		
D1	4.70	5.10	4.90		
D2	3.70	4.10	3.90		
D3	3.90	4.30	4.10		
Е	(	6.15 BSC	;		
E1	5.60	6.00	5.80		
E2	3.28	3.68	3.48		
E3	3.99	4.39	4.19		
е	1.27 BSC				
G	0.51	0.71	0.61		
K	0.51	-	-		
L	0.51	0.71	0.61		
L1	0.100	0.200	0.175		
М	3.235	4.035	3.635		
M1	1.00	1.40	1.21		
Θ	10°	12°	11°		
Θ1	6°	8°	7°		
All Dimensions in mm					

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI5060-8



Dimensions	Value (in mm)			
С	1.270			
G	0.660			
G1	0.820			
X	0.610			
X1	4.100			
X2	0.755			
Х3	4.420			
X4	5.610			
Υ	1.270			
Y1	0.600			
Y2	1.020			
Y3	0.295			
Y4	1.825			
Y5	3.810			
Y6	0.180			
Y7	6.610			



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