



### FLEXRAY BUS ESD PROTECTION DIODE

## **Product Summary**

V <sub>BR</sub> (Min)	IPP (Max)	Ст (Тур)
25.4V	3A	11pF

### **Features**

- 200W Peak Power Dissipation per Line (8/20µs Waveform)
- Provides ESD Protection per IEC 61000-4-2 Standard: Air ±30kV, Contact ±30kV
- 2 Channels of ESD Protection
- Low Channel Input Capacitance
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The DESD1FLEX2SOQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

## **Description and Applications**

This DESD1FLEX2SOQ is a next generation ESD and surge protection device packaged in a small footprint surface mount package. It is qualified to AEC-Q101, supported by a PPAP and is designed to protect two automotive FlexRay bus lines from ElectroStatic Discharge and other transients.

- FlexRay Bus Protection
- Industrial Control Network

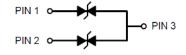
### **Mechanical Data**

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound.
  UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Alloy 42 Leadframe
  (Lead Free Plating). Solderable per MIL-STD-202, Method 208(e3)
- Weight: 0.009 grams (Approximate)

### SOT23



**Bottom View** 



**Device Schematic** 

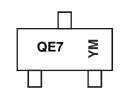
## **Ordering Information (Note 4)**

Part Number	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
DESD1FLEX2SOQ-7	Automotive	QE7	7	8	3,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

### **Marking Information**



SOT23

QE7 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020) M = Month (ex: 9 = September)

## Date Code Key

Year	2016		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	D		Н	I	J	K	L	М	N	0	Р	R
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



# **Maximum Ratings** ( $@T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Power Dissipation	P <sub>PP</sub>	200	W	8/20μs, per Figure 1
Peak Pulse Current	IPP	3	Α	8/20μs, per Figure 1
ESD Protection – Contact Discharge	VESD_Contact	±30	kV	IEC 61000-4-2 Standard
ESD Protection – Air Discharge	V <sub>ESD_Air</sub>	±30	kV	IEC 61000-4-2 Standard

## **Thermal Characteristics**

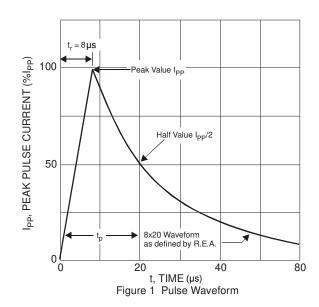
Characteristic	Symbol	Value	Unit
Package Power Dissipation (Note 5)	P <sub>D</sub>	300	mW
Thermal Resistance, Junction to Ambient (Note 5)	Reja	417	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-65 to +150	°C

## Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Standoff Voltage	V <sub>RWM</sub>	_	_	24	V	_
Channel Leakage Current (Note 6)	I <sub>RM</sub>	_	<1	50	nA	V <sub>RWM</sub> = 24V
Clemning Veltage Positive Transients	V	_	_	40	V	I <sub>PP</sub> = 1A, tp = 8/20μs, Figure 1
Clamping Voltage, Positive Transients	VCL	_	_	70	V	I <sub>PP</sub> = 3A, tp = 8/20μs, Figure 1
Breakdown Voltage	V <sub>BR</sub>	25.4	27.8	30.3	V	I <sub>R</sub> = 1mA
Channel Input Capacitance	Ст	_	11	17	pF	V <sub>R</sub> = 0V, f = 5MHz
Charmer input Capacitance	CI	-	_	17	рі	
ABS Parasitic Capacitance Matching (Channel 1 – Channel 2)	$\Delta$ (CT_Ch1-CT_Ch2) ) / C <sub>T</sub> Max)	1	0.12	2	%	V <sub>R</sub> = 5V, f = 5MHz
(Chainer 1 – Chainer 2)	Δ (Cτ_Ch1-Cτ_Ch2)	_	0.02	0.34	pF	

Notes:

- 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at http://www.diodes.com/package-outlines.html.
- 6. Short duration pulse test used to minimize self-heating effect.



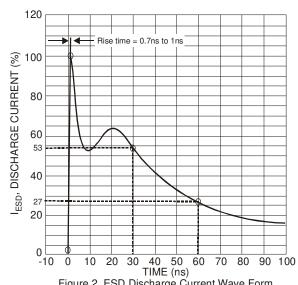
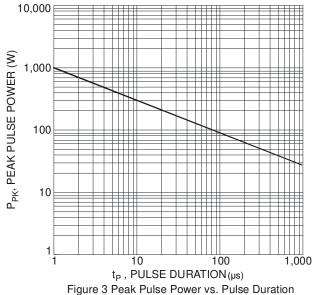
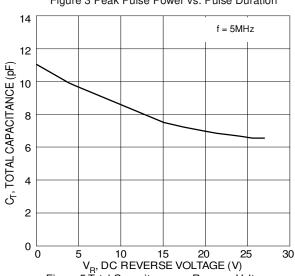


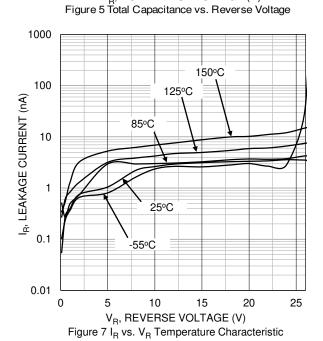
Figure 2 ESD Discharge Current Wave Form IEC 61000-4-2 (330Ω/150pF)



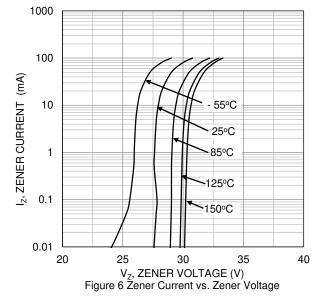








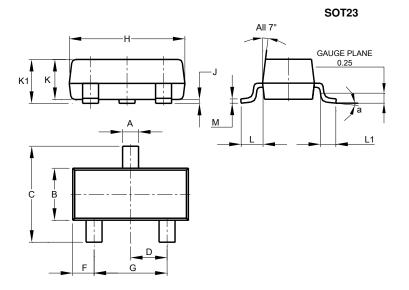
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## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

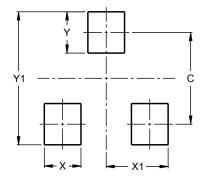


SOT23							
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
K	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
М	0.085	0.150	0.110				
а	0°	8°					
All Dimensions in mm							

# **Suggested Pad Layout**

 $Please \ see \ http://www.diodes.com/package-outlines.html \ for \ the \ latest \ version.$ 

### SOT23



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Υ	0.9
Y1	2.9



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