

## 0.5A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

### Features

- Low Forward Voltage Drop
- Guard Ring Construction for Transient Protection
- High Conductance
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.**  
<https://www.diodes.com/quality/product-definitions/>
- **An Automotive-Compliant Part is Available Under Separate Datasheet (B0540WQ)**

### Mechanical Data

- Package: SOD123
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Lead Free Plating (Matte Tin Finish Annealed over Alloy 42 Leadframe) Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Weight: 0.01 grams (Approximate)



Top View

### Ordering Information (Note 4)

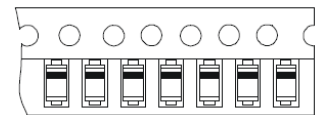
Part Number	Package	Packing	
		Qty.	Carrier
B0540W-7-F	SOD123	3,000	Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

### Marking Information



SF = Product Type Marking Code  
 YM & ŸM = Date Code Marking  
 Y & Ÿ = Year (ex: J = 2022)  
 M = Month (ex: 9 = September)



#### Date Code Key

Year	2002	...	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	O	...	J	K	L	M	N	O	P	R	S	T

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.  
For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	40	V
Working Peak Reverse Voltage	V <sub>RWM</sub>		
DC Blocking Voltage	V <sub>R</sub>		
RMS Reverse Voltage	V <sub>R(RMS)</sub>	28	V
Average Rectified Output Current (See Figure 5)	I <sub>O</sub>	0.5	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	5.5	A

## Thermal Characteristics

Characteristic	Symbol	Typ	Max	Unit
Typical Thermal Resistance Junction to Ambient Air (Note 5) T <sub>A</sub> = +25°C	R <sub>θJA</sub>	385	—	°C/W
Typical Thermal Resistance Junction to Ambient Air (Note 6) T <sub>A</sub> = +25°C	R <sub>θJA</sub>	325	—	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150		°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Test Conditions
Minimum Reverse Breakdown Voltage (Note 7)	V <sub>(BR)R</sub>	40	V	I <sub>R</sub> = 20μA
Maximum Forward Voltage Drop	V <sub>FM</sub>	0.510	V	I <sub>F</sub> = 0.5A, T <sub>J</sub> = +25°C
		0.620		I <sub>F</sub> = 1.0A, T <sub>J</sub> = +25°C
		0.460		I <sub>F</sub> = 0.5A, T <sub>J</sub> = +100°C
		0.610		I <sub>F</sub> = 1.0A, T <sub>J</sub> = +100°C
Maximum Leakage Current (Note 7)	I <sub>RM</sub>	10	μA	V <sub>R</sub> = 20V, T <sub>J</sub> = +25°C
		20		V <sub>R</sub> = 40V, T <sub>J</sub> = +25°C
		5.0	mA	V <sub>R</sub> = 20V, T <sub>J</sub> = +100°C
13	V <sub>R</sub> = 40V, T <sub>J</sub> = +100°C			
Total Capacitance	C <sub>T</sub>	170	pF	f = 1MHz, V <sub>R</sub> = 0V DC

Notes: 5. FR-4 PCB, minimum recommended pad layout per <http://www.diodes.com/package-outlines.html>.  
6. Polyimide PCB, minimum recommended pad layout per <http://www.diodes.com/package-outlines.html>.  
7. Short duration pulse test used to minimize self-heating effect.

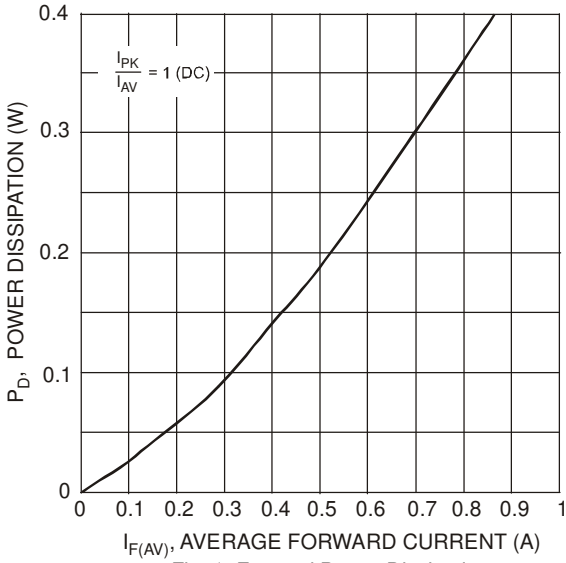


Fig. 1 Forward Power Dissipation

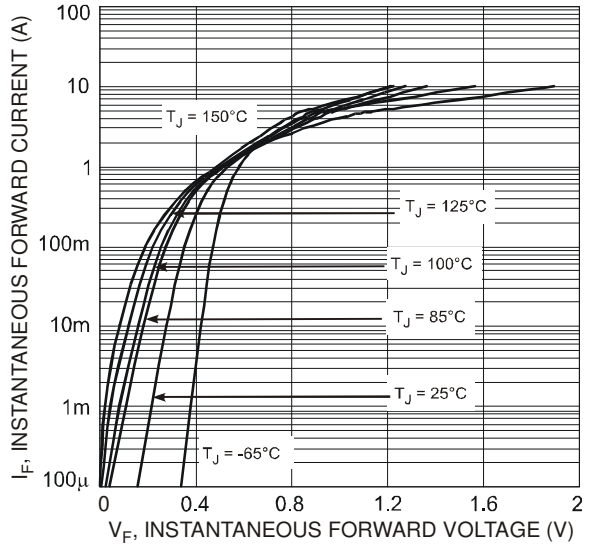


Fig. 2 Typical Forward Characteristics

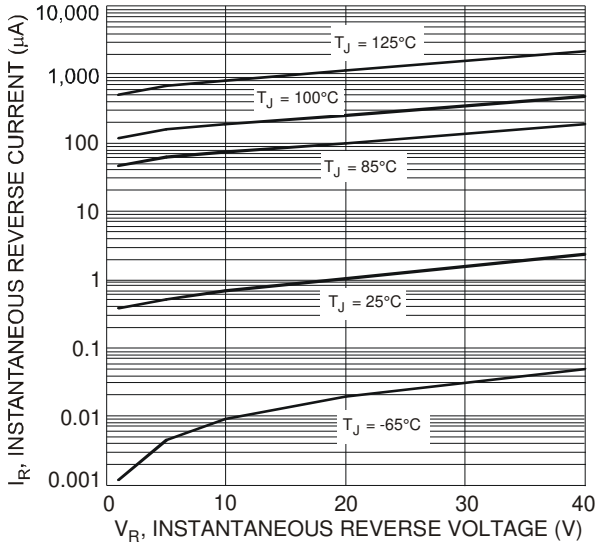


Fig. 3 Typical Reverse Characteristics

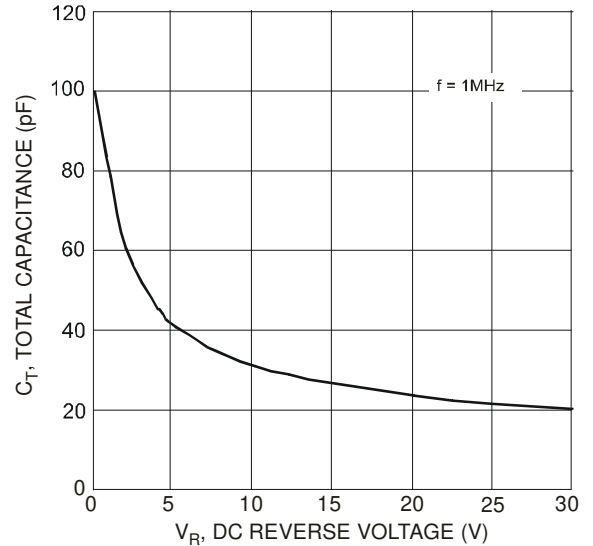


Fig. 4 Total Capacitance vs. Reverse Voltage

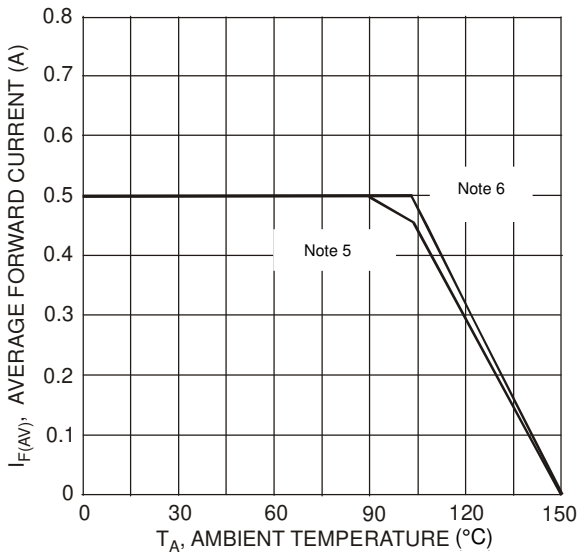
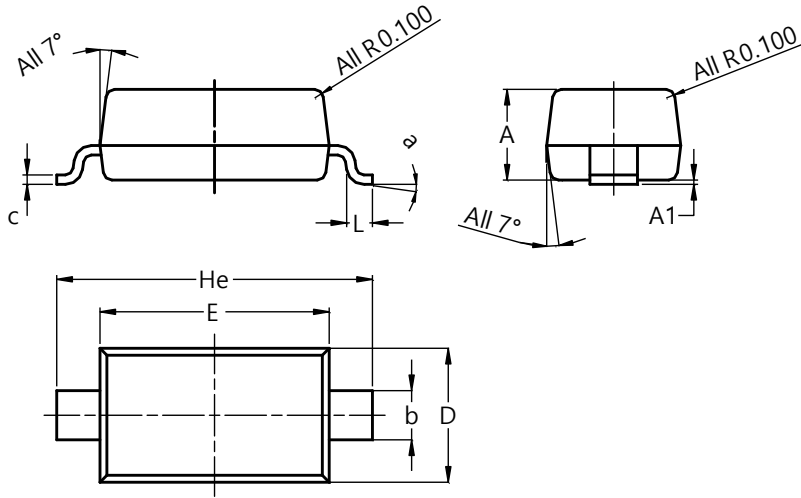


Fig. 5 Forward Current Derating Curve

**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SOD123**

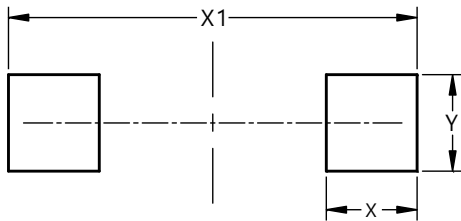


SOD123			
Dim	Min	Max	Typ
A	1.00	1.35	1.05
A1	0.00	0.10	0.05
b	0.52	0.62	0.57
c	0.10	0.15	0.11
D	1.40	1.70	1.55
E	2.55	2.85	2.65
He	3.55	3.85	3.65
L	0.25	0.40	0.30
a	0°	8°	--
All Dimensions in mm			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SOD123**



Dimensions	Value (in mm)
X	0.900
X1	4.050
Y	0.950

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