

# RoHS Compliant

# TFT LCD Module with Metallized Projective Capacitive Touch Panel (FLETAS touch panel)

# **Hardware Specification**

# Model: GTWV070C3A00PA

| Specification No: | DS-2082-0001-01   |      |
|-------------------|-------------------|------|
| Date of Issue:    | November 29, 2019 | (00) |
| Revision:         | December 26, 2019 | (01) |

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This specification is subject to change without prior notice.

# This product complies with RoHS Directive Please contact our sales consultant for details and to confirm the current status

FLETAS are products manufactured by Noritake itron Corporation.

# Notice for the Cautious Handling of LCD Modules

Handling and usage precautions: Please carefully follow the appropriate product application notes and operation standards for proper usage, safe handling, and maximum performance.

| [Constr    | uction]   |  |  |  |  |
|------------|---|--|--|--|--|
| 0          | The FLETAS touch panel is made of glass.<br>When using this product, please be sure to<br>install a protective overlay such as cover<br>glass, acrylic plate, etc.  |  |  |  |  |
| 0          | Please handle the FLETAS touch panel very carefully as it may crack if it is pressed with excessive force.  |  |  |  |  |
| 0          | If this product is bent or twisted, it may cause<br>the breakage of parts on the product board.<br>Please handle it very carefully without bending<br>or twisting.  |  |  |  |  |
| $\bigcirc$ | Please do not hold the FPC or other cables on this product as it may disconnect vital components.   |  |  |  |  |
| 0          | The end faces of the FLETAS touch panel are not polished. Please handle it very carefully to avoid injury.  |  |  |  |  |
| 0          | The FLETAS touch panel is made of glass.<br>It may be damaged by falling / impact /<br>excessive vibration. In the unlikely event that<br>the glass shatters, please handle the glass<br>fragments very carefully to avoid injury.  |  |  |  |  |
| 0          | The LCD panel generates heat. Please<br>provide clearance for heat dissipation<br>between this product and its enclosure. Also,<br>if a structure has electronic parts that are<br>densely collected near this product, we<br>recommend that it be cooled with a fan or<br>something similar.   |  |  |  |  |
| Cable C    | Connection  |  |  |  |  |
| $\bigcirc$ | Please do not remove the power cable and<br>signal cable if the product is in an energized<br>state. It may cause damage to the power<br>supply circuit and/or the I/O circuit of this<br>product.  |  |  |  |  |
| $\bigcirc$ | As a rule of thumb, please do not input any signals while the product is not receiving adequate power. It may cause damage to the input/ output circuit.  |  |  |  |  |
| 0          | <ul><li>When plugging-in or unplugging the cable for this product, please do not apply excessive force, such as pulling the cable.</li><li>Please plug-in or unplug in a straight direction (alignment) with the terminal, without bending or twisting forces.</li><li>If it is not properly plugged-in or unplugged, damage may occur to the cable or connector.</li></ul> |  |  |  |  |
| 0          | Please avoid sudden bending of the cable from<br>the base of the insert connector part. It may<br>cause damage to the cable or disconnection of<br>the connector. If loads are expected on<br>connectors and cables, please fix cables etc.   |  |  |  |  |

#### [Electrostatic Charge]

| 0       | Since we are using semiconductor products,<br>please pay attention to the electrostatic<br>breakdown during handling and transportation.<br>(If it is judged that the product is defective due<br>to electrostatic destruction during its return to<br>our company, it may be repaired for a fee.) |
|---------|--|
| [Power] |  |
|         | Please use a fully stabilized power supply.  |

| 0 | Please use a fully stabilized power supply.<br>If the poweSr supply's voltage is outside of the<br>product's rated supply voltage, the operation<br>of the power supply circuit may become<br>unstable. |
|---|---|
| 0 | In-rush current flows when turning the power<br>on. Please use a power supply that can<br>withstand more than twice the normal current.   |
| 0 | As a safety measure, we recommend using a power supply with overcurrent protection and a fuse.  |
| 0 | Please confirm that the power supply voltage<br>is within the rating of the connector.<br>Please use a power cable with the appropriate<br>thickness and length.  |

#### [Interface]



Please use an interface cable that has a length that has been thoroughly verified.

#### [Implementation]



When installing this product, please make sure that the on-board electronic components and FPC do not touch the metal chassis. It may cause the product to malfunction.



If you need to make the product drip-proof, please use waterproof measures for products by using rubber etc.

Please handle the product carefully when you take it out from the case and when you install the product, since it is a precision part.

#### GTWV070C3A00PA Hardware Specification

#### [Storage and Operating Environment]

| [Storage   | e and Operating Environment]  |
|------------|---|
| 0          | Please use this product within the<br>environmental condition range stated in its<br>specification.<br>Exceeding the stated temperature, humidity,<br>vibration, and impact limitations (along with<br>other stated limitations) may cause<br>malfunction.  |
| 0          | Please do not exceed the absolute maximum ratings stated in the specification even for a moment. It may cause malfunction.  |
| 0          | Under a high temperature environment, the<br>FLETAS touch panel surface also becomes<br>hot.<br>If you touch the FLETAS panel with your bare<br>hand, please be careful of burns, injury, etc.  |
| 0          | Malfunction may occur when the product is stored and/or used in environments with a lot of salt, sulfur, dust, etc.   |
| [Dispos    | al]   |
| 0          | When disposing of this product, please follow the relevant regulations.   |
| [Others]   | -   |
| $\bigcirc$ | Do not reverse engineer this product (i.e. firmware disassembly).   |
| $\bigcirc$ | Do not modify, disassemble, repair, replace<br>parts, etc.<br>It may cause EMI failure, etc.<br>(We cannot assume responsibility for troubles<br>caused by modifying these products.)   |
| 0          | This specification does not give license of the intellectual property rights that our company owns. Also, it does not guarantee the implementation of a third party's rights.   |
| 0          | Neither whole nor partial copying of these<br>specifications is permitted without our<br>approval. If necessary, please ask for<br>assistance from one of our sales consultants.  |
| 0          | This product is designed with careful attention<br>to EMI and ESD. However, the characteristics<br>of EMI and ESD change when the product is<br>incorporated into a system. Please be sure to<br>perform testing with the finished product.<br>When used in noisy environments, please<br>take measurements against noise around the<br>casing. |
| 0          | This product is not designed for military,<br>aerospace, medical, or other life-critical<br>applications. If you choose to use this product<br>for these applications, please ask us for prior<br>consultation or we cannot accept<br>responsibility for problems that may occur.   |



Image persistence may occur if the same screen is displayed for a prolonged period of time. The effect will gradually disappear by displaying a screensaver pattern, etc, or by powering off the display. The time needed for the effect to disappear is not fixed, as it depends on the exact usage, screen settings, power settings, and environmental temperature, etc.

To avoid image persistence, it is recommended to avoid displaying a fixed pattern or the same image for a prolonged period of time.

|        | ontents    |   | •  |
|--------|------------|---|----|
| N<br>4 | otice to   | or the Cautious Handling of LCD Modules<br>General Description                |    |
| 1      | 1.1        | Construction  |    |
|        | 1.1        | Outline   |    |
|        | 1.2        | Block Diagram   |    |
| 2      | 1.5        | General Purpose I/O Port  |    |
| 23     |            | Electrical Specifications   |    |
| 3      | 3.1        | Absolute Maximum Ratings  |    |
|        | 3.2        | Electrical Ratings  |    |
|        | 3.2<br>3.3 | Electrical Characteristics  |    |
| 4      | 5.5        | Environmental Specifications  |    |
| 5      |            | Optical Specifications (at 25°C)  |    |
| 6      |            | Physical Specifications   |    |
| 7      |            | Applicable Specifications   |    |
| 8      |            | Interface Details   |    |
| Ŭ      | 8.1        | USB Interface (CN3)   |    |
|        | 8.1.       |   |    |
|        | 8.1.2      |   |    |
|        | 8.2        | Serial Interface (CN9)  |    |
|        | 8.2.       |   | 11 |
|        | 8.2.2      |   |    |
|        | 8.2.3      |   |    |
|        | 8.3        | Transmit data interface   |    |
|        | 8.4        | Reset   |    |
| 9      | -          | FLETAS Touch Panel  |    |
|        | 9.1        | Outline   |    |
|        | 9.2        | Basic Operation   | 17 |
| 1(     | )          | Installation Method   | 17 |
| 11     | l          | Setup   | 18 |
|        | 11.1       | Jumper  | 18 |
| 12     | 2          | Connectors  |    |
|        | 12.1       | USB Interface (CN3)   | 19 |
|        | 12.2       | Power Through-holes (CN8)   |    |
|        | 12.3       | Serial Interface & Power Through-holes (CN9)                                  |    |
|        | 12.4       | Extension General Purpose I/O Port Through-holes & Power Through-holes (CN10) |    |
|        | 12.5       | General Purpose I/O Port Through-holes & Power Through-holes (CN11)           |    |
|        | 12.6       | Connector Position  | 20 |
| 1:     | 3          | Lot No./ Firmware Version Specification                                       |    |
| 14     | -          | Physical Dimensions   |    |
| R      | evisior    | n history   | 23 |

# **1** General Description

This specification covers the hardware aspects of the GTWV070C3A00PA TFT-LCD graphic display module with metallized projective capacitive touch (FLETAS touch panel). This product is basically identical to the GT800X480A-C903PA, but with a replaced TFT panel.

#### 1.1 Construction

The module consists of an 800 x RGB X 480 color graphic TFT-LCD with touch panel, refresh RAM, character generator, DC/DC converter, Microcontroller, and all necessary control logic. The module can simultaneously display graphic patterns, images, and text characters on the screen.

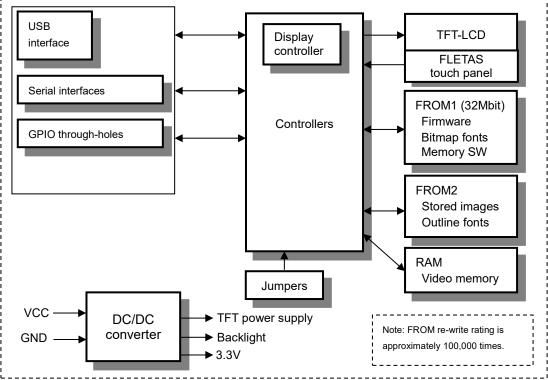
#### 1.2 Outline

| Display:      | Diagonal 7.0 inches, 800×480 pixels,                       |
|---------------|--|
|               | 65K colors (16-bit color (RGB 5-5-5 + 1bit intensity))     |
| Power supply: | Single 5 V <sub>DC</sub>                                   |
|               | Serial interface (UART / I <sup>2</sup> C / SPI CMOS 3.3V) |
|               | USB interface (High-speed / full-speed)                    |
| I/O port:     | 26 bits general-purpose I/O port                           |
| Function:     | Character display  |
|               | Graphic display  |
|               | Control command  |
|               | Display action command                                     |
|               | Window function  |
|               | General-purpose I/O port control                           |
|               | Macro, program macro function                              |
|               | Character and image download function                      |
|               | Memory SW, data storage                                    |
|               | Display orientation function                               |
|               | Power saving function                                      |
|               | Touch panel input  |

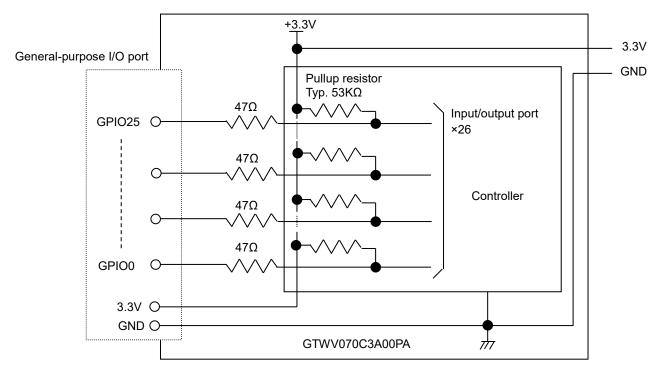
Related specifications:

DS-2007-0003-xx: "General Function" Software Specification. DS-1940-0005-xx: "Program Macro" Software Specification.

# 1.3 Block Diagram



#### **General Purpose I/O Port** 2



Refer to 3.3 Electrical Characteristics, "General Purpose I/O Port Voltage". For controlling, refer to "General Function" Software Specification, "I/O Port Input / Output Setting", "I/O Port Output", and "I/O Port Input".

#### 3 **Electrical Specifications**

# 1 Absolute Maximum Ratings Power supply voltage 3.1

| Parameter            | Symbol | Min. | Тур. | Max. | Unit | Note |
|----------------------|--------|------|------|------|------|------|
| Power supply voltage | VCC    | -0.3 | -    | +6.0 | V    | —    |

#### Logic voltage

| 9.• · • · · · · · · · · · · · · · · · · · |   |       |      |      |      |      |        |  |  |
|---|---|-------|------|------|------|------|--------|--|--|
|   | Parameter           UART         Logic input voltage           RXD, DSR |       | Min. | Тур. | Max. | Unit | Note   |  |  |
| UART                                      |   |       | -0.3 | -    | +3.6 | V    | VCC=5V |  |  |
| l <sup>2</sup> C                          | Logic input voltage<br>SCL,SDA  | VsIN2 | -0.3 | _    | +3.6 | V    | VCC=5V |  |  |
| SPI                                       | Logic input voltage<br>SCK, MOSI, SSEL                                  | VsIN3 | -0.3 | _    | +3.6 | V    | VCC=5V |  |  |
| Common                                    | Logic input voltage<br>/RESET   | VsIN4 | -0.3 | _    | +3.6 | V    | VCC=5V |  |  |
| I/O port                                  | Logic input voltage<br>port 0, 1, 2, 3                                  | VilN  | -0.3 | _    | +3.6 | V    | VCC=5V |  |  |

#### 3.2 **Electrical Ratings**

| Parameter        |                                | Symbol | Min. | Тур. | Max. | Unit | Note             |
|------------------|--------------------------------|--------|------|------|------|------|------------------|
| Power supply     | Power supply voltage           | VCC    | 4.75 | 5.0  | 5.25 | VDC  | —                |
| UART             | Logic input voltage            | VsIH1  | 2.7  | —    | —    | VDC  | —                |
| UART             | RXD, DSR                       | VsIL1  |      | —    | 0.6  | VDC  | —                |
| l <sup>2</sup> C | Logic input voltage<br>SCL,SDA | VsIH2  | 2.7  | _    | _    | VDC  | Internal pull-up |
| TC               |                                | VsIL2  | _    | —    | 0.6  | VDC  | resistor 10kΩ    |
| SPI              | Logic input voltage            | VsIH3  | 2.7  | —    | —    | VDC  | —                |
| 551              | SCK, MOSI, SSEL                |        |      | —    | 0.6  | VDC  | —                |
| RESET *1         | Logic input voltage<br>/RESET  | VsIL4  |      | _    | 0.6  | VDC  | _                |

\*1 Please use open-drain output. All driving voltage for the TFT-LCD is generated from the on-board DC/DC converter.

## 3.3 Electrical Characteristics

#### Logic input/output condition

Measuring conditions: ambient temperature = 25 °C, VCC=5.0VDC

| Parameter            |  | Symbol | Min. | Тур. | Max. | Unit | Condition  |
|----------------------|--|--------|------|------|------|------|------------|
|                      | Logic output voltage                   | VsOH1  | 2.8  | -    | -    | VDC  | IOH= 1.6mA |
| UART                 | TXD, DTR                               | VsOL1  | -    | -    | 0.4  | VDC  | IOL=-1.6mA |
| Logic                | Logic output voltage                   | VsOH2  | 2.8  | -    | -    | VDC  | IOH= 1.6mA |
| I <sup>2</sup> C I/F | TRDY, SBUSY                            | VsOL2  | -    | -    | 0.4  | VDC  | IOL=-1.6mA |
| 101/1                | Internal pull-up<br>resistance SDA,SCL | Rp     |      | 10   |      | kΩ   | —          |
|                      | Logic output voltage                   | VsOH3  | 2.8  | -    | -    | VDC  | IOH= 1.6mA |
| SPI                  | TRDY, SBUSY,<br>MISO                   | VsOL3  | -    |      | 0.4  | VDC  | IOL=-1.6mA |

#### General purpose I/O port and power 3.3V voltage

Measuring conditions: ambient temperature = 25 °C, VCC=5.0VDC

| Par             | Symbol                               | Min. | Тур. | Max. | Unit | Condition |                |  |  |
|-----------------|--------------------------------------|------|------|------|------|-----------|----------------|--|--|
|                 | Logic input current                  | lilH | -    | -    | 1.0  | μADC      | -              |  |  |
|                 | Port 0,1,2,3                         | lilL | -    | -    | -200 | μADC      | -              |  |  |
| General-purpose | Logic input voltage<br>Port 0,1,2,3  | VilH | 2.4  | -    | 3.3  | VDC       | -              |  |  |
| I/O port        |                                      | VilL | 0    | -    | 0.8  | VDC       | -              |  |  |
|                 | Logic output voltage<br>Port 0,1,2,3 | ViOH | 2.8  | -    | -    | VDC       | liOHp = -200µA |  |  |
|                 |                                      | ViOL | -    | -    | 0.4  | VDC       | liOLp = 1.6mA  |  |  |
| Power 3.3V      | Logic output voltage<br>3.3V         | VpOH | 3.0  | -    | 3.6  | VDC       | IpOHp = -20mA  |  |  |

Power 3.3V is used internal logic, so external noise injection must be avoided to prevent product malfunction.

#### General purpose I/O port and power 3.3V output permissible current

Measuring conditions: ambient temperature = 25 °C, VCC=5.0VDC

| Parameter   |                             | Symbol | Min. | Тур. | Max. | Unit | Condition            |
|---|-----------------------------|--------|------|------|------|------|----------------------|
|   | Output current port 0,1.2,3 | liOHp  | 0    | -    | -5   | mADC | -                    |
| General-purpose   | , , , ,                     | liOLp  | 0    | -    | 5    | mADC | -                    |
| I/O port<br>Output current port<br>0,1,2,3<br>(Sum of all port)<br>Power 3.3V<br>Output current 3.3V<br>(Single port) |                             | liOHt  | 0    | -    | -80  | mADC | Including power 3.3V |
|   | , , ,                       | liOLt  | 0    | -    | 80   | mADC | -                    |
|   | ІрОНр                       | 0      | -    | -20  | mADC | -    |                      |

Output current should not exceed the above values.

Please be sure to add a current limiting resistor in the output line if driving an LED or other device that may otherwise draw excessive current.

#### Power supply condition

Measuring conditions: ambient temperature = 25 °C, VCC=5.0VDC, IpOHp=0mA

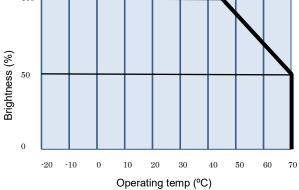
| 5                    |        |      |      |      |      |                                      |
|----------------------|--------|------|------|------|------|--------------------------------------|
| Parameter            | Symbol | Min. | Тур. | Max. | Unit | Condition                            |
|                      | ICC-1  | -    | 720  | 940  | mADC | Brightness 100%<br>All black display |
| Power supply current | ICC-2  | -    | 460  | -    | mADC | Brightness 50%<br>All white display  |
|                      | ICC-3  | -    | 230  | 300  | mADC | Display power OFF                    |
|                      | ICC-4  | -    | 65   | 95   | mADC | Sleep mode<br>Wakeup on GPIO         |
|                      | P-1    | -    | 3.6  | 4.7  | W    | Brightness 100%<br>All black display |
| Power consumption    | P-2    | -    | 2.3  | -    | W    | Brightness 50%<br>All white display  |
| 1                    | P-3    | -    | 1.2  | 1.5  | W    | Display power OFF                    |
|                      | P-4    | -    | 0.3  | 0.5  | W    | Sleep mode<br>Wakeup on GPIO         |

The rise time of supply voltage should not exceed 100ms.

Inrush current at power-on may exceed twice normal current.

# 4 Environmental Specifications

| Operating temperature:                         | −20 to +70 °C  |  |  |  |
|--|--|--|--|--|
| Storage temperature:                           | −30 to +80 °C  |  |  |  |
| Operating humidity:                            | Temp. $\leq$ 60 °C, 80% RH MAX. (No condensation)  |  |  |  |
|  | Temp. $>$ 60 °C , Absolute humidity shall be less than 80% RH at 60 °C (No condensation)                   |  |  |  |
| Storage humidity:                              | Temp. $\leq 60 ^{\circ}$ C, 80 $^{\circ}$ KH MAX. (No condensation)  |  |  |  |
|  | Temp. $> 60 ^{\circ}$ C , Absolute humidity shall be less than 80% RH at 60 $^{\circ}$ C (No condensation) |  |  |  |
| Vibration (non-operating):                     | 10-55-10Hz, all amplitude 1mm, 30 minutes, X-Y-Z   |  |  |  |
| Shock (non-operating):<br>Brightness derating: | 392m/s <sup>2</sup> (40G) 9ms X-Y-Z, 3 times each direction  |  |  |  |
|  |  |  |  |  |



# 5 Optical Specifications (at 25°C)

LCD yype: Brightness TFT with touch panel: TFT, negative, transmission

| -                                 | Typ. 425 cd/ m <sup>2</sup> (nit) |
|-----------------------------------|-----------------------------------|
| Min. 500 cd/ m <sup>2</sup> (nit) | Typ. 850 cd/ m <sup>2</sup> (nit) |
| (White screen at the c            | enter of display)                 |
| RGB                               |                                   |
| 12 o'clock                        |                                   |
| 6 o'clock                         |                                   |

50% brightness 100% brightness

Color: View direction: Gray scale inversion direction:

# 6 Physical Specifications

Number of pixels: Display area: Pixel pitch: Weight: 800 × RGB × 480 154.08mm × 85.92mm (Diagonal 7.0 inches) 0.1926mm × 0.179mm Approximately 267g

# 7 Applicable Specifications

Applicable reliability specification: TT-99-3102x Applicable production specification: TT-98-3413x

# 8 Interface Details

USB, UART, I<sup>2</sup>C, and SPI serial interfaces are included on this module. The module will accept data from any configured interface. Response data is generally transmitted via the USB interface if it is active or via the configured serial interface otherwise (refer to 8.3 Transmit data interface).

# 8.1 USB Interface (CN3)

#### 8.1.1 USB Interface – Summary

The USB interface is USB 2.0 High-speed (480Mbps) / Full-speed (12 Mbps) capable.

Typically, the supplied USB driver is used, and the display module is accessed via a "Virtual COM port" (refer to **USB VCOM Driver Installation Manual DS-1972-0000-xx**). The display module can also be used as a *WinUSB* device, using the standard *WinUSB* driver.

Alternatively, refer to USB Interface – Technical Details (below) if using the USB interface with a custom driver or embedded system, etc.

Refer to the USB 2.0 Specification for further details.

#### 8.1.2 USB Interface – Technical Details

The device has a single configuration, with a single interface, supporting two endpoints for data transfer:

| Endpoint   | Туре     | Maximum packet size                         |
|------------|----------|---|
| Endpoint 0 | Control  | 64 bytes                                    |
| Endpoint 1 | Bulk IN  | Full speed: 64 bytes, High speed: 512 bytes |
| Endpoint 2 | Bulk OUT | Full speed: 64 bytes, High speed: 512 bytes |

Vendor ID: 0EDA (hex) Product ID: 1200 (hex) Device class and interface class: FF (hex) (vendor-specific) USB serial number: Firmware F1.00 onwards: Unique serial number Prior firmware: Not supported

WinUSB compatibility:

Firmware versions F1.00 onwards support *Microsoft OS String Descriptors*, which enable automatic recognition of *WinUSB* compatibility for applicable operating systems (manual configuration, using an .inf file, is also possible). The Device Interface GUID reported by the device is {6120D798-D192-4463-B0DE-2B65ED2F4870}. This GUID is used by applications on the host in order to access the device.

#### 8.2 Serial Interface (CN9)

One of three modes is selected by jumpers J5 and J6. For all modes, the SBUSY/ DTR signal reflects the state of the receive buffer. This can be used for flow control if necessary. Data received is stored in the internal receive buffer, and processed in order of receipt. The TRDY signal indicates whether or not there is data currently held in the transmit buffer.

#### **Buffer capacity**

| Receive buffer  | 2,047 bytes |
|-----------------|-------------|
| Transmit buffer | 127 bytes   |

SBUSY / DTR signal change timing

| SBUSY change         | 'L'(READY)→'H'(BUSY) | 'H'(BUSY) →'L'(READY) |
|----------------------|----------------------|-----------------------|
| Receive buffer space | 62 bytes or less     | 128 bytes or more     |

TRDY signal change timing

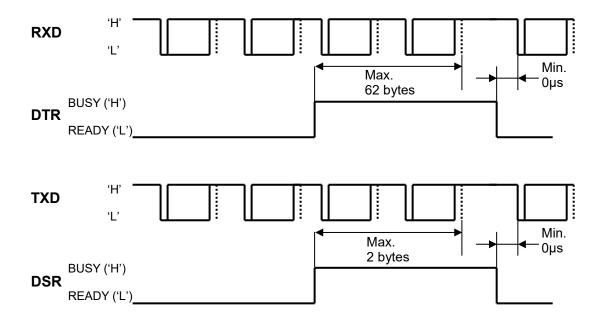
| TRDY      | READY('H')              | EMPTY('L')                 |
|-----------|-------------------------|----------------------------|
| Condition | Data in transmit buffer | No data in transmit buffer |

# 8.2.1 UART Mode (J5 = SHORT, J6 = SHORT)

Interface conditions:

| 4800 to 115200bps (set by jumper and memory SW) <b>Default setting: 38400bps</b> |
|--|
| None, even, odd (set by memory SW) Default setting: None                         |
| Start (1 bit) + data (8 bit) + parity (0 or 1 bit) + stop (1 bit)                |
| DSR, DTR, /TRDY  |
|  |

• Data transmitted from the display is placed in the internal transmit buffer and sent, in order, when DSR = READY.



#### 8.2.2 SPI Mode (J5 = OPEN, J6 = OPEN)

Display module operates as an SPI slave; data is sent and received in response to host (master) operations.

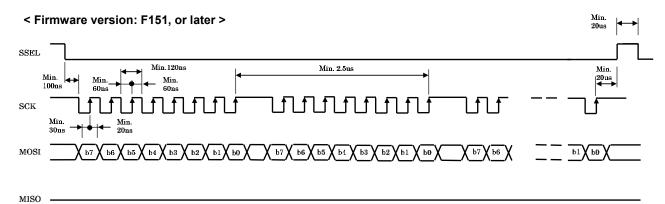
| 1st byte | Operation mode                         |
|----------|--|
| 44h      | Data write (Host $\rightarrow$ Module) |
| 54h      | Data read (Host ← Module)              |
| 58h      | Status read (Host $\leftarrow$ Module) |

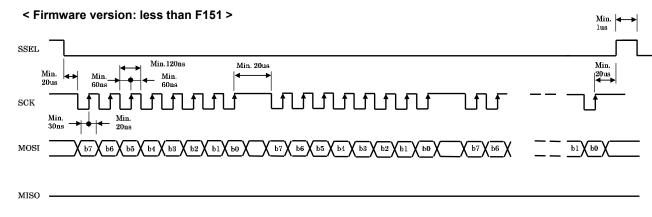
#### Data write

When 44h is input as the first byte, the module receives the 2nd and subsequent bytes as data.

| •    | 1st byte | 2nd byte | 3rd byte | <br>n byte    |
|------|----------|----------|----------|---------------|
| MOSI | 44h      | Data(1)  | Data(2)  | <br>Data(n-1) |
| MISO | -        | -        | -        | <br>-         |

#### Timing (data write)





#### Please refer to 13 Lot No./ Firmware Version Specification for the firmware version.

#### Data read

When 54h is input as the first byte, the module outputs valid data on the 3rd and subsequent bytes. The host must read the number of bytes reported by the immediately preceding Status Read command. The number of bytes reported by the status read command will be transmitted, with any unread bytes discarded.

|      | 1st byte | 2nd byte | 3rd byte | <br>n byte    |
|------|----------|----------|----------|---------------|
| MOSI | 54h      | -        | -        | <br>-         |
| MISO | -        | 00h      | Data(1)  | <br>Data(n-2) |

#### **Status Read**

When 58h is input as the first byte, the module outputs status data. For the 3rd and any subsequent bytes, the most recent status data is provided.

|      | 1st byte | 2nd byte |        | n byte |
|------|----------|----------|--------|--------|
| MOSI | 58h      | -        |        | -      |
| MISO | -        | Status   | Status | Status |

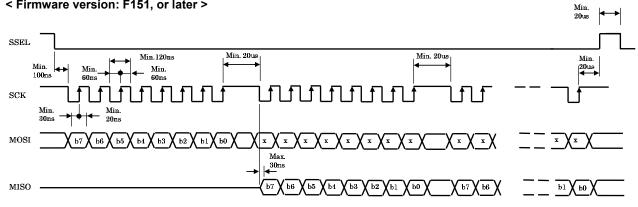
Status bit assignment

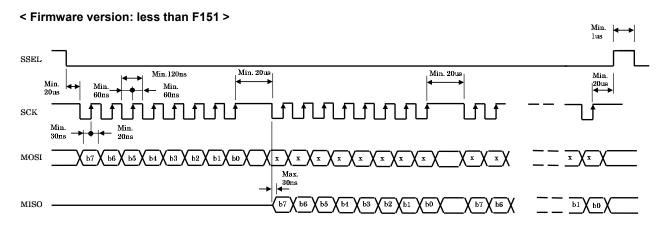
| b7    | b6 | b5        | b4     | b3     | b2     | b1     | b0     |
|-------|----|-----------|--------|--------|--------|--------|--------|
| SBUSY | 0* | TL(b5)    | TL(b4) | TL(b3) | TL(b2) | TL(b1) | TL(b0) |
| SBUSY | :  | SBUSY sig | gnal   |        |        |        |        |

Number of transmit data bytes available (maximum 63 bytes) TL \*: If b6 is 1, the status byte is invalid.

#### Timing (data read, status read)

#### < Firmware version: F151, or later >





Please refer to 13 Lot No./ Firmware Version Specification for the firmware version.

### 8.2.3 I<sup>2</sup>C Mode (J5 = SHORT, J6 = OPEN)

Slave address is set by Jumpers J0 and J1

| JO    | J1    | Function                             |
|-------|-------|--------------------------------------|
| OPEN  | OPEN  | I <sup>2</sup> C Slave Address = 50h |
| SHORT | OPEN  | I <sup>2</sup> C Slave Address = 51h |
| OPEN  | SHORT | I <sup>2</sup> C Slave Address = 52h |
| SHORT | SHORT | I <sup>2</sup> C Slave Address = 53h |

Note: Module does not respond to the General call address (00h).

#### **Communication Parameters**

| Communication                 |   |
|-------------------------------|---|
| Baud rate                     | Max. 400kbps                            |
| Format                        | l <sup>2</sup> C                        |
| Slave address                 | Set by J0 and J1 (Default setting: 50h) |
| Supported                     | ACK response, Clock stretch             |
| functions                     |   |
| Communication control signals | SBUSY, TRDY                             |

\*Note: If clock stretch is applied during processing of a command, the host (master) will not be able to send or receive any more data until command processing has finished.

If TRDY = EMPTY, FFh is transmitted from the module in response to a read sequence.

In addition to the Clock stretch function, flow can also be controlled by monitoring the MBUSY signal. MBUSY signal changes according to the receive buffer state. The host should send data when MBUSY=READY (refer to 8.2 Serial Interface (CN9)).

#### Data write sequence

| S    | SLAV | 'E ADDF | RESS | R/*W |     |    | DATA |    |     |        | DATA |    |     | D |
|------|------|---------|------|------|-----|----|------|----|-----|--------|------|----|-----|---|
| (Sr) | b7   |         | b1   | b0   | ACK | b7 |      | b0 | ACK | <br>b7 |      | b0 | ACK | Р |

#### Data read sequence

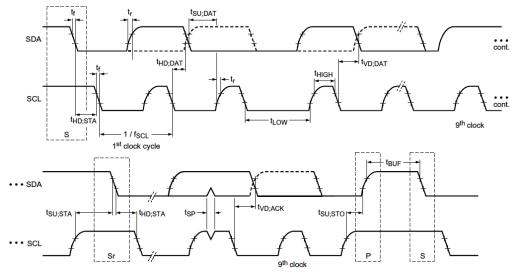
| S    | SLAV | 'E ADDF | RESS | R/*W |     |    | DATA |    |     |        | DATA |    |      | D |
|------|------|---------|------|------|-----|----|------|----|-----|--------|------|----|------|---|
| (Sr) | b7   |         | b1   | b0   | ACK | b7 |      | b0 | ACK | <br>b7 |      | b0 | NACK | Р |



Host (master) is transmitter, display module (slave) is receiver

Host (master) is receiver, display module (slave) is transmitter

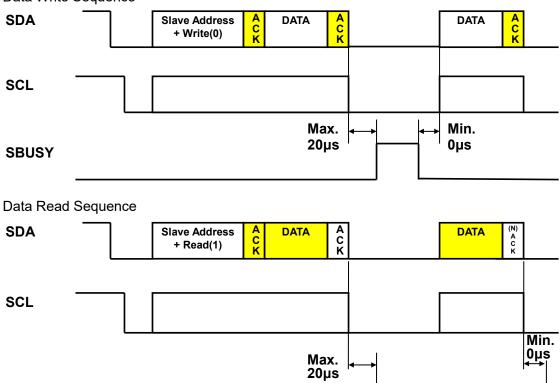
# I<sup>2</sup>C Timing



| Parameter   | Symbol              | Condition | Min. | Тур. | Max. | Unit |
|---|---------------------|-----------|------|------|------|------|
| SCL clock frequency                               | f <sub>SCL</sub>    | -         | 0    | -    | 400  | kHz  |
| Start condition hold time                         | t <sub>HD;STA</sub> | -         | 0.6  | -    | -    | μs   |
| SCL 'L' time                                      | t <sub>LOW</sub>    | -         | 1.3  | -    | -    | μs   |
| SCL 'H' time                                      | t <sub>HIGH</sub>   | -         | 0.6  | -    | -    | μs   |
| Start condition setup time                        | tsu;sta             | -         | 0.6  | -    | -    | μs   |
| Data hold time                                    | t <sub>HD;DAT</sub> | -         | 0    | -    | -    | μs   |
| Data setup time                                   | t <sub>su;dat</sub> | -         | 100  | -    | -    | ns   |
| SCL, SDA rise time                                | tr                  | -         | 20   | -    | 300  | ns   |
| SCL, SDA fall time                                | t <sub>f</sub>      | -         |      | -    | 300  | ns   |
| Stop condition setup time                         | t <sub>su;sто</sub> | -         | 0.6  | -    | -    | μs   |
| Stop condition – start condition bus<br>idle time | t <sub>BUF</sub>    | -         | 20   | -    | -    | μs   |

# Data Write Sequence

TRDY



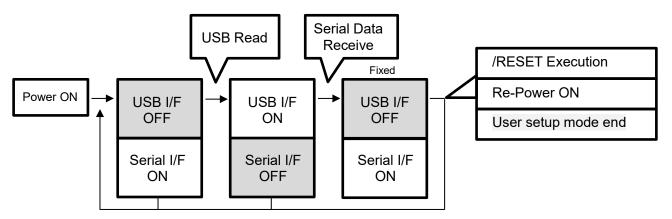
# 8.3 Transmit data interface

If the USB interface is used in addition to a Serial interface, confusion can arise over which interface data transmitted from the display module is sent via.

Initially, the configured Serial interface is selected for transmit data.

If the USB interface is active and a USB read is attempted by the host (ie, a USB bulk-IN transaction is directed at the device's Endpoint 1), the USB interface is selected for transmit data.

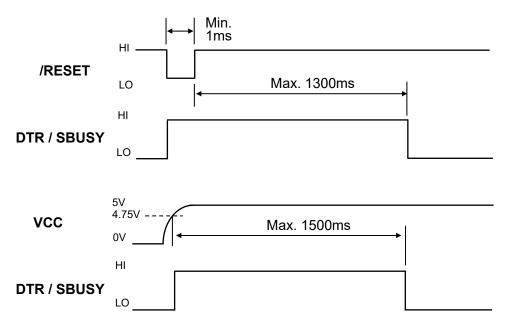
However, if data is subsequently received on the configured Serial port, the configured Serial port is then used instead, from that point onwards, for all transmit data (regardless of USB activity). This state continues until an external reset, power cycle, or "User setup mode end" command.



## 8.4 Reset

This product can be reset by inputting a LO signal to /RESET.

Note: After releasing /RESET, there is a delay, as shown below, before the module is ready to receive data. The DTR / SBUSY signal should be monitored to confirm that data is able to be received.



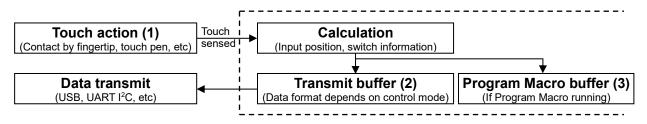
# 9 FLETAS Touch Panel

# 9.1 Outline

Detection method: MPCT (Metallised Projective Capacitive Touch) supporting multi-touch. Material: Glass

# 9.2 Basic Operation

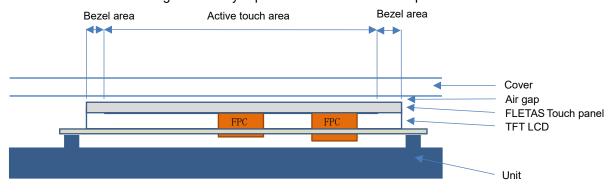
The display module features a FLETAS touch panel for handling input by fingertip or touch pen, etc. The FLETAS touch panel function sends data for the input position or operation of user-defined switch buttons.





# 10 Installation Method

FLETAS touch panels are made of glass. When using this product, please be sure to install a protective overlay such as cover glass, acrylic plate, etc. Since this touch panel is capacitive type, touch won't work if a conductive material is placed on the touch area or bezel area. Please use non-conductive material like a glass or acrylic panel as a cover. An example is shown below.



#### Installation example

- Because edges and corners are sharp of FLETAS touch panel, please be careful with installation.
- If it gives a strong shock it may cause destruction.
- Do not hold a cable (FPC) of FLETAS touch panel. Also, do not install such as to stress the cable.
- Please handle this product carefully because it is a precision part. When holding this product, please touch PCB edge, not FLETAS touch panel.

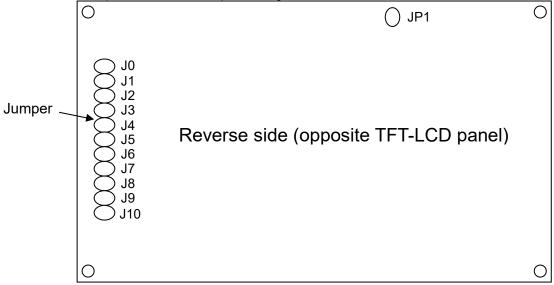
# 11 Setup

# 11.1 Jumper

| Jumper | Function                            | Default |
|--------|-------------------------------------|---------|
| JO     | I <sup>2</sup> C Slave Address      | Open    |
| J1     | I-C Slave Address                   | Open    |
| J2     | Reserved                            | Open    |
| J3     | Baud Rate                           | Open    |
| J4     |                                     | Open    |
| J5     | Serial Interface (CN9) mode         | Open    |
| J6     | Senar menace (CN9) mode             | Open    |
| J7     | Macro Start                         | Open    |
| J8     | Reserved                            | Open    |
| J9     | Reserved                            | Open    |
| J10    | FLETAS touch panel calibration mode | Open    |

Open = "H", Short = "L"

Refer to Software Specification for Jumper setting details.



# 12 Connectors

# 12.1 USB Interface (CN3)

| Pin No. | Signal name |
|---------|-------------|
| 1       | VBUS        |
| 2       | D-          |
| 3       | D+          |
| 4       | ID          |
| 5       | GND         |

Shorting JP1 enables power supply from VBUS.

# 12.2 Power Through-holes (CN8)

| Pin No. | Signal name | Function                              |
|---------|-------------|---------------------------------------|
| 1       | VCC         | Power supply (5V)                     |
| 2       | NC(*TEST)   | No connection (factory test use only) |
| 3       | GND         | Ground                                |

\* Factory use only (leave this pin open)

#### 12.3 Serial Interface & Power Through-holes (CN9)

| Pin | S      | ignal name |                  | Function          | Direction    |
|-----|--------|------------|------------------|-------------------|--------------|
| No. | UART   | SPI        | l <sup>2</sup> C | Function          | Direction    |
| 1   | /RESET | /RESET     | /RESET           | Reset             | Input        |
| 2   | -      | SCK        | SCL              | Serial clock      | Input        |
| 3   | RXD    | MOSI       | SDA              | Data receive      | Input/output |
| 4   | DTR    | SBUSY      | SBUSY            | Display busy      | Output       |
| 5   | DSR    | SSEL       | -                | Host busy         | Input        |
| 6   | TXD    | MISO       | -                | Data send         | Output       |
| 7   | TRDY   | TRDY       | TRDY             | Target ready      | Output       |
| 8   | VCC    | VCC        | VCC              | Power supply (5V) | -            |
| 9   | GND    | GND        | GND              | Ground            | -            |

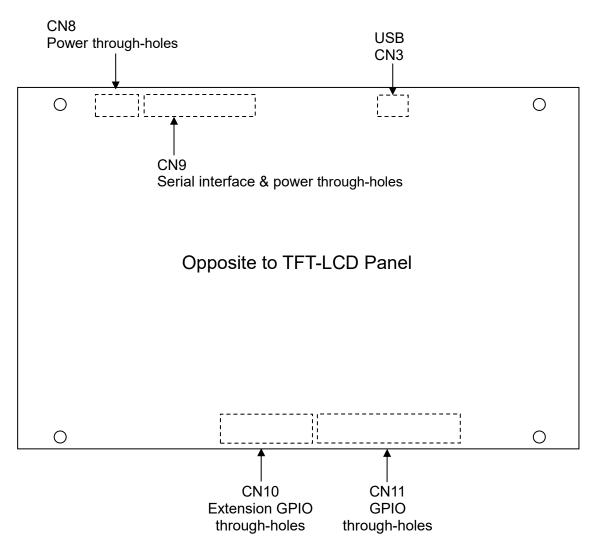
# 12.4 Extension General Purpose I/O Port Through-holes & Power Through-holes (CN10)

| Pin<br>No. | Signal<br>name | Function             | Direction    | Pin<br>No.   | Signal<br>name | Function | Direction |              |
|------------|----------------|----------------------|--------------|--------------|----------------|----------|-----------|--------------|
| 1          | VCC            | Power supply<br>(5V) | Input        | 2            | 3.3V           | 3.3V     | Output    |              |
| 3          | GPIO 25        | Port 3               |              | 4            | GPIO 24        | Port 3   |           |              |
| 5          | GPIO 23        | Port 2               | Input/output | 6            | GPIO 22        |          |           |              |
| 7          | GPIO 21        |                      |              | Input/output | 8              | GPIO 20  | Port 2    | Input/output |
| 9          | GPIO 19        |                      |              | 10           | GPIO 18        | Folt 2   |           |              |
| 11         | GPIO 17        |                      |              | 12           | GPIO 16        |          |           |              |
| 13         | GND            | Ground               | Input        | 14           | GND            | Ground   | Input     |              |

## 12.5 General Purpose I/O Port Through-holes & Power Through-holes (CN11)

| Pin<br>No. | Signal<br>name | Function             | Direction      | Pin<br>No. | Signal<br>name | Function | Direction    |
|------------|----------------|----------------------|----------------|------------|----------------|----------|--------------|
| 1          | VCC            | Power supply<br>(5V) | Input          | 2          | 3.3V           | 3.3V     | Output       |
| 3          | GPIO 15        | Port 1               | - Input/output | 4          | GPIO 14        |          |              |
| 5          | GPIO 13        |                      |                | 6          | GPIO 12        | Port 1   |              |
| 7          | GPIO 11        |                      |                | 8          | GPIO 10        | - Fort T | Input/output |
| 9          | GPIO 9         |                      |                | 10         | GPIO 8         |          |              |
| 11         | GPIO 7         | Port 0               |                | 12         | GPIO 6         |          |              |
| 13         | GPIO 5         |                      |                | 14         | GPIO 4         | Port 0   |              |
| 15         | GPIO 3         |                      |                | 16         | GPIO 2         | Forto    |              |
| 17         | GPIO 1         |                      |                | 18         | GPIO 0         |          |              |
| 19         | GND            | Ground               | Input          | 20         | GND            | Ground   | Input        |

# 12.6 Connector Position



# 13 Lot No./ Firmware Version Specification

Lot No. and firmware version are attached on the product. They are determined based on the following specifications.

Example:

Lot No. Firmware Ver.  

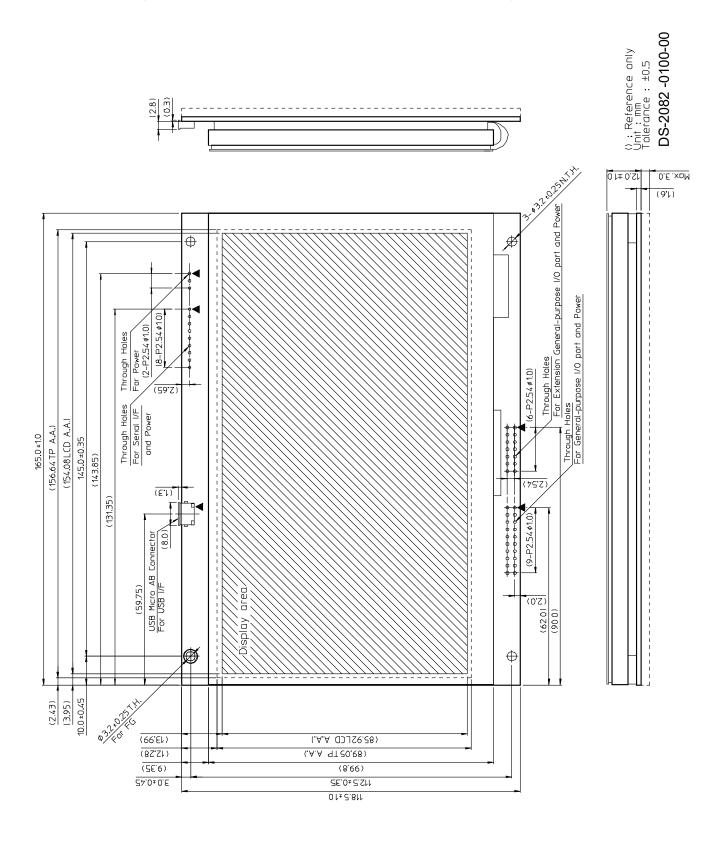
$$\frac{18C}{A} \stackrel{22}{_{B}} \stackrel{22}{_{C}} \stackrel{E}{_{D}} \stackrel{H}{_{E}} \frac{F100b}{F}$$

| A: Production year                  | The last two digits of the year are indicated.                           |
|-------------------------------------|--|
| B: Production month                 | The production month is indicated by one alphabetic character.           |
|                                     | January: A, February: B to December: L                                   |
| C: Production date                  | The production date is indicated by one or two digits.                   |
| D: Manufacturing factory            | Indicates the factory that was manufactured.                             |
|                                     | E: Omiya factory in Japan  |
| E: Identification of RoHS compliant | Indicates the identification of RoHS compliant products.                 |
|                                     | H: RoHS compliant  |
| F: Firmware version                 | Indicates the firmware version.  |
|                                     | In test mode and information read command,                               |
|                                     | F100 may be described as 1.00.   |
|                                     | Lowercase letters indicate font versions.                                |
|                                     | First edition: a, 2 <sup>nd</sup> edition: b, 3 <sup>rd</sup> edition: c |
|                                     |  |

The example above is a RoHS compliant product with firmware version 1.00b manufactured at the Omiya Plant on March 22, 2018.

# 14 Physical Dimensions

Mounting holes are close to the panel; use caution when assembling.



# **Revision history**

| Specification number | Date                           | Revision   |
|----------------------|--------------------------------|--|
| DS-2082-0001-00      | Nov. 29, 2019                  | Initial issue<br>This product is basically identical to the GT800X480A-C903PA, but with a replaced |
| DS-2082-0001-00      | Nov. 29, 2019<br>Dec. 26, 2019 |  |
|                      |                                |  |