

FEATURES

- HSPA Compliant
- 4th Generation HELP[™] technology
- High Efficiency (R99 waveform):
 - 39 % @ Pout = +28.5 dBm
 - 26 % @ Pout = +17 dBm
 - 17 % @ Pout = +13.5 dBm
 - 16 % @ Pout = +7 dBm
 - 9 % @ Pout = +3.5 dBm
- Low Quiescent Current: 3 mA
- Low Leakage Current in Shutdown Mode: <5 μA
- Internal Voltage Regulator
- Integrated "daisy chainable" directional coupler with CPLIN and CPLOUT port.
- Optimized for a 50 Ω System
- 1.8V Control Logic
- RoHS Compliant Package, 260 °C MSL-3

APPLICATIONS

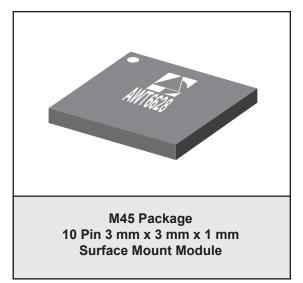
Band 8 (EGSM) WCDMA/HSPA Wireless Devices

PRODUCT DESCRIPTION

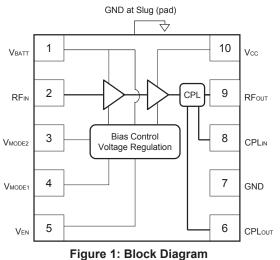
The AWT6628 HELP4[™] PA is a 4th generation HELP[™] product for UMTS850 (Band 8) devices. This PA incorporates ANADIGICS' HELP4[™] technology to deliver exceptional efficiency at low power levels and low quiescent current without the need for external voltage regulators or converters. The device is manufactured using advanced

InGaP-*Plus*[™] HBT technology offering state-of-theart reliability, temperature stability, and ruggedness. Three selectable bias modes that optimize efficiency for different output power levels and a shutdown mode with low leakage current increase handset talk and standby time. A "daisy chainable" directional coupler is integrated in the module, thus eliminating

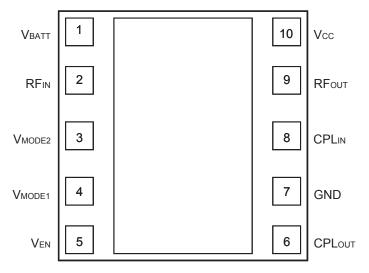
AWT6628 HELP4[™] UMTS900 (Band 8) WCDMA 28.5 dBm Linear PAM PRELIMINARY DATA SHEET - Rev 2.0

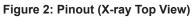


the need of an external coupler. The self-contained 3 mm x 3 mm x 1 mm surface mount package incorporates matching networks optimized for output power, efficiency, and linearity in a 50 Ω system.



AWT6628





PIN	NAME	DESCRIPTION
1	VBATT	Battery Voltage
2	RFℕ	RF Input
3	VMODE2	Mode Control Voltage 2
4	V _{MODE1}	Mode Control Voltage 1
5	Ven	PA Enable Voltage
6	CPLout	Coupler Output
7	GND	Ground
8	CPLℕ	Coupler Input
9	RFout	RF Output
10	Vcc	Supply Voltage

Table 1: Pin Description

ELECTRICAL CHARACTERISTICS

PARAMETER	MIN	MAX	UNIT				
Supply Voltage (Vcc)	0	+5	V				
Battery Voltage (VBATT)	0	+6	V				
Control Voltages (VMODE1, VMODE2, VEN)	0	+3.5	V				
RF Input Power (Pℕ)	-	+10	dBm				
Storage Temperature (T _{STG})	-40	+150	°C				

Table 2: Absolute Minimum and Maximum Ratings

Stresses in excess of the absolute ratings may cause permanent damage. Functional operation is not implied under these conditions. Exposure to absolute ratings for extended periods of time may adversely affect reliability.

PARAMETER	MIN	ТҮР	MAX	UNIT	COMMENTS
Operating Frequency (f)	880	-	915	MHz	
Supply Voltage (Vcc)	+3.2	+3.4	+4.2	V	Роит <u><</u> +28.5 dBm
Enable Voltage (V _{EN})	+1.35 0	+1.8 -	+3.1 +0.5	V	PA "on" PA "shut down"
Mode Control Voltage (VMODE1,VMODE2)	+1.35 0	+1.8 -	+3.1 +0.5	V	Low Bias Mode High Bias Mode
Output Power (UMTS) R99 WCDMA, HPM HSPA (MPR=0), HPM R99 WCDMA, MPM HSPA (MPR=0), MPM R99 WCDMA, LPM HSPA (MPR=0), LPM	28.0 ⁽¹⁾ 27.0 ⁽¹⁾ - -	28.5 27.5 17 16 7 6		dBm	3GPP TS 34.121-1, Rel 8 Table C.11.1.3, SUBTEST 1
Case Temperature (Tc)	-30	-	+90	°C	

Table 3: Operating Ranges

The device may be operated safely over these conditions; however, parametric performance is guaranteed only over the conditions defined in the electrical specifications.

Notes:

(1) For operation at 3.2 V, Pout is derated by 0.5 dB.

Table 4: Electrical Specifications - WCDMA Operation (R99 Modulation)
(Tc = +25 °C, Vcc = +3.4 V, V _{BATT} = +3.4 V, V _{EN} = +1.8 V, 50 Ω system)

		ТҮР		UNIT	COMMENTS			
PARAMETER	MIN		MAX		Ролт	V _{MODE1}	VMODE2	
Gain	24.5 14.5 10	27 17.5 12.5	30.5 21 16	dB	+28.5 dBm +17 dBm +7 dBm	0 V 1.8 V 1.8 V	0 V 0 V 1.8 V	
ACLR1 at 5 MHz offset ⁽¹⁾	- -	-41 -41 -41	-37.5 -37.5 -37.5	dBc	+28.5 dBm +17 dBm +7 dBm	0 V 1.8 V 1.8 V	0 V 0 V 1.8 V	
ACLR2 at 10 MHz offset	- - -	-57 <-60 <-60	-48 -48 -48	dBc	+28.5 dBm +17 dBm +7 dBm	0 V 1.8 V 1.8 V	0 V 0 V 1.8 V	
Power-Added Efficiency (1)	35 22 - 13 -	39 26 17 16 9.5		%	+28.5 dBm +17.0 dBm +13.5 dBm +7 dBm +3.5 dBm	0 V 1.8 V 1.8 V 1.8 V 1.8 V 1.8 V	0 V 0 V 0 V 1.8 V 1.8 V	
Quiescent Current (lcq) Low Bias Mode	-	3	4.5	mA	through Vcc pin	1.8 V	1.8 V	
Mode Control Current	-	.07	0.15	mA	through VMODE pin	IS, VMODE1,2	2 = +1.8 V	
Enable Current	-	.06	0.1	mA	through V _{EN} pin, V _{EN} = 1.8 V		V	
BATT Current	-	0.7	1.5	mA	through V _{BATT} pin, V _{MODE1,2} = +1.8 V		= +1.8 V	
Leakage Current	-	<5	10	μA	V _{BATT} = +4.2 V, V _{CC} = +4.2 V, V _{EN} = 0 V, V _{MODE1,2} = 0 V		2 V,	
Noise in Receive Band	-	-134	-	dBm/Hz	925 MHz to 960	MHz		
Harmonics 2fo 3fo, 4fo	-	-50 -55	-35 -42	dBc	Роит <u><</u> +28.5 dBi	m		
Input Impedance	-	-	2:1	VSWR				
Coupling Factor	-	20	-	dB				
Directivity	-	20	-	dB				
Coupler IN_OUT Daisy Chain Insertion Loss	-	0.35	-	dB	698 MHz to 262 Pin 6-8, Shutdov			
Spurious Output Level (all spurious outputs)	-	-	-70	dBc	Pout <u>≤</u> +28.5 dB In-band load VS Out-of-band load Applies over all	WR < 5:1 I VSWR ·	< 10:1	
Load mismatch stress with no permanent degradation or failure	8:1	-	-	VSWR	Applies over full	operatino	g range	

(1) ACLR and Efficiency measured at 897.5 MHz.

APPLICATION INFORMATION

To ensure proper performance, refer to all related Application Notes on the ANADIGICS web site: http://www.anadigics.com

Shutdown Mode

The power amplifier may be placed in a shutdown mode by applying logic low levels (see Operating Ranges table) to the VEN, VMODE1 and VMODE2 voltages.

Bias Modes

The power amplifier may be placed in either Low, Medium or High Bias modes by applying the appropriate logic level (see Operating Ranges table) to the V_{MODE} voltages. The Bias Control table below lists the recommended modes of operation for various applications.

Three operating modes are recommended to optimize current consumption. High Bias/High Power operating mode is for Pout levels \geq 17 dBm. At ~17dBm - 7 dBm, the PA should be "Mode Switched" to Medium Power Mode. For Pout levels \leq ~7 dBm, the PA can be switched to Low Power Mode for even lower quiescent current consumption.

APPLICATION	Ρουτ LEVELS	BIAS MODE	Ven	V MODE1	V MODE2	Vcc	VBATT
Low power (Low Bias Mode)	<u><</u> +7 dBm	Low	+1.8 V	+1.8 V	+1.8 V	3.2 - 4.2 V	<u>></u> 3.2 V
Med power (Medium Bias Mode)	> 7 dBm <u><</u> +17 dBm	Low	+1.8 V	+1.8 V	0 V	3.2 - 4.2 V	<u>></u> 3.2 V
High power (High Bias Mode)	> +17 dBm	High	+1.8 V	0 V	0 V	3.2 - 4.2 V	<u>></u> 3.2 V
Shutdown	-	Shutdown	0 V	0 V	0 V	3.2 - 4.2 V	<u>></u> 3.2 V

Table 5: Bias Control

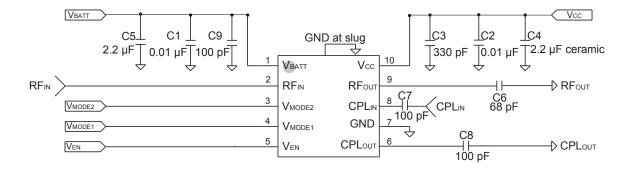
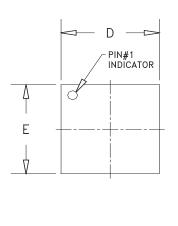
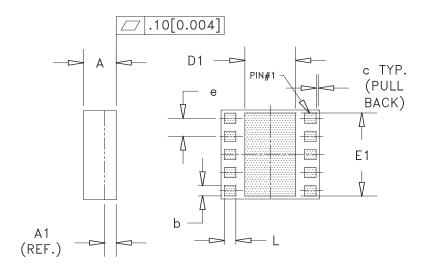


Figure 3: Evaluation Board Schematic

AWT6628

PACKAGE OUTLINE

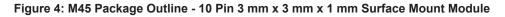




SYMBOL	MI	LLIMETER	RS	INCHES			NOTE
-0 _L	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.	
Α	0.91	1.03	1.13	0.035	0.041	0.044	-
A1	A1 LAMINATE CONTROL DRAWING						
b	0.32	0.35	0.40	0.013	0.014	0.016	3
с	-	0.10	-	-	0.004	-	-
D	2.88	3.00	3.12	0.113	0.118	0.123	-
D1	1.45	1.50	1.57	0.057	0.059	0.062	3
E	2.88	3.00	3.12	0.113	0.118	0.123	-
E1	2.70	2.75	2.85	0.106	0.108	0.112	3
е		0.60			0.024		3
L	0.32	0.35	0.40	0.013	0.014	0.016	3

NOTES:

- 1. CONTROLLING DIMENSIONS: MILLIMETERS
- CONTROLLING DIMENSIONS: MILLIMETERS
 UNLESS SPECIFIED TOLERANCE=±0.076[0.003].
 PADS (INCLUDING CENTER) SHOWN UNIFORM SIZE FOR REFERENCE ONLY. ACTUAL PAD SIZE AND LOCATION WILL VARY WITHIN MIN. AND MAX. DIMENSIONS ACCORDING TO SPECIFIC LAMINATE DESIGN.
- UNLESS SPECIFIED DIMENSIONS ARE SYMMETRICAL ABOUT CENTER LINES SHOWN. 4.
- LAMINATE CONTROL DRAWING SPECIFIED BY PART NUMBER. 5.



TOP BRAND

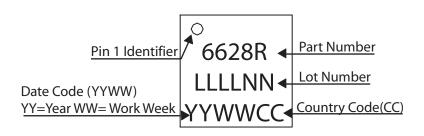
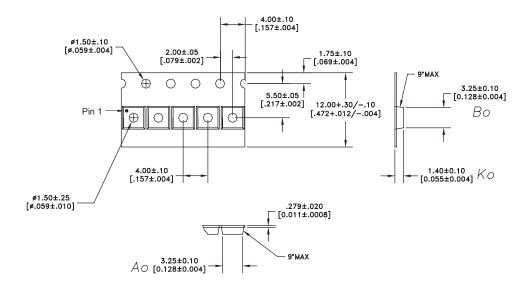


Figure 5: Branding Specification - M45 Package

COMPONENT PACKAGING



NOTES:

1. MATERIAL: 3000 (CARBON FILLED POLYCARBONATE) 100% RECYCLABLE. DIMENSIONS ARE IN MILLIMETERS [INCHES]

DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994



Table	6:	Таре	&	Reel	Dimensions
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PACKAGE TYPE	TAPE WIDTH	POCKET PITCH	REEL CAPACITY	MAX REEL DIA
3 mm x 3 mm x 1 mm	12 mm	4 mm	2500	7"

ORDERING INFORMATION

ORDER NUMBER	TEMPERATURE RANGE	PACKAGE DESCRIPTION	COMPONENT PACKAGING
AWT6628RM45Q7	-30 °C to +90 °C	RoHS Compliant 10 Pin 3 mm x 3 mm x 1 mm Surface Mount Module	Tape and Reel, 2500 pieces per Reel
AWT6628RM45P9	-30 °C to +90 °C	RoHS Compliant 10 Pin 3 mm x 3 mm x 1 mm Surface Mount Module	Partial Tape and Reel

ANADIGICS

ANADIGICS, Inc.

141 Mount Bethel Road Warren, New Jersey 07059, U.S.A. Tel: +1 (908) 668-5000 Fax: +1 (908) 668-5132

URL: http://www.anadigics.com

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