SQM10250E

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Automotive N-Channel 250 V (D-S) 175 °C MOSFET

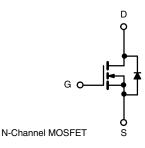


PRODUCT SUMMARY					
V _{DS} (V)	250				
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 V$	0.0300				
$R_{DS(on)}(\Omega)$ at $V_{GS} = 7.5 V$	0.0320				
I _D (A)	65				
Configuration	Single				
Package	TO-263				

FEATURES

- TrenchFET® power MOSFET
- · Package with low thermal resistance
- AEC-Q101 qualified
- 100 % $\rm R_g$ and UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>





ABSOLUTE MAXIMUM RATINGS ($T_c = 25 \text{ °C}$, unless otherwise noted)						
PARAMETER		SYMBOL	LIMIT	UNIT		
Drain-source voltage		V _{DS}	250	v		
Gate-source voltage		V _{GS}	± 20	V		
Continuous drain current	T _C = 25 °C	1	65			
	T _C = 125 °C	I _D	37			
Continuous source current (diode conduction) ^a		I _S	120	А		
Pulsed drain current ^b		I _{DM}	180			
Single pulse avalanche current	L = 0.1 mH	I _{AS}	41			
Single pulse avalanche energy	L = 0.1 IIIH	E _{AS}	84	mJ		
Maximum power dissipation ^b	T _C = 25 °C	- P _D	375	w		
	T _C = 125 °C		125	vv v		
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +175	°C		

THERMAL RESISTANCE RATINGS						
PARAMETER		SYMBOL	LIMIT	UNIT		
Junction-to-ambient	PCB mount ^c	R _{thJA}	40	°C/W		
Junction-to-case (drain)		R _{thJC}	0.4	0/11		

Notes

a. Package limited

b. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %

c. When mounted on 1" square PCB (FR4 material)

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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static					•			
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 V, I_D = 250 \mu A$		250	-	-	v	
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$		2.5	3.0	3.5	v	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA	
		$V_{GS} = 0 V$	V _{GS} = 0 V V _{DS} = 250 V		-	1		
Zero gate voltage drain current	I _{DSS}	$V_{GS} = 0 V$	V _{DS} = 250 V, T _J = 125 °C	-	-	50	μA	
		$V_{GS} = 0 V$	V _{DS} = 250 V, T _J = 175 °C	-	-	600		
On-state drain current ^a	I _{D(on)}	$V_{GS} = 10 V$	$V_{DS} \ge 5 V$	30	-	-	Α	
		V _{GS} = 10 V	I _D = 15 A	-	0.0244	0.0300	1	
Durin country on state unsistence 2	P	$V_{GS} = 7.5 V$	I _D = 10 A	-	0.0260	0.0320	Ω	
Drain-source on-state resistance ^a	R _{DS(on)}	$V_{GS} = 10 V$	I _D = 15 A, T _J = 125 °C	-	-	0.0650		
		$V_{GS} = 10 V$	I _D = 15 A, T _J = 175 °C	-	-	0.0868		
Forward transconductance b	9 _{fs}	V _{DS} = 15 V, I _D = 15 A		-	50	-	S	
Dynamic ^b	•	•			•	•		
Input capacitance	C _{iss}			-	2880	4050		
Output capacitance	C _{oss}	$V_{GS} = 0 V$	V _{GS} = 0 V V _{DS} = 25 V, f = 1 MHz		1480	2100	pF	
Reverse transfer capacitance	C _{rss}			-	58	85		
Total gate charge ^c	Qg			-	50	75		
Gate-source charge ^c	Q _{gs}	$V_{GS} = 10 V$	$V_{DS} = 125 \text{ V}, I_{D} = 10 \text{ A}$	-	12	-	nC	
Gate-drain charge ^c	Q _{gd}			-	15	-		
Gate Resistance	R _g	f = 1 MHz		1.40	2.84	4.40	Ω	
Turn-on delay time ^c	t _{d(on)}			-	14	30		
Rise time ^c	tr	V _{DD} =	$\label{eq:VDD} \begin{array}{l} V_{\text{DD}} = 125 \text{ V}, \ R_{\text{L}} = 12.5 \ \Omega \\ I_{\text{D}} \cong 10 \ A, \ V_{\text{GEN}} = 10 \ V, \ R_{\text{g}} = 1 \ \Omega \end{array}$		6	15	ns	
Turn-off delay time ^c	t _{d(off)}	I _D ≅ 10 A,			38	60		
Fall time ^c	t _f			-	10	20		
Source-Drain Diode Ratings and Char	acteristics ^b	•			•	•		
Pulsed current ^a	I _{SM}			-	-	180	Α	
Forward voltage	V _{SD}	$I_F = 20 \text{ A}, V_{GS} = 0 \text{ V}$		-	0.82	1.5	V	
Body diode reverse recovery time	t _{rr}	I _F = 10 A, di/dt = 100 A/μs		-	155	260	ns	
Body diode reverse recovery charge	Q _{rr}			-	933	1400	nC	
Reverse recovery fall time	t _a			-	122	-	ns	
Reverse recovery rise time	t _b			-	33	-		
Body diode peak reverse recovery current	I _{RM(REC)}			-	-11.6	-	А	

Notes

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %

b. Guaranteed by design, not subject to production testing

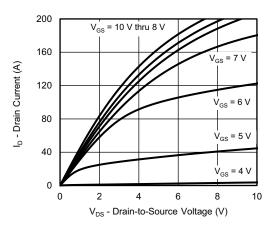
c. Independent of operating temperature

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

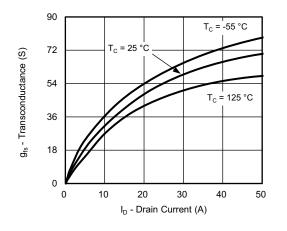
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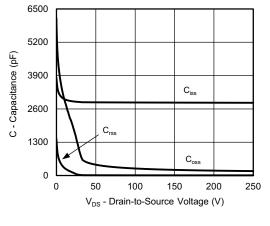
TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)



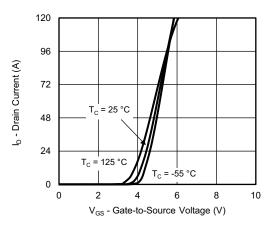
Output Characteristics



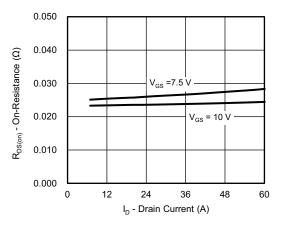
Transconductance



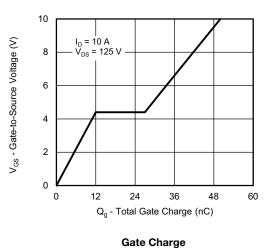
Capacitance



Transfer Characteristics



On-Resistance vs. Drain Current



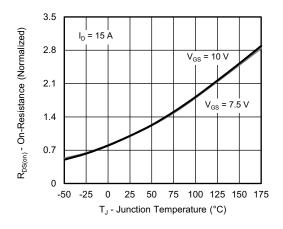
S18-0341-Rev. A, 26-Mar-18

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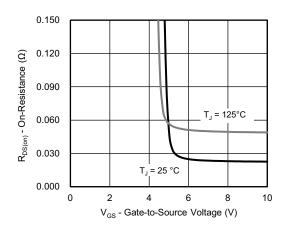
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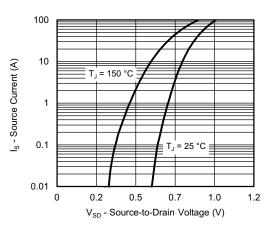
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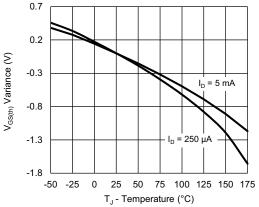
On-Resistance vs. Junction Temperature

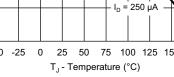


On-Resistance vs. Gate-to-Source Voltage

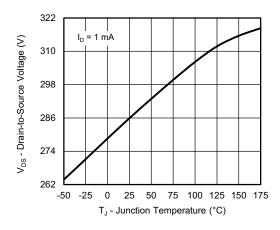


Source Drain Diode Forward Voltage









Drain Source Breakdown vs. Junction Temperature

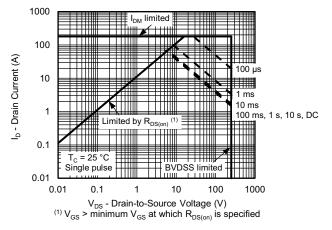
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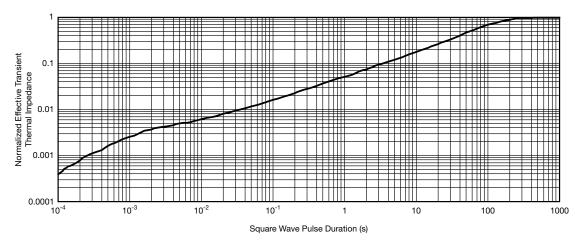
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THERMAL RATINGS ($T_A = 25 \text{ °C}$, unless otherwise noted)



Safe Operating Area

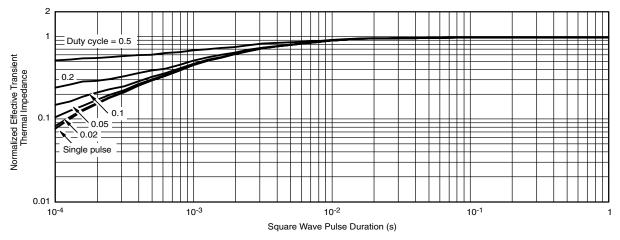


Normalized Thermal Transient Impedance, Junction-to-Ambient



Document Number: 75035

THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Case

Note

• The characteristics shown in the two graphs

S18-0341-Rev. A, 26-Mar-18

- Normalized Transient Thermal Impedance Junction to Ambient (25 °C)

- Normalized Transient Thermal Impedance Junction to Case (25 °C)

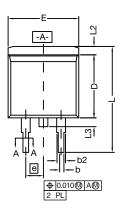
are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

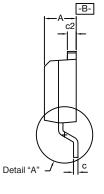
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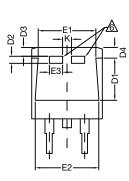
6



TO-263 (D²PAK): 3-LEAD

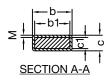








DETAIL A (ROTATED 90°)



		INCHES		MILLIMETERS		
DIM.		MIN.	MAX.	MIN.	MAX.	
А		0.160	0.190	4.064	4.826	
	b	0.020	0.039	0.508	0.990	
	b1	0.020	0.035	0.508	0.889	
	b2	0.045	0.055	1.143	1.397	
С*	Thin lead	0.013	0.018	0.330	0.457	
	Thick lead	0.023	0.028	0.584	0.711	
c1	Thin lead	0.013	0.017	0.330	0.431	
	Thick lead	0.023	0.027	0.584	0.685	
	c2	0.045	0.055	1.143	1.397	
	D	0.340	0.380	8.636	9.652	
	D1	0.220	0.240	5.588	6.096	
	D2	0.038	0.042	0.965	1.067	
D3		0.045	0.055	1.143	1.397	
	D4	0.044	0.052	1.118	1.321	
	E	0.380	0.410	9.652	10.414	
	E1	0.245	-	6.223	-	
E2		0.355	0.375	9.017	9.525	
	E3	0.072	0.078	1.829	1.981	
	е	0.100 BSC		2.54 BSC		
	К	0.045	0.055	1.143	1.397	
L		0.575	0.625	14.605	15.875	
L1		0.090	0.110	2.286	2.794	
L2		0.040	0.055	1.016	1.397	
L3		0.050	0.070	1.270	1.778	
	L4	0.010 BSC		0.254 BSC		
	M - 0.002 -		0.050			
ECN: T13-0707-Rev. K, 30-Sep-13 DWG: 5843						

Notes

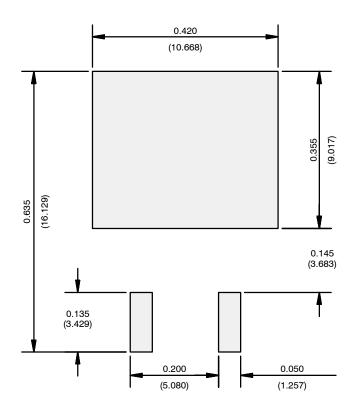
- 1. Plane B includes maximum features of heat sink tab and plastic. 2. No more than 25 $\,\%\,$ of L1 can fall above seating plane by
- max. 8 mils. 3. Pin-to-pin coplanarity max. 4 mils.
- 4. *: Thin lead is for SUB, SYB.
 - Thick lead is for SUM, SYM, SQM.
- 5. Use inches as the primary measurement.

This feature is for thick lead.

Revison: 30-Sep-13



RECOMMENDED MINIMUM PADS FOR D²PAK: 3-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

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