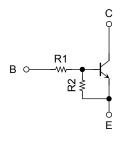
TOSHIBA Transistor Silicon NPN Epitaxial Type (PCT process) (Bias Resistor built-in Transistor)

# RN1101CT, RN1102CT, RN1103CT RN1104CT, RN1105CT, RN1106CT

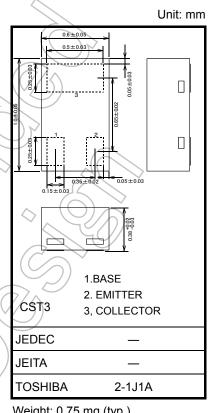
Switching Applications **Inverter Circuit Applications** Interface Circuit Applications **Driver Circuit Applications** 

- Incorporating a bias resistor into a transistor reduces parts count. Reducing the parts count enable the manufacture of ever more compact equipment and save assembly cost.
- Complementary to RN2101CT to RN2106CT

#### **Equivalent Circuit and Bias Resistor Values**



Type No.	R1 (kΩ)	R2 (kΩ)
RN1101CT	4.7	4.7
RN1102CT	10	10
RN1103CT	22	22
RN1104CT	47	47
RN1105CT	2.2	47
RN1106CT	4.7	47



Weight: 0.75 mg (typ.)

### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Collector-base voltage	RN1101CT to 1106CT	V <sub>CBO</sub>	20	V	
Collector-emitter voltage	KINTOTOT TO TTOOCT	VCEO	20	V	
Emitter-base voltage	RN1101CT to 1104CT	Veno	10	V	
	RN1105CT, 1106CT	V <sub>EBO</sub>	5		
Collector current		IC	50	mA	
Collector power dissipation	RN1101CT ro 1106CT	PC	50	mW	
Junction temperature	NIVITOTO TOOCT	Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e.operatingtemperature/current/voltage, etc.) are within the absolute maximum ratings.

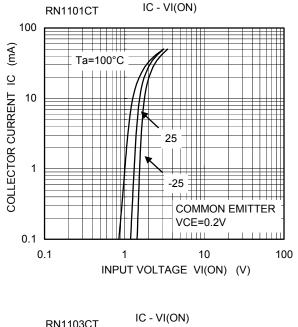
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

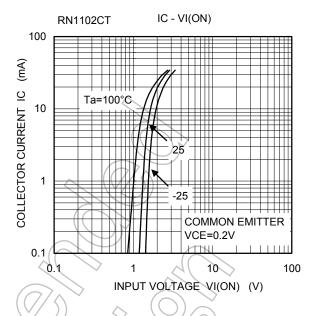
> Start of commercial production 2004-10

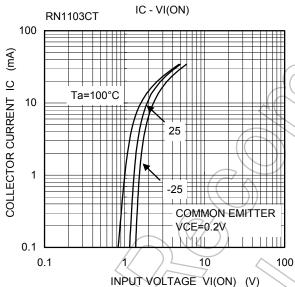


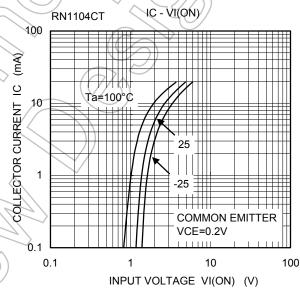
## Electrical Characteristics (Ta = 25°C)

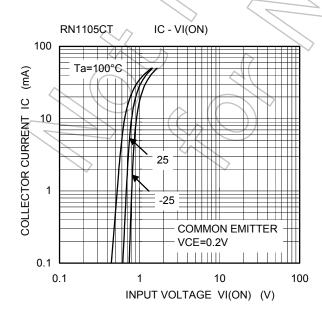
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	RN1101CT to 1106CT	I <sub>CBO</sub>	$V_{CB} = 20 \text{ V}, I_E = 0$	_	_	100	nA
		I <sub>CEO</sub>	$V_{CE} = 20 \text{ V}, I_B = 0$	_	_	500	11/
Emitter cut-off current	RN1101CT		V <sub>EB</sub> = 10 V, I <sub>C</sub> = 0	0.89	_	1.33	- mA
	RN1102CT			0.41	_	0.63	
	RN1103CT	IFDO		0.18	) /_	0.29	
	RN1104CT	lebo		0.088	_	0.133	
	RN1105CT			0.085	_	0.127	
	RN1106CT		VEB = 5 V, IC = 0	0.08	_	0.121	
DC current gain	RN1101CT			30	_	_	
	RN1102CT			60			
	RN1103CT	h <sub>FE</sub>		100	74	$\nearrow$	
	RN1104CT		$V_{CE} = 5 \text{ V, } I_{C} = 10 \text{ mA}$	120	7-//	> —	
	RN1105CT			120	7 <i>F</i>	) —	
	RN1106CT			120	90	_	
Collector-emitter saturation voltage	RN1101CT to 1106CT	V <sub>CE</sub> (sat)	$I_C = 5 \text{ mA}, I_B = 0.25 \text{ mA}$		_	0.15	V
	RN1101CT		$V_{CE} = 0.2 \text{ V}, I_{C} = 5 \text{ mA}$	1.0	_	2.0	V
	RN1102CT	7		1.0	_	2.2	
Input voltage (ON)	RN1103CT	V <sub>L</sub> (ON)		1.1	_	2.7	
Input voltage (ON)	RN1104CT			1.2	_	3.6	
	RN1105CT	))		0.6	_	1.1	
	RN1106CT			0.6	_	1.2	
Input voltage (OFF)	RN1101CT to 1104CT	VI. (055)	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 0.1 mA	0.8	_	1.5	\/
	RN1105CT, 1106CT	V <sub>I</sub> (OFF)	VGE - 3 V, IC - 0.1 IIIA	0.4	_	0.8	V
Collector output capacitance	RN1101CT to 1106CT	Cob	$V_{CB} = 10 \text{ V}, I_E = 0,$ f = 1 MHz	_	1.2	_	pF
Input resistor	RN1101CT	R1	_	3.76	4.7	5.64	kΩ
	RN1102CT			8	10	12	
	RN1103CT			17.6	22	26.4	
	RN1104CT			37.6	47	56.4	
	RN1105CT			1.76	2.2	2.64	
	RN1106CT			3.76	4.7	5.64	
	RN1101CT to 1104CT			0.8	1.0	1.2	
Resistor ratio	RN1105CT	R1/R2	_	0.0376	0.0468	0.0562	
	RN1106CT	1		0.08	0.1	0.12	

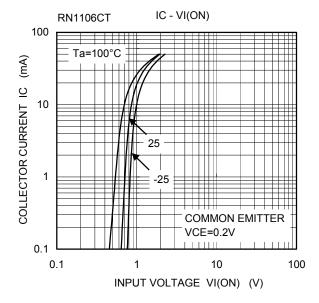




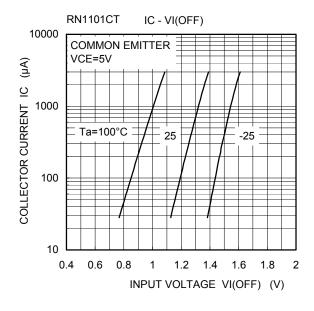


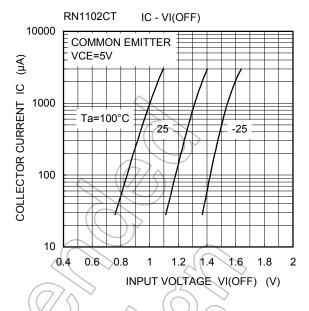


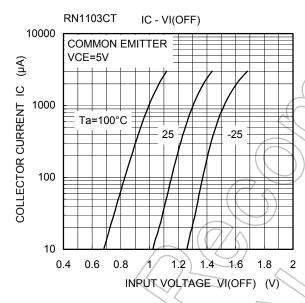


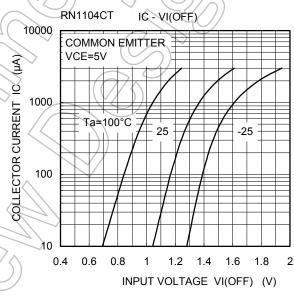


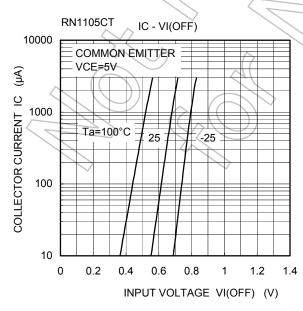
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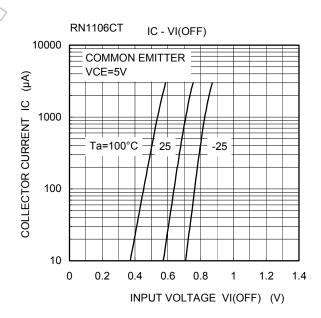


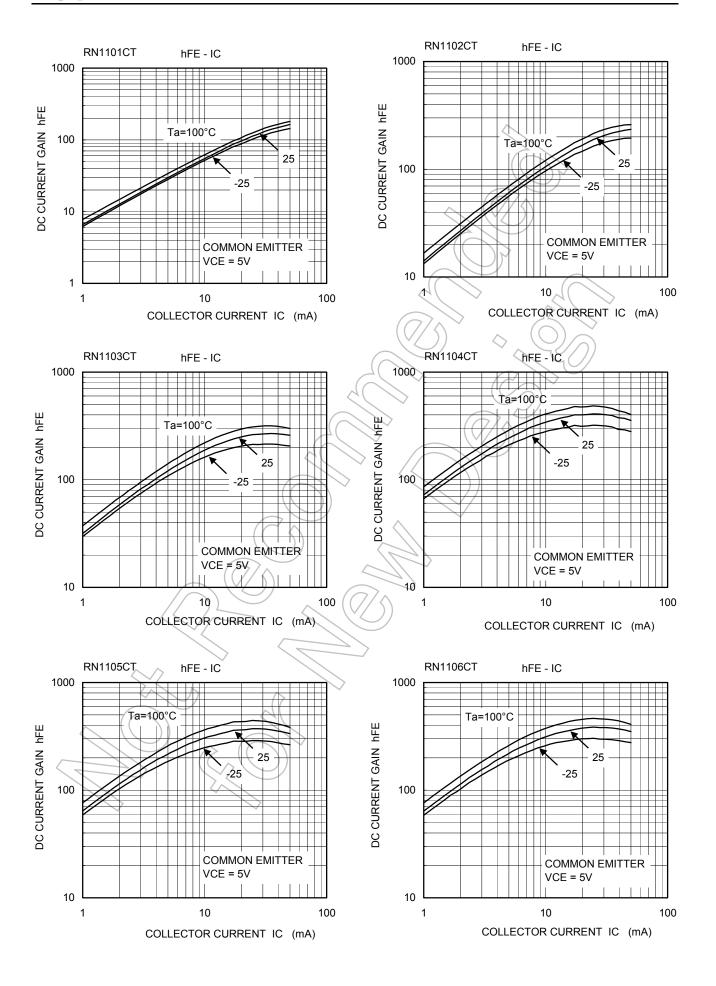


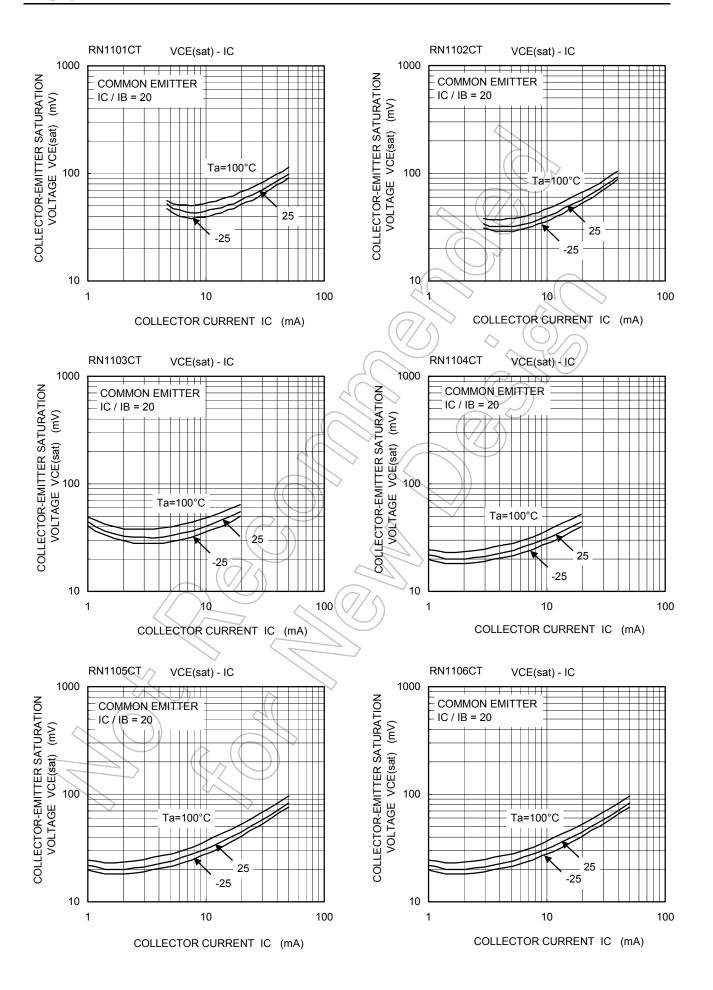


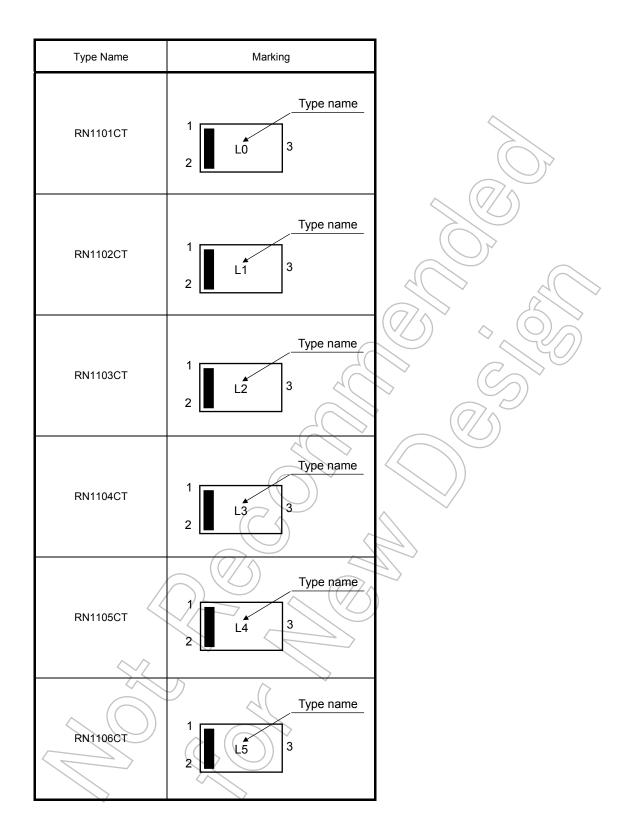












## **Handling Precaution**

When handling individual devices (which are not yet mounted on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

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