



DMN2056U

20V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(on)} max	I _D max T _A = +25°C
	38mΩ @ V _{GS} = 4.5V	4.0A
20V	45mΩ @ V _{GS} = 2.5V	3.7A

Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- · Fast Switching Speed
- Low Input/Output Leakage
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at

https://www.diodes.com/products/automotive/automotive-products/.

 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

https://www.diodes.com/quality/product-definitions/

Description and Applications

This MOSFET is designed to minimize the on-state resistance $(R_{DS(on)})$ and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

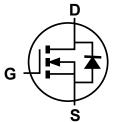
- Battery Charging
- Power Management Functions
- DC-DC Converters
- Portable Power Adaptors

Mechanical Data

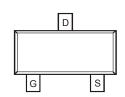
- Case: SOT23 (Standard)
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 3
- Terminals Connections: See Diagram Below
- Weight: 0.009 grams (Approximate)



Top View



Internal Schematic



Top View

Ordering Information (Note 4)

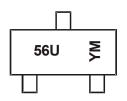
Part Number	Case	Packaging
DMN2056U-7	SOT23 (Standard)	3000/Tape & Reel
DMN2056U-13	SOT23 (Standard)	10000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Marking Information



56U = Product Type Marking Code YM = Date Code Marking Y or Y = Year (ex: I = 2021) M = Month (ex: 9 = September)

Date Code Key

Date Code Hoj												
Year	2016		2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	D		ı	J	K	L	М	N	0	Р	R	S
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Maximum Ratings (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Drain-Source Voltage	V_{DSS}	20	V	
Gate-Source Voltage		V_{GSS}	±8	V
Continuous Drain Current (Note 6) V _{GS} = 4.5V	I _D	4.0 3.2	Α	
Maximum Body Diode Forward Current (Note 6)	Is	1.0	Α	
Pulsed Drain Current (10µs pulse, duty cycle = 1%)		I _{DM}	22	Α

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Power Dissipation (Note 5)		P _D	0.66	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	192	°C/W
Power Dissipation (Note 6)		P _D	0.94	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{θJA}	136	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C



Electrical Characteristics (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)	•		•			
Drain-Source Breakdown Voltage	BV _{DSS}	20	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μΑ	V _{DS} = 20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 8V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(th)}	0.4	0.6	1.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$
		_	30	38		V _{GS} = 4.5V, I _D = 3.6A
Static Drain-Source On-Resistance	R _{DS(on)}	_	34	45	mΩ	$V_{GS} = 2.5V, I_D = 3.1A$
		_	52	85		V _{GS} = 1.5V, I _D = 2.0A
Diode Forward Voltage	V _{SD}	_	0.7	1.2	V	V _{GS} = 0V, I _S = 1A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	_	339	_		
Output Capacitance	Coss	_	47	_	pF	$V_{DS} = 10V, V_{GS} = 0V$ f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}	_	34	_		1 – 1.0001112
Gate Resistance	R_G	_	2.6	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge	Qg	_	4.3	_		
Gate-Source Charge	Q _{gs}	_	0.5	_	nC	V_{DS} = 10V, V_{GS} = 4.5V, I_{D} = 3.6A
Gate-Drain Charge	Q_{gd}	_	0.8	_		
Turn-On Delay Time	t _{D(on)}	_	1.8	_		
Turn-On Rise Time	t _R	_	2.8	_		$V_{GS} = 4.5V, V_{DD} = 10V, R_G = 1\Omega,$
Turn-Off Delay Time	t _{D(off)}	_	8.5	_	ns	I _D = 3.6A
Turn-Off Fall Time	t _F	_	1.7			
Body Diode Reverse Recovery Time	t _{RR}	_	4.7	_	ns	I _F = 3.6A, dl/dt = 100A/µs
Body Diode Reverse Recovery Charge	Q_{RR}		0.7		nC	I _F = 3.6A, dl/dt = 100A/µs

Notes:

- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.



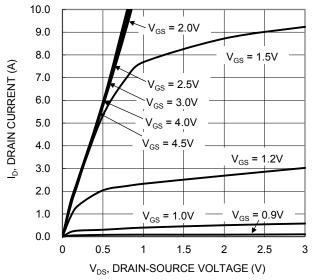


Figure 1. Typical Output Characteristic

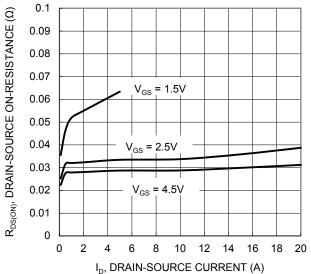


Figure 3. Typical On-Resistance vs Drain Current and Gate Voltage

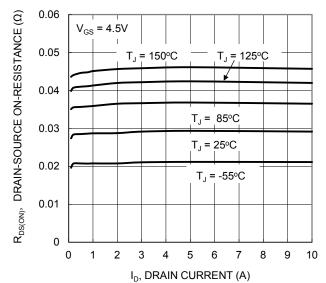
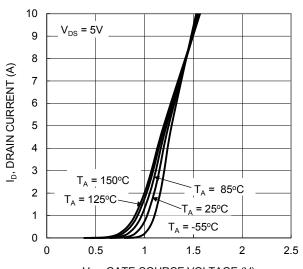


Figure 5. Typical On-Resistance vs Drain Current and Junction Temperature



V_{GS}, GATE-SOURCE VOLTAGE (V) Figure 2. Typical Transfer Characteristic

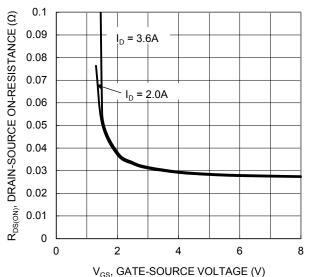


Figure 4. Typical Transfer Characteristic

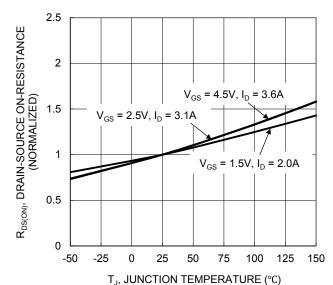


Figure 6. On-Resistance Variation with Junction Temperature





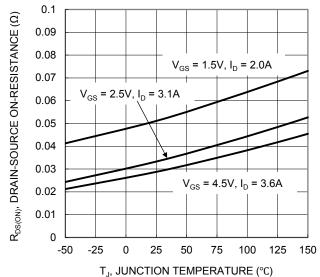


Figure 7. On-Resistance Variation with Junction Temperature

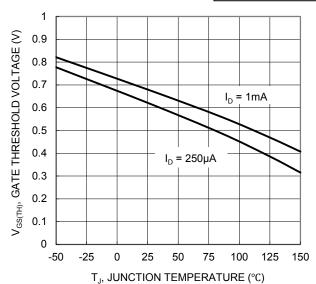
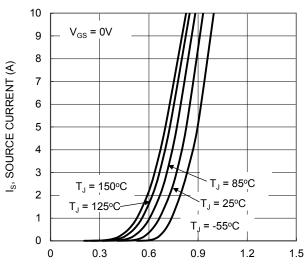
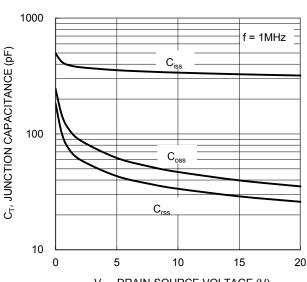


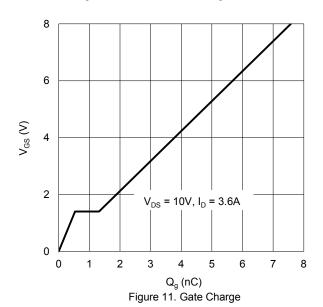
Figure 8. Gate Threshold Variation vs Junction Temperature



V_{SD}, SOURCE-DRAIN VOLTAGE (V) Figure 9. Diode Forward Voltage vs Current



V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 10. Typical Junction Capacitance



R_{DS(ON)} Limited $P_W = 1 ms$ = 10ms = 100µs 10 ID, DRAIN CURRENT (A) $P_W = 1s$ T_{J(Max)} = 150°C 0.1 $T_C = 25^{\circ}C$ P_w = 100ms Single Pulse DUT on 1*MRP Board $V_{GS} = 4.5V$ 0.01 0.1 100 V_{DS}, DRAIN-SOURCE VOLTAGE (V)

100

July 2021



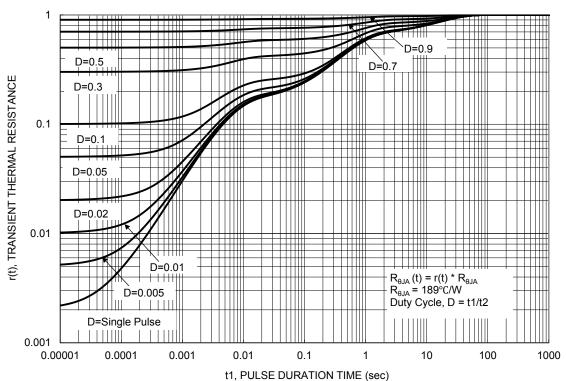


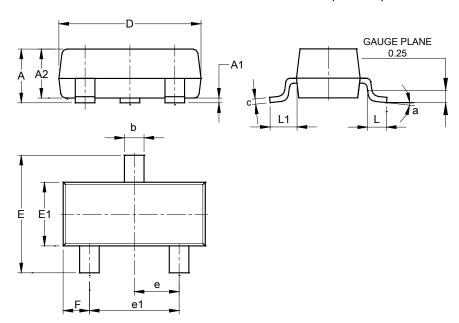
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23 (Standard)

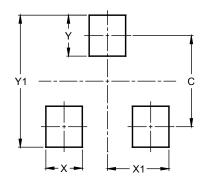


SOT23 (Standard)								
Dim	Min	Max	Тур					
Α	0.90	1.15	1.025					
A1	0.00	0.10	0.05					
A2	0.85	1.10	0.975					
b	0.30	0.51	0.40					
С	0.080	0.202	0.11					
D	2.80	3.00	2.90					
Е	2.25	2.55	2.40					
E1	1.20	1.40	1.30					
е	0.89	1.03	0.915					
e1	1.78	2.05	1.83					
F	0.40	0.60	0.535					
L1	0.45	0.61	0.55					
L	0.25	0.55	0.40					
а	0°	8°						
All Dimensions in mm								

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23 (Standard)



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	29



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