

40 V, 0.5 A low VF MEGA Schottky barrier rectifier

12 May 2016

Product data sheet

1. General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier with an integrated guard ring for stress protection, encapsulated in an SOD323F (SC-90) very small and flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- Average forward current: $I_{F(AV)} \le 0.5 \text{ A}$
- Reverse voltage: V_R ≤ 40 V
- Low forward voltage typ. V_F = 550 mV
- Low reverse current typ. I_R = 1.5 μA
- · Very small and flat lead SMD plastic package
- AEC-Q101 qualified

3. Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch Mode Power Supply (SMPS)
- Reverse polarity protection
- Low power consumption applications
- Automotive applications

4. Quick reference data

Table 1. Quick reference dat	Table 1	1.	Quick	reference	data
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Table 1. Quick						
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{F(AV)}	average forward current	δ = 0.5 ; f = 20 kHz; $T_{sp} \leq ~135 \ ^{\circ}\text{C};$ square wave	-	-	0.5	A
V _R	reverse voltage	T _j = 25 °C	-	-	40	V
V _F	forward voltage	I_{F} = 500 mA; t_{p} \leq 300 $\mu\text{s};$ δ \leq 0.02 $;$ T_{j} = 25 °C	-	550	640	mV
I _R	reverse current	V_R = 40 V; pulsed; T_j = 25 °C	-	1.5	8	μA
		V_R = 40 V; pulsed; T_j = 125 °C	-	1	8	mA

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5. Pinning information

Table 2. Pinning information							
Pin	Symbol	Description	Simplified outline	Graphic symbol			
1	К	cathode	1 2	1 - 1 - 2			
2	A	anode	SOD323F	sym001			

6. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
PMEG4005CEJ	SOD323F	plastic surface-mounted package; 2 leads	SOD323F			

7. Marking

Table 4. Marking codes

Type number	Marking code
PMEG4005CEJ	2F

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8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _R	reverse voltage	T _j = 25 °C		-	40	V
I _F	forward current	T _{sp} ≤ 130 °C; δ = 1		-	0.7	А
I _{F(AV)}	average forward current	δ = 0.5 $~;$ f = 20 kHz; $T_{sp} \leq ~135 ~^\circ\text{C};$ square wave		-	0.5	A
I _{FRM}	repetitive peak forward current	t _p ≤ 1 ms; δ ≤ 0.25		-	2	A
I _{FSM}	non-repetitive peak forward current	t_p = 8 ms; $T_{j(init)}$ = 25 °C; square wave		-	8	A
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	415	mW
			[2]	-	715	mW
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

9. Thermal characteristics

Table 6. Thermal characteristics

	ai characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
	thermal resistance from junction to ambient	-	[1][2]	-	-	300	K/W
			[1][3]	-	-	175	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point		[4]	-	-	45	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses P_R are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

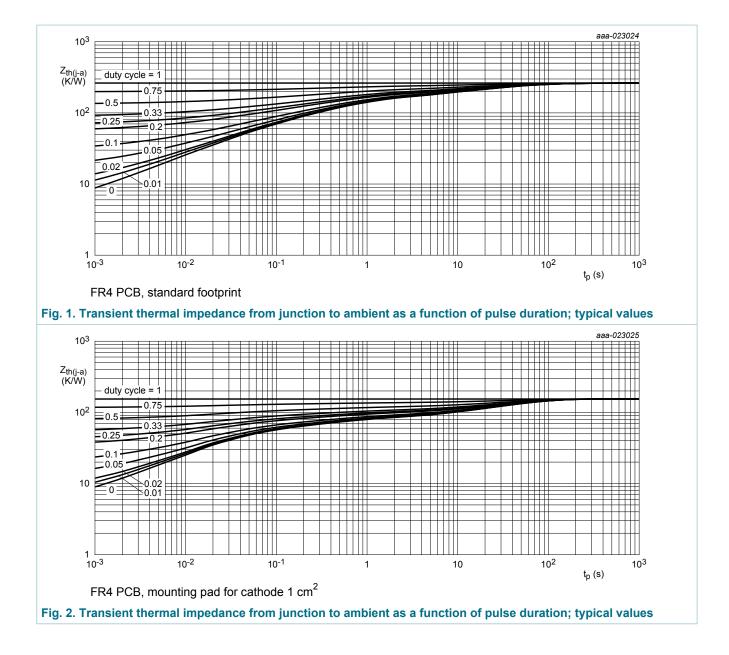
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm².

[4] Soldering point of cathode tab.

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PMEG4005CEJ

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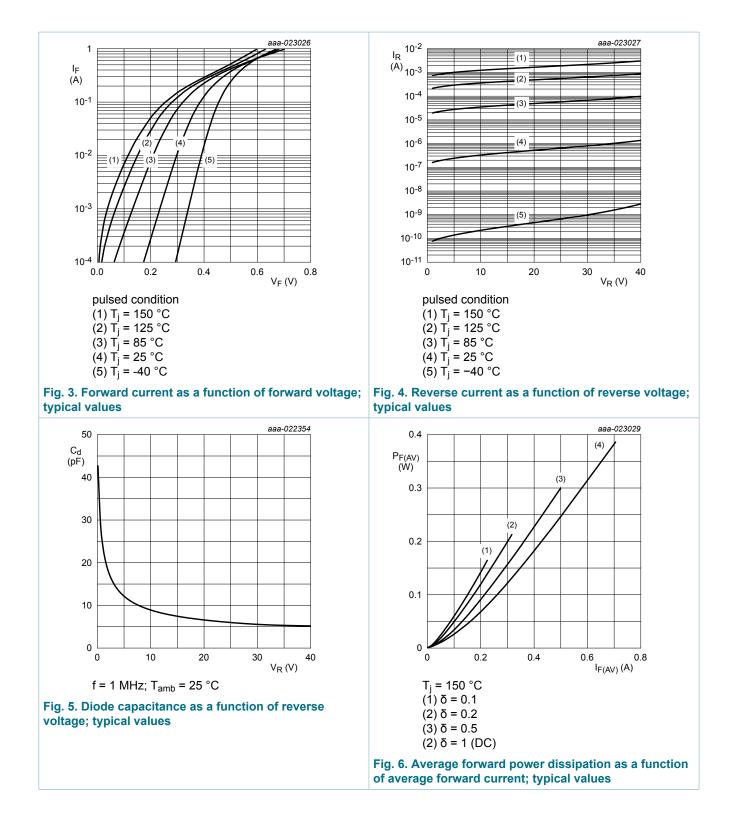
10. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{(BR)R}	reverse breakdown voltage	I_{R} = 1 mA; t_{p} $\leq~$ 300 μ s; $\delta~\leq~$ 0.02 ; T_{j} = 25 $^{\circ}C$	40	-	-	V
V _F for	forward voltage	I_{F} = 10 mA; t_{p} \leq 300 μ s; δ \leq 0.02 ; T_{j} = 25 $^{\circ}\text{C}$	-	300	380	mV
		I _F = 100 mA; t _p ≤ 300 μs; δ ≤ 0.02 ; T _j = 25 °C	-	390	470	mV
		I _F = 200 mA; t _p ≤ 300 μs; δ ≤ 0.02 ; T _j = 25 °C	-	435	510	mV
		I_{F} = 300 mA; t_{p} \leq 300 $\mu\text{s};$ δ \leq 0.02 ; T_{j} = 25 °C	-	475	560	mV
		I _F = 400 mA; t _p ≤ 300 μs; δ ≤ 0.02 ; T _j = 25 °C	-	515	600	mV
		I _F = 500 mA; t _p ≤ 300 μs; δ ≤ 0.02 ; T _j = 25 °C	-	550	640	mV
		I_{F} = 500 mA; t_{p} $\leq~$ 300 $\mu\text{s};$ δ $\leq~$ 0.02 $;$ T_{j} = -40 $^{\circ}\text{C}$	-	570	670	mV
		I _F = 500 mA; t _p ≤ 300 μs; δ ≤ 0.02 ; T _j = 125 °C	-	520	610	mV
R	reverse current	V_R = 30 V; pulsed; T _j = 25 °C	-	1	5	μA
		V_R = 40 V; pulsed; T _j = 25 °C	-	1.5	8	μA
		V _R = 40 V; pulsed; T _j = 125 °C	-	1	8	mA
C _d diode capacita	diode capacitance	V _R = 1 V; f = 1 MHz; T _j = 25 °C	-	24	-	pF
		V _R = 4 V; f = 1 MHz; T _j = 25 °C	-	13.5	-	pF
		V _R = 10 V; f = 1 MHz; T _j = 25 °C	-	9	-	pF
rr	reverse recovery time	$I_F = 0.5 \text{ A}; I_R = 0.5 \text{ A}; I_{R(meas)} = 0.1 \text{ A};$ $T_i = 25 \text{ °C}$	-	1.8	-	ns

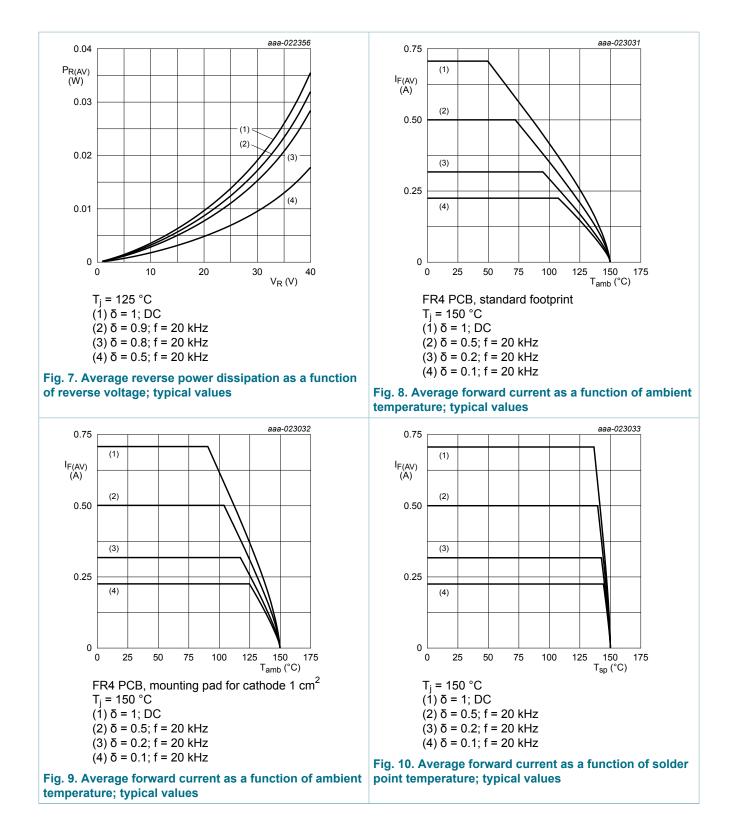
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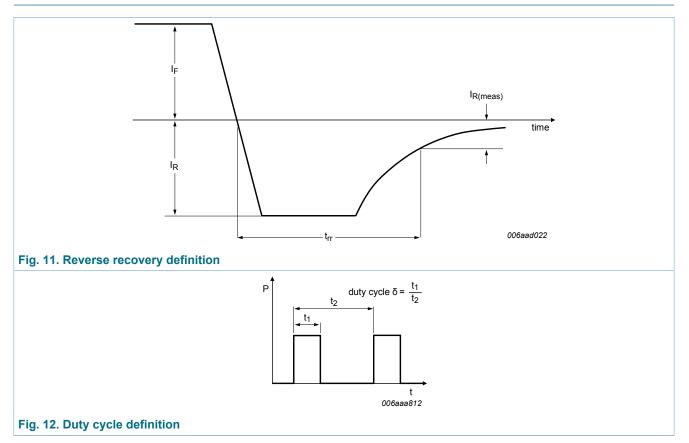


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11. Test information



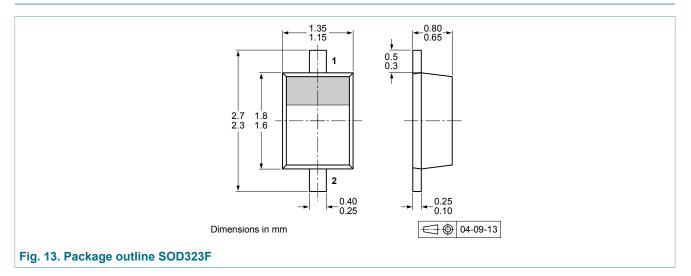
The current ratings for the typical waveforms are calculated according to the equations: $I_{F(AV)} = I_M \times \delta$ with I_M defined as peak current, $I_{RMS} = I_{F(AV)}$ at DC, and $I_{RMS} = I_M \times \sqrt{\delta}$ with I_{RMS} defined as RMS current.

Quality information

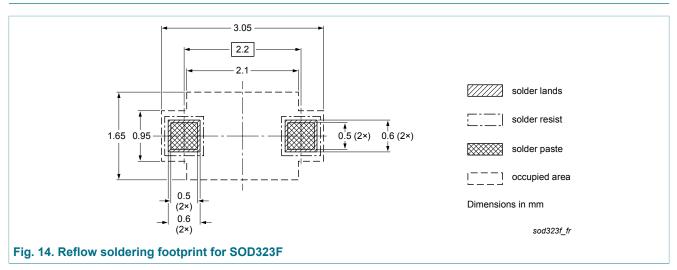
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

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12. Package outline



13. Soldering



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14. Revision history

Table 8. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PMEG4005CEJ v.1	20160512	Product data sheet	-	-		

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15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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