



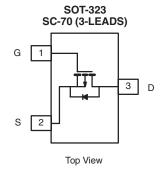
P-Channel 2.5-V (G-S) MOSFET

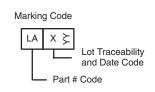
PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
- 20	0.430 at V _{GS} = - 4.5 V	- 0.72		
	0.480 at V _{GS} = - 3.6 V	- 0.68		
	0.700 at V _{GS} = - 2.5 V	- 0.56		

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFETs
- 2.5 V Rated
- · Compliant to RoHS Directive 2002/95/EC







Ordering Information: Si1303DL-T1-E3 (Lead (Pb)-free)

Si1303DL-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS $T_A =$	25 °C, unless other	erwise noted			
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	- 20		V
Gate-Source Voltage		V_{GS}	± 12		
Continuous Proin Current /T = 150 °C\a	T _A = 25 °C	- I _D	- 0.72	- 0.67	Α
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		- 0.58	- 0.54	
Pulsed Drain Current		I _{DM}	- 2.5		^
Continuous Diode Current (Diode Conduction) ^a		I _S	- 0.28	- 0.24	
Maniana Barras Biasinational	T _A = 25 °C	P _D	0.34	0.29	W
Maximum Power Dissipation ^a	T _A = 70 °C		0.22	0.19	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	t ≤ 5 s	R _{thJA}	315	375	
	Steady State		360	430	°C/W
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	285	340	

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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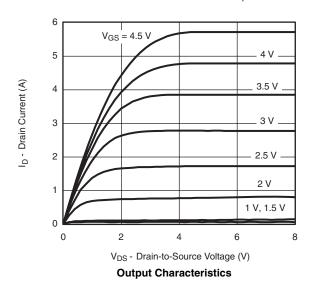
SPECIFICATIONS T _J = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_{D} = -250 \mu A$	- 0.6		- 1.4	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 20 V, V _{GS} = 0 V			- 1	μΑ	
Zero Gate Voltage Drain Gunent		$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 ^{\circ}\text{C}$			- 5		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 2.5			Α	
		$V_{GS} = -4.5 \text{ V}, I_D = -1 \text{ A}$		0.360	0.430		
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -3.6 \text{ V}, I_D = -0.7 \text{ A}$		0.400	0.480	Ω	
		V _{GS} = - 2.5 V, I _D = - 0.3 A		0.560	0.700		
Forward Transconductancea	9 _{fs}	V _{GS} = - 10 V, I _D = - 1 A		1.7		S	
Diode Forward Voltage ^a	V _{SD}	I _S = - 0.3 A, V _{GS} = 0 V			- 1.2	V	
Dynamic ^b							
Total Gate Charge	Q_g			1.7	2.2		
Gate-Source Charge	Q_{gs}	Q_{gs} $V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -1 \text{ A}$		0.38		nC	
Gate-Drain Charge	Q_{gd}			0.63			
Turn-On Delay Time	t _{d(on)}			9	15		
Rise Time	t _r	$V_{DD} = -10 \text{ V}, R_L = 10 \Omega$		31	45		
Turn-Off DelayTime	t _{d(off)}	$I_D \cong$ - 1 A, V_{GEN} = - 4.5 V, R_g = 6 Ω		12.5	20	ns	
Fall Time	t _f	7		14	20		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1 A, dI/dt = 100 A/μs		35	55		

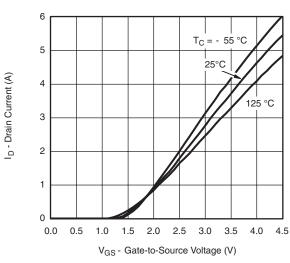
Notes:

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

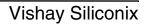
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



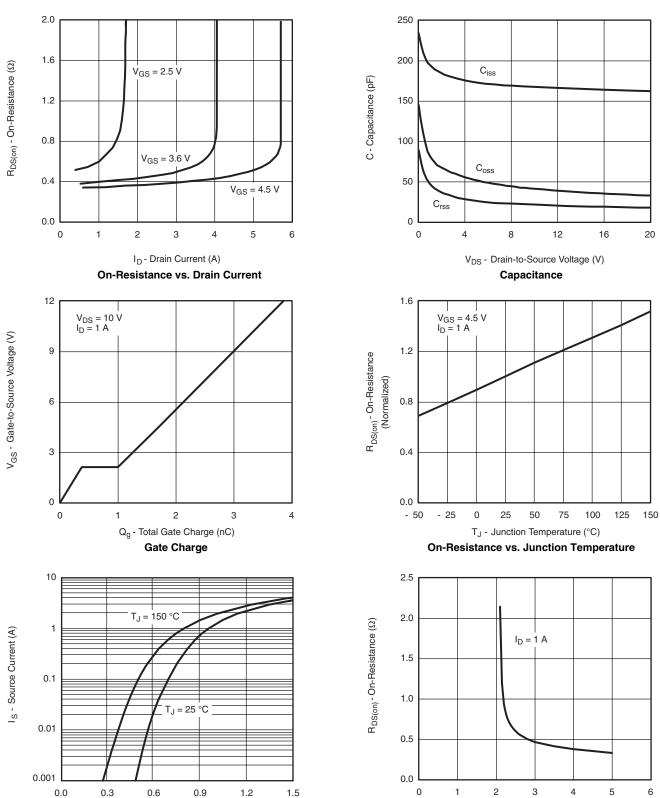








TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



V_{SD} - Source-to-Drain Voltage (V)

Source-Drain Diode Forward Voltage

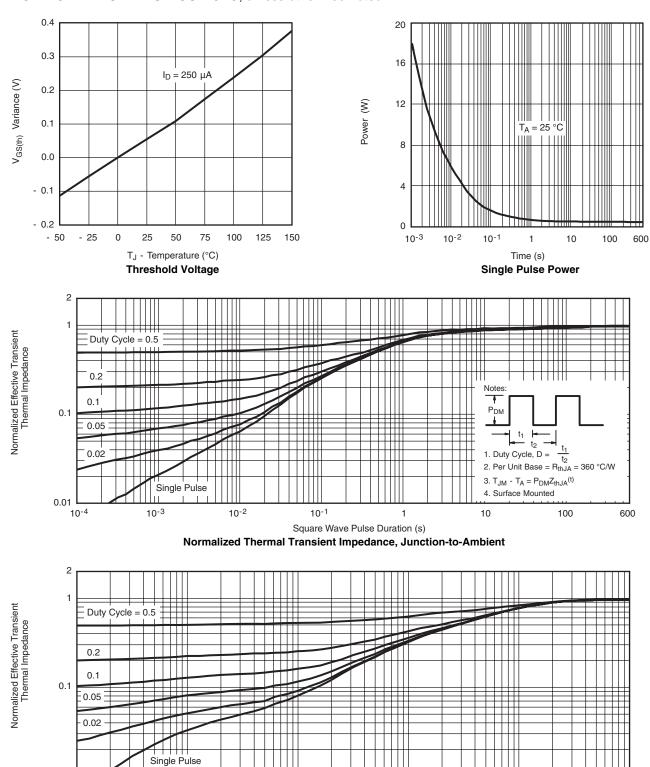
V_{GS} - Gate-to-Source Voltage (V)

On-Resistance vs. Gate-to-Source Voltage

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Square Wave Pulse Duration (s) Normalized Thermal Transient Impedance, Junction-to-Foot

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