ON Semiconductor

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74AC32 74ACT32

Quad 2-Input OR Gate

Features

- I_{CC} reduced by 50% on 74AC only
- Outputs source/sink 24mA
- ACT32 has TTL-compatible inputs

General Description

The AC32/ACT32 contains four, 2-input OR gates

Ordering Information

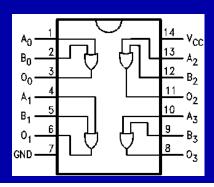
	Package Description
	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow
	14-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide
	14-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide
	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide
	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150" Narrow
	14-Lead Thin Shrink Small Outline Package (TSSOP), JEDEC MO-153, 4.4mm Wide
	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300" Wide

Device also available in Tape and Reel. Specify by appending suffix letter "X" to the ordering number.

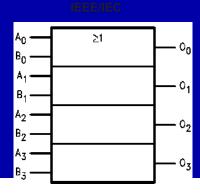


II packages are lead free per JEDEC: J-STD-020B standard

Connection Diagram



Logic Symbol



Pin Description

Pin Names	Description				
A _n , B _n	Inputs				
On	Outputs				

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Rating
V_{CC}	Supply Voltage	−0.5V to +7.0V
I _{IK}		
V _I		
I _{OK}		
Vo		
I _O		
I _{CC} or I _{GND}		
T _{STG}		
TJ		

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. ON Semiconductor does not recommend exceeding them or designing to absolute maximum ratings.

Symbol	
V _{CC}	
V _I	
Vo	
T _A	
ΔV / Δt	
ΔV / Δt	

DC Electrical Characteristics for AC

		V _{CC}	Conditions	$T_A = $	+25°C	$T_A = -40$ °C to +85°C		
Symbol	Parameter	(V)		Тур.	G	uaranteed Limits	Units	
V _{IH}	Minimum HIGH Level	3.0	$V_{OUT} = 0.1V$ or	1.5	2.1	2.1	V	
	Input Voltage	4.5	V _{CC} – 0.1V	2.25	3.15	3.15		

DC Electrical Characteristics for ACT

		V _{CC}		T _A = -	+25°C	T _A = -40°C to +85°C	
Symbol	Parameter	(V)	Conditions	Тур.	C	Suaranteed Limits	Units
V _{IH}	Minimum HIGH Level	4.5	$V_{OUT} = 0.1V$ or	1.5	2.0	2.0	V
	Input Voltage	5.5	V _{CC} – 0.1V	1.5	2.0	2.0	

Notes:

- 4. All outputs loaded; thresholds on input associated with output under test.
- 5. Maximum test duration 2.0ms, one output loaded at a time.

AC Electrical Characteristics for AC

			T _A = +25°C, C _L = 50pF		$T_A = -40$ °C to +85°C, $C_L = 50$ pF			
Symbol	Parameter	$V_{CC}(V)^{(6)}$	Min.	Тур.	Max.	Min.	Max.	Units
t _{PLH}	Propagation Delay	3.3	1.5	7.5	9.0	1.5	10.0	ns
		5.0	1.5	5.5	7.5	1.0	8.5	
t _{PHL}								

Note:

6. Voltage range 3.3 is 3.3V \pm 0.3V. Voltage range 5.0 is 5.0V \pm 0.5V.

AC Electrical Characteristics for ACT

Symbol							Units
t _{PLH}							ns
t _{PHL}							ns

Note:

7. Voltage Range 5.0 is 5.0V ± 0.5V.

Capacitance

Symbol		Units
C _{IN}		рF
C_{PD}		рF

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