# **General Purpose Transistors**

### **NPN Silicon**

These transistors are designed for general purpose amplifier applications. They are housed in the SC-75/SOT-416 package which is designed for low power surface mount applications.

#### **Features**

- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- Pb-Free Packages are Available

### **MAXIMUM RATINGS** $(T_A = 25^{\circ}C)$

Rating	Symbol	Max	Unit
Collector–Emitter Voltage	V <sub>CEO</sub>	45	V
Collector-Base Voltage	V <sub>CBO</sub>	50	V
Emitter-Base Voltage	V <sub>EBO</sub>	6.0	V
Collector Current – Continuous	Ic	100	mAdc

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

### THERMAL CHARACTERISTICS

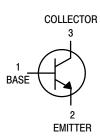
Characteristic	Symbol	Max	Unit
Total Device Dissipation, FR-4 Board (Note 1) T <sub>A</sub> = 25°C Derated above 25°C	P <sub>D</sub>	200	mW mW/°C
Defated above 25°C		1.0	IIIVV/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{ heta JA}$	600	°C/W
Total Device Dissipation, FR-4 Board (Note 2) T <sub>A</sub> = 25°C Derated above 25°C	P <sub>D</sub>	300	mW mW/°C
	_		
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ hetaJA}$	400	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	–55 to +150	°C

- 1. FR-4 @ min pad.
- 2. FR-4 @ 1.0 × 1.0 in pad.



### ON Semiconductor®

#### http://onsemi.com





**CASE 463** SC-75/SOT-416 STYLE 1

#### MARKING DIAGRAM



XX = Device Code

M = Date Code

= Pb-Free Package (Note: Microdot may be in either location)

#### **ORDERING INFORMATION**

See detailed ordering, marking and shipping information in the package dimensions section on page 5 of this data sheet.

# **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	<u>'</u>		•	•	•
Collector – Emitter Breakdown Voltage (I <sub>C</sub> = 10 mA) BC847 Series	V <sub>(BR)CEO</sub>	45	-	_	V
Collector – Emitter Breakdown Voltage ( $I_C = 10 \mu A, V_{EB} = 0$ ) BC847 Series	V <sub>(BR)CES</sub>	50	-	-	V
Collector – Base Breakdown Voltage ( $I_C = 10 \mu A$ ) BC847 Series	V <sub>(BR)CBO</sub>	50	-	-	V
Emitter – Base Breakdown Voltage (I <sub>E</sub> = 1.0 μA) BC847 Series	V <sub>(BR)EBO</sub>	6.0	-	-	V
Collector Cutoff Current ( $V_{CB} = 30 \text{ V}$ ) ( $V_{CB} = 30 \text{ V}$ , $T_A = 150^{\circ}\text{C}$ )	I <sub>CBO</sub>	- -	- -	15 5.0	nA μA
ON CHARACTERISTICS					
DC Current Gain $ (I_C = 10 \ \mu\text{A}, \ V_{CE} = 5.0 \ V) \\ BC847A \\ BC847B \\ BC847C $	h <sub>FE</sub>	- - -	90 150 270	- - -	_
$(I_{C} = 2.0 \text{ mA}, V_{CE} = 5.0 \text{ V})$ BC847A BC847B BC847C		110 200 420	180 290 520	220 450 800	
Collector – Emitter Saturation Voltage ( $I_C = 10$ mA, $I_B = 0.5$ mA) ( $I_C = 100$ mA, $I_B = 5.0$ mA)	V <sub>CE(sat)</sub>	-	_ _	0.25 0.6	V
Base – Emitter Saturation Voltage ( $I_C = 10$ mA, $I_B = 0.5$ mA) ( $I_C = 100$ mA, $I_B = 5.0$ mA)	V <sub>BE(sat)</sub>	-	0.7 0.9	- -	V
Base – Emitter Voltage ( $I_C$ = 2.0 mA, $V_{CE}$ = 5.0 V) ( $I_C$ = 10 mA, $V_{CE}$ = 5.0 V)	V <sub>BE(on)</sub>	580 -	660 -	700 770	mV
SMALL-SIGNAL CHARACTERISTICS			•	•	
Current – Gain – Bandwidth Product (I <sub>C</sub> = 10 mA, V <sub>CE</sub> = 5.0 Vdc, f = 100 MHz)	f <sub>T</sub>	100	_	_	MHz
Output Capacitance (V <sub>CB</sub> = 10 V, f = 1.0 MHz)	C <sub>obo</sub>	_	_	4.5	pF
Noise Figure (I <sub>C</sub> = 0.2 mA, $V_{CE}$ = 5.0 Vdc, $R_S$ = 2.0 k $\Omega$ , f = 1.0 kHz, BW = 200 Hz)	NF	_	_	10	dB

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

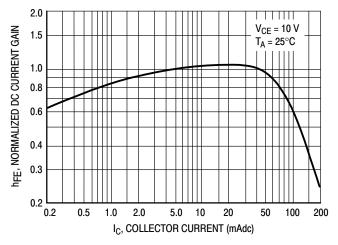


Figure 1. Normalized DC Current Gain

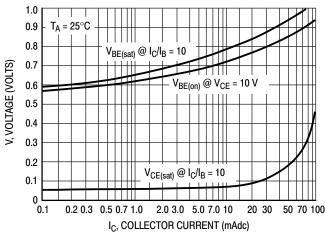


Figure 2. "Saturation" and "On" Voltages

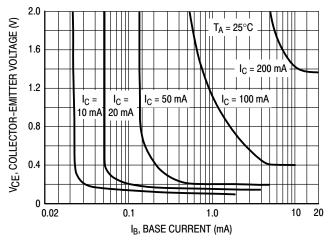


Figure 3. Collector Saturation Region

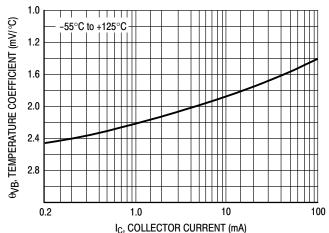


Figure 4. Base-Emitter Temperature Coefficient

### **BC847**

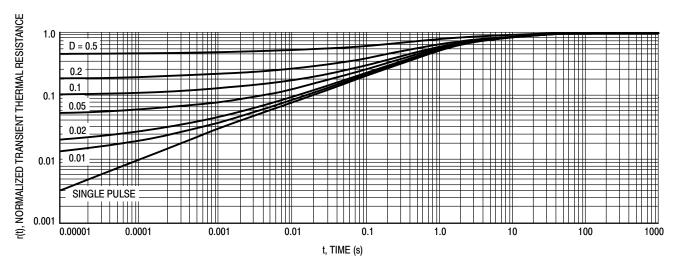


Figure 5. Normalized Thermal Response

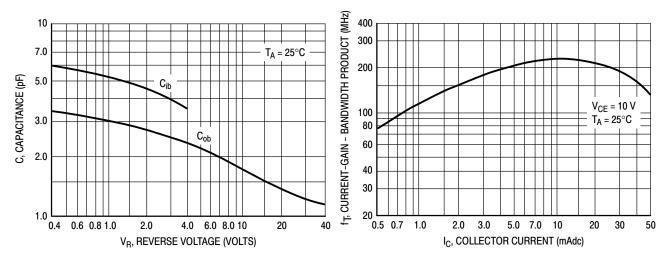


Figure 6. Capacitances

Figure 7. Current-Gain - Bandwidth Product

### **ORDERING INFORMATION**

Device	Marking	Package	Shipping <sup>†</sup>
BC847ATT1	1E	SC-75/SOT-416	3,000 / Tape & Reel
BC847BTT1	1F	SC-75/SOT-416	
BC847BTT1G	1F	SC-75/SOT-416 (Pb-Free)	3,000 / Tape & Reel
NSVBC847BTT1G*	1F	SC-75/SOT-416 (Pb-Free)	3,000 / Tape & Reel
BC847CTT1G	1G	SC-75/SOT-416 (Pb-Free)	3,000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.
\*NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q101 Qualified and PPAP

Capable.

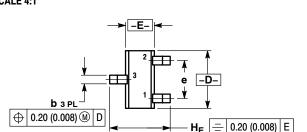
# **MECHANICAL CASE OUTLINE**

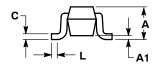




SC-75/SOT-416 CASE 463-01 **ISSUE G** 

**DATE 07 AUG 2015** 





STYLE 1: PIN 1. BASE 2. EMITTER

3. COLLECTOR

STYLE 4: PIN 1. CATHODE 2. CATHODE 3. ANODE STYLE 5: PIN 1. GATE 2. SOURCE 3. DRAIN

STYLE 2: PIN 1. ANODE 2. N/C 3. CATHODE

STYLE 3: PIN 1. ANODE 2. ANODE 3. CATHODE

#### NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: MILLIMETER.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.70	0.80	0.90	0.027	0.031	0.035
A1	0.00	0.05	0.10	0.000	0.002	0.004
b	0.15	0.20	0.30	0.006	0.008	0.012
С	0.10	0.15	0.25	0.004	0.006	0.010
D	1.55	1.60	1.65	0.061	0.063	0.065
Е	0.70	0.80	0.90	0.027	0.031	0.035
е	1.00 BSC			.04 BS0		
L	0.10	0.15	0.20	0.004	0.006	0.008
HE	1.50	1.60	1.70	0.060	0.063	0.067

### **GENERIC MARKING DIAGRAM\***



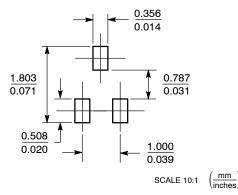
XX= Specific Device Code

Μ = Date Code

= Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

### **SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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