

SI-3000ZD Series**Surface-Mount, Low Dropout Voltage****■Features**

- Compact surface-mount package (TO263-5)
- Output current: 3.0A
- Low dropout voltage: $V_{DIF} \leq 0.6V$ (at $I_o = 3.0A$)
- Low circuit current at output OFF: $I_q (\text{OFF}) \leq 1\mu A$
- Built-in overcurrent and thermal protection circuits

■Applications

- Secondary stabilized power supply (local power supply)

■Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit	($T_a=25^\circ C$)
DC Input Voltage	V_{IN}^{*1}	10	V	
Output Control Terminal Voltage	V_c	6	V	
DC Output Current	I_o^{*1}	3.0	A	
Power Dissipation	P_d^{*3}	3	W	
Junction Temperature	T_j	-30 to +125	$^\circ C$	
Operating Ambient Temperature	T_{op}	-30 to +85	$^\circ C$	
Storage Temperature	T_{stg}	-40 to +125	$^\circ C$	
Thermal Resistance (Junction to Ambient Air)	θ_{j-a}	33.3	$^\circ C/W$	
Thermal Resistance (Junction to Case)	θ_{j-c}	3	$^\circ C/W$	

■Recommended Operating Conditions

Parameter	Symbol	Ratings	Unit	Remarks
Input Voltage	V_{IN}	$^{*2} 2$ to 6^{*1}	V	
Output Current	I_o	0 to 3	A	
Operating Ambient Temperature	$T_{op}^{(a)}$	-20 to +85	$^\circ C$	
Operating Junction Temperature	$T_{op}^{(j)}$	-20 to +100	$^\circ C$	
Output Voltage Variable Range	V_{ADJ}	1.2 to 5	V	Only for SI-3011ZD. Refer to the block diagram.

*1: V_{IN} (max) and I_o (max) are restricted by the relation $P_o = (V_{IN} - V_o) \times I_o$.

*2: Set the input voltage to 2.4V or higher when setting the output voltage to 2.0V or lower (SI-3011ZD).

*3: When mounted on glass-epoxy board of 40 × 40mm (copper laminate area 100%).

■Electrical Characteristics

($T_a=25^\circ C$, $V_c=2V$, unless otherwise specified)

Parameter	Symbol	SI-3011ZD (Variable type)			SI-3033ZD			Unit
		min.	typ.	max.	min.	typ.	max.	
Output Voltage (Reference Voltage V_{ADJ} for SI-3011ZD)	$V_o (V_{ADJ})$	1.078	1.100	1.122	3.234	3.300	3.366	V
Line Regulation <small>Conditions: $V_{IN}=V_o+1V$, $I_o=10mA$</small>	ΔV_{OLINE}							mV
Load Regulation <small>Conditions: $V_{IN}=3.3$ to 5V, $I_o=0$ to 3A ($V_o=2.5V$)</small>	ΔV_{LOAD}		10				10	mV
Dropout Voltage <small>Conditions: $I_o=3A$ ($V_o=2.5V$)</small>	V_{DIF}		40				40	mV
Quiescent Circuit Current <small>Conditions: $V_{IN}=V_o+1V$, $V_c=0V$</small>	I_q	1	1.5		1	1.5		mA
Circuit Current at Output OFF <small>Conditions: $V_{IN}=V_o+1V$, $V_c=0V$</small>	$I_q (\text{OFF})$		1				1	μA
Temperature Coefficient of Output Voltage <small>Conditions: $T_j=0$ to 100°C</small>	$\Delta V_o / \Delta T_a$	± 0.3				± 0.3		$mV/^\circ C$
Ripple Rejection <small>Conditions: $V_{IN}=V_o+1V$, $I_o=100$ to 120Hz, $I_o=0.1A$</small>	R_{REJ}	60			60			dB
Overcurrent Protection Starting Current ^{*2} ^{*4}	I_{S1}	3.2			3.2			A
Control Voltage (Output ON) ^{*3} <small>Conditions: $V_c=2.7V$</small>	V_c, I_H	2			2			V
Control Voltage (Output OFF) ^{*3} <small>Conditions: $V_c=0V$</small>	V_c, I_L		0.8			0.8		
Control Current (Output ON) <small>Conditions: $V_c=2.7V$</small>	I_c, I_H		100			100		μA
Control Current (Output OFF) <small>Conditions: $V_c=0V$</small>	I_c, I_L	-5	0		-5	0		μA

*1: Set the input voltage to 2.4V or higher when setting the output voltage to 2.0V or lower.

*2: I_{S1} is specified at the -5% drop point of output voltage V_o under the condition of Output Voltage parameter.

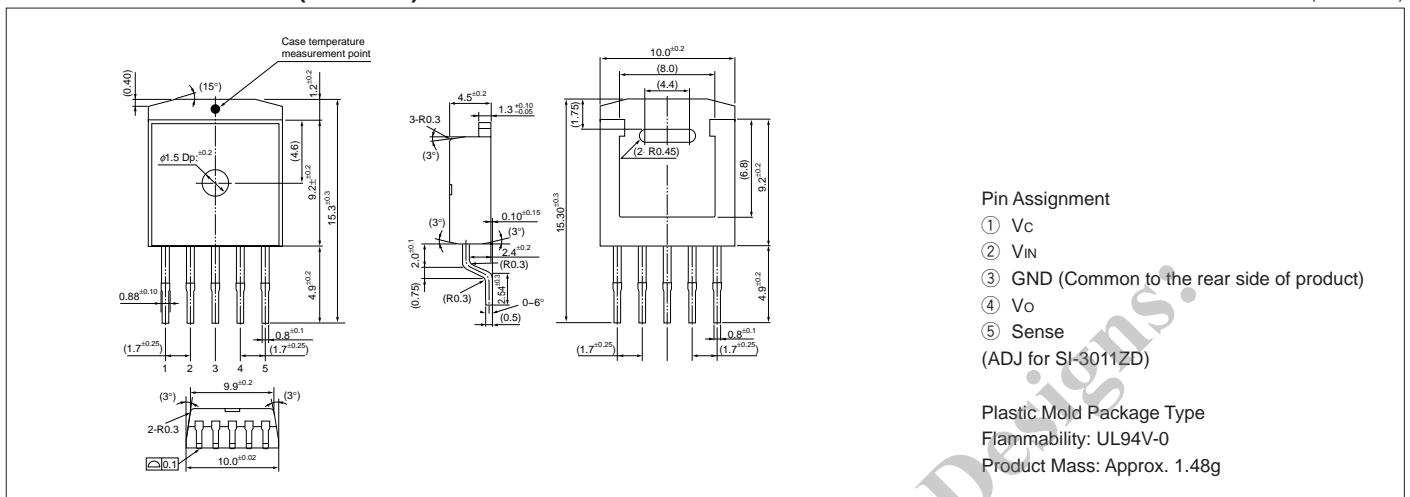
*3: Output is OFF when the output control terminal (V_c terminal) is open. Each input level is equivalent to LS-TTL level. Therefore, the device can be driven directly by LS-TTLs.

*4: These products cannot be used for the following applications because the built-in foldback-type overcurrent protection may cause errors during start-up stage.

(1) Constant current load (2) Positive and negative power supply (3) Series-connected power supply (4) V_o adjustment by raising ground voltage

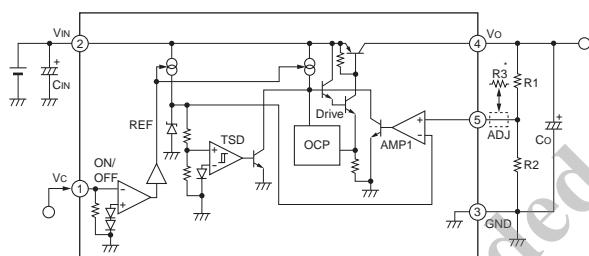
■External Dimensions (TO263-5)

(Unit : mm)



■Block Diagram

SI-3011ZD

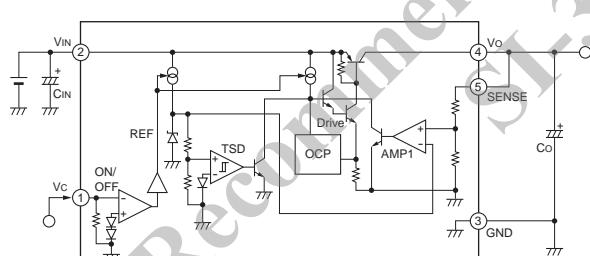


Cin: Input capacitor (Approx. 10μF)

Co: Output capacitor (47μF or larger)

The output voltage may oscillate if a low ESR type capacitor (such as a ceramic capacitor) is used for the output capacitor in the SI-3000ZD Series.

SI-3033ZD



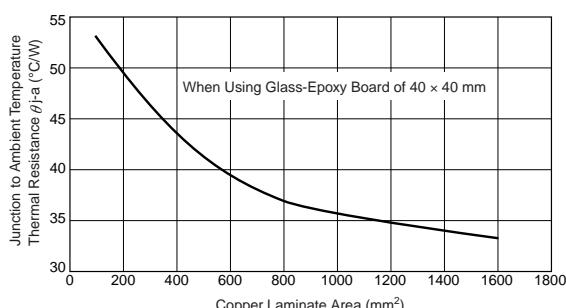
R1, R2: Output voltage setting resistors

The output voltage can be set by connecting R1 and R2 as shown at left.
The recommended value for R2 is 10kΩ or 11kΩ.

$R1 = (V_o - V_{adj}) / (V_{adj}/R2)$
*: Insert R3 in case of setting Vo to $V_o \leq 1.8V$. The recommended value for R3 is 10kΩ.

■Reference Data

Copper Laminate Area (on Glass-Epoxy Board) vs.
Thermal Resistance (from Junction to Ambient Temperature) (Typical Value)



- A higher heat radiation effect can be achieved by enlarging the copper laminate area connected to the inner frame to which a monolithic IC is mounted.
- Obtaining the junction temperature
Measure GND terminal temperature T_c with a thermocouple, etc. Then substitute this value in the following formula to obtain the junction temperature.

$$T_j = P_D \times \theta_{j-a} + T_c \quad P_D = (V_{IN} - V_o) \times I_{OUT}$$