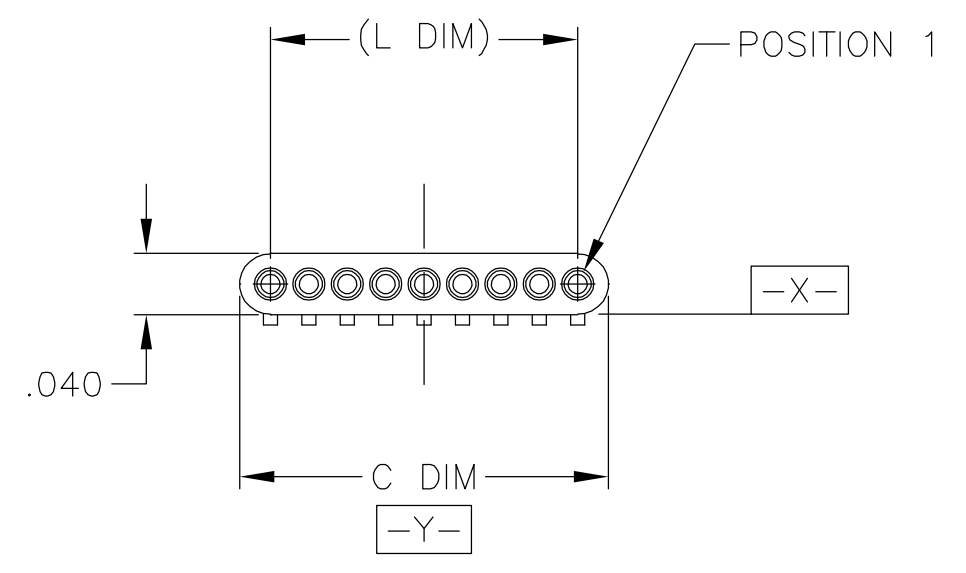
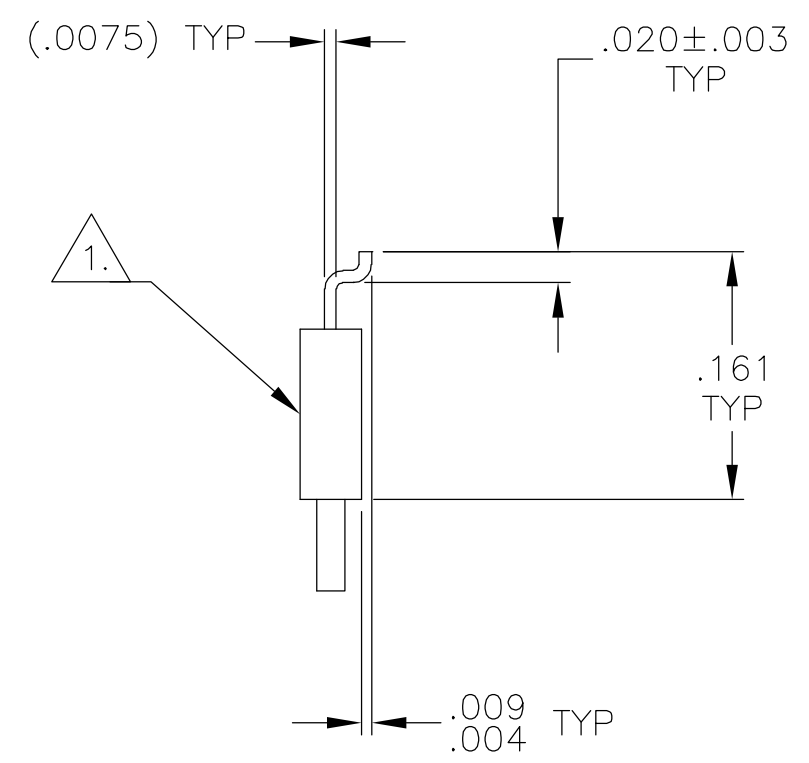
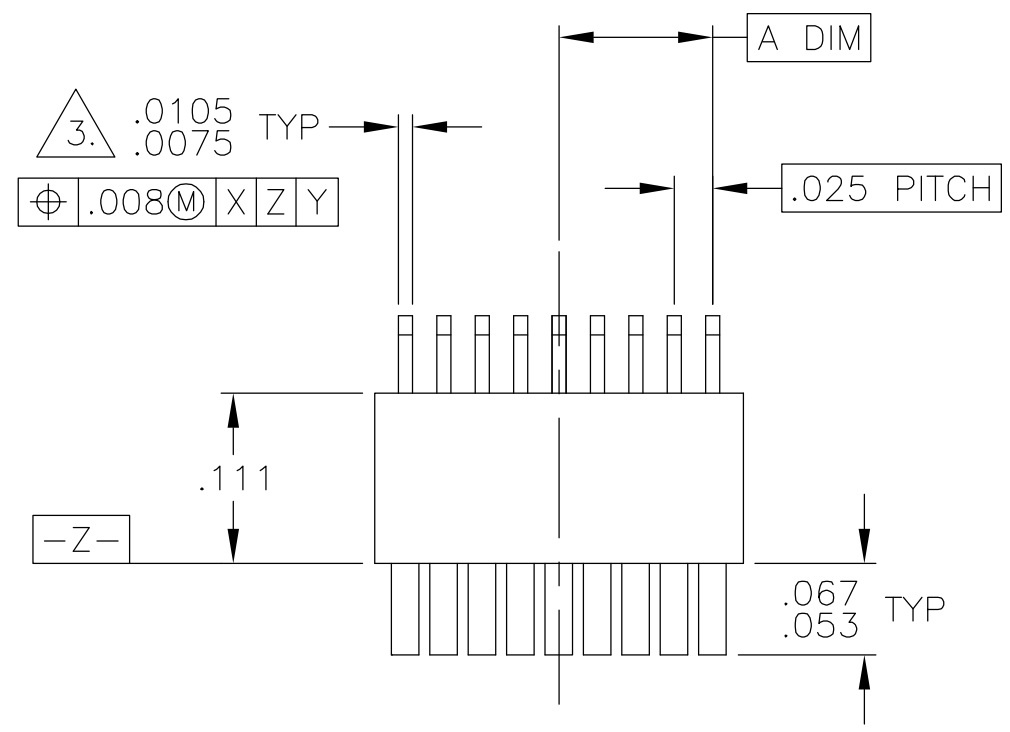


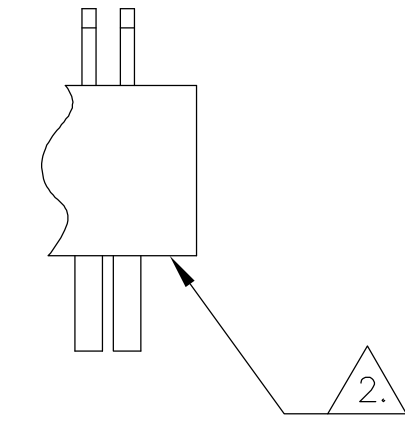
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LOC	DIST	REVISIONS					
DF	D0	P	LTR	DESCRIPTION	DATE	DWN	APVD
		R		UPDATE PER 0H2Y-0008-04	9 JAN 04	CAS	MKS



SIZE	A DIM	C DIM	(L DIM)
04	.0375	.115	(.075)
09	.100	.240	(.200)
15	.175	.390	(.350)

5.	25	.300	.640	(.600)
5.	37	.450	.940	(.900)
5.	51	.625	1.290	(1.250)



DETAIL A
 POSITION "T" SHOWN FOR REFERENCE

- T ○○○○○○○○● POSITION 1
- U ○○○○○○○○●○ POSITION 2
- V ○○○○○○○●○○○ POSITION 3
- W ●○○○○○○○● POSITION 1, AND SECOND TO LAST POSITION
- Y ○○●○○○○○○○● POSITION 1, AND THIRD TO LAST POSITION
- Z ○○○○○○○○●○ POSITION 2, AND THIRD TO LAST POSITION

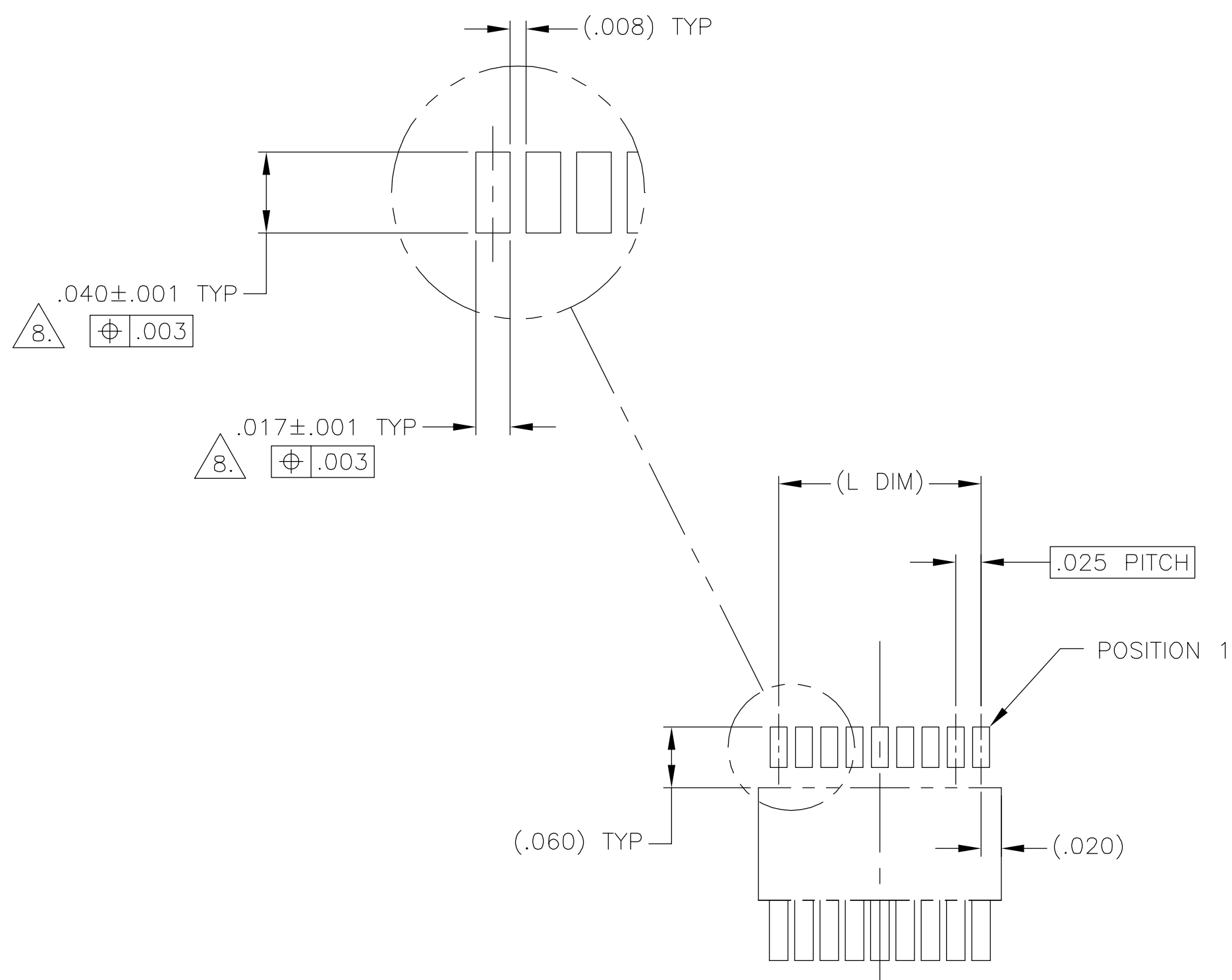
2. POLARIZING POSITIONS, SEE DETAIL A

1. INSULATOR MATERIAL: LCP (LIQUID CRYSTAL POLYMER) PER ASTM D5138
2. OPTIONAL POLARIZATION POSITION SHOWN FOR REFERENCE ONLY. IF POLARIZATION GUIDE POST(S) ARE REQUIRED ON THE MATING PLUG, THE CORRESPONDING LOCATION MUST BE SPECIFIED IN THE NANONICS RECEPTACLE PART NUMBER. THE CONTACT CAVITY IS LEFT EMPTY FOR ACCEPTANCE OF THE PLUG GUIDE POST.
3. SMT LEADS ARE BeCu, TIN LEAD PLATED 60/40 COMPOSITION PER SAE-AMS-P-81728
4. TERMINATION CODE: L2
5. WHEN ORDERING THESE SIZES, FIRST CONSULT TYCO ELECTRONICS/NANONICS FOR ADDITIONAL INFORMATION.
6. DUE TO THE SMALL AREA OF THIS STRIP RECEPTACLE, CONSIDER SURFACE MOUNTING THE STRIP PLUG (1589451).
7. THIS DRAWING PREVIOUSLY IDENTIFIED AS NANONICS N10138/032

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN D. RYAN 20 APR 92	 Tyco Electronics Corporation Harrisburg, PA 17105
DIMENSIONS: INCHES		CHK E. BURGER 09 FEB 93	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD M. STORRY 25 APR 01	NAME
0 PLC ± - 1 PLC ± - 2 PLC ± .010 3 PLC ± .005 4 PLC ± - ANGLES ± 1		PRODUCT SPEC	RECEPTACLE ASSEMBLY, UNSHROUDED STRIP, HORIZONTAL SMT, PLASTIC
MATERIAL SEE NOTES		FINISH SEE NOTES	SIZE A2
		WEIGHT -	CAGE CODE 00779
		CUSTOMER DRAWING	DRAWING NO C=1589452
		SCALE 8:1	SHEET 1 OF 2
			REV R

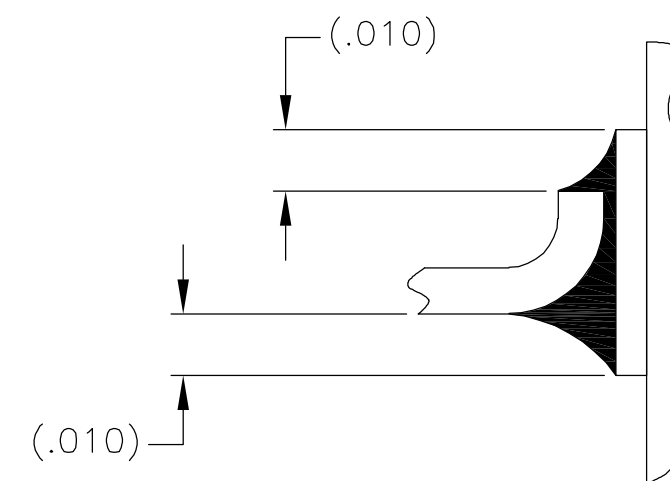
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LOC		DIST		REVISIONS			
P	LTR	DESCRIPTION		DATE	DWN	APVD	
DF	DO	R		UPDATE PER 0H2Y-0008-04	9 JAN 04	CAS	MKS

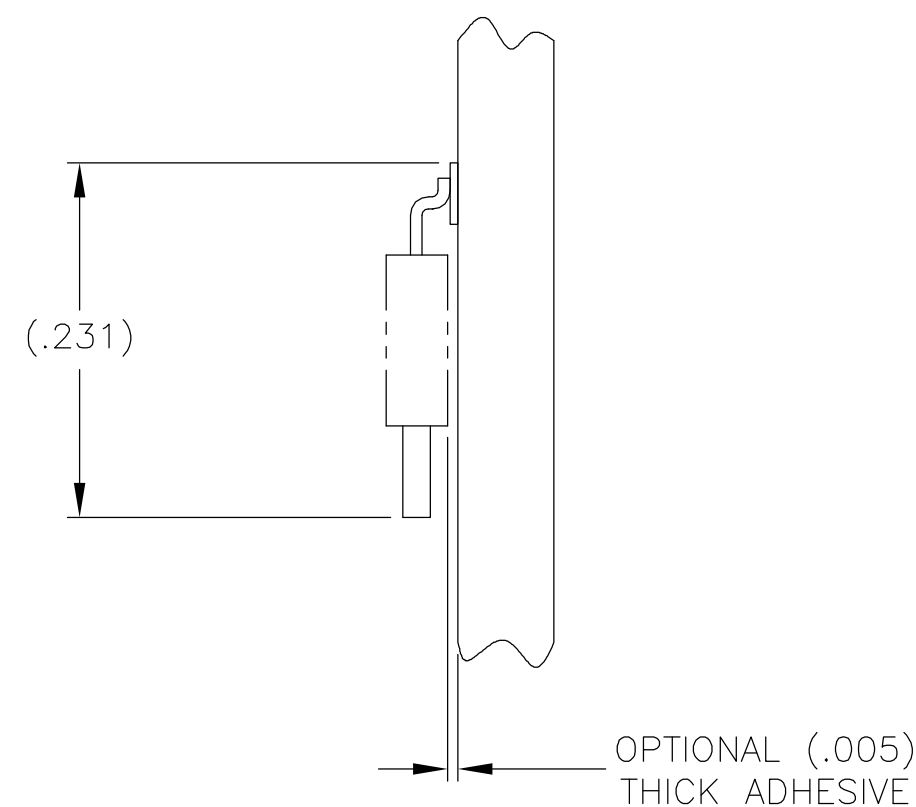


SIZE 09 SHOWN FOR REFERENCE

TYPICAL PCB LAYOUT



TYPICAL FOOT PLACEMENT ON SOLDER PAD



8. POSITIONAL TOLERANCES FOR BASIC DIMENSIONED FEATURES ARE RELATIVE TO FIDUCIALS OR SOME SIMILAR DATUM REFERENCE DEFINED BY THE PCB DESIGNER.

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN	-	tyco Electronics		Tyco Electronics Corporation Harrisburg, PA 17105	
DIMENSIONS: INCHES		CHK	-			NAME	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD	-	PRODUCT SPEC		SIZE	A2
0 PLC ± -		APPLICATION SPEC	-	CAGE CODE		00779	DRAWING NO
1 PLC ± -		WEIGHT		C=1589452		RESTRICTED TO	
2 PLC ± -		CUSTOMER DRAWING		SCALE		8:1	SHEET 2 OF 2
3 PLC ± -		SCALE		REV		R	
4 PLC ± -		SCALE		REV		R	
ANGLES ± -		SCALE		REV		R	
FINISH		SCALE		REV		R	