

DUAL PRE-BIASED TRANSISTORS FOR POWER MANAGEMENT

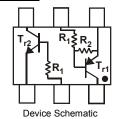
Features

- Epitaxial Planar Die Construction
- Built-In Biasing Resistors
- One 500mA PNP and One 100mA NPN
- Lead Free/RoHS Compliant (Note 1)
- "Green" Devices (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability

Part Numb	er	R1	R2	Marking
DIMD10A	Tr1	0.1K	10K	C72
אטו טואווט TUA	Tr2	10K	_	6/3

Mechanical Data

- Case: SC-74R
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Finish annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Marking Information: See Table and Page 3
- Ordering Information: See Page 3
- Weight: 0.015 grams (approximate)



Maximum Ratings PNP Section Tr1 @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Supply Voltage	Vcc	-50	V
Input Voltage	V_{IN}	-5 to +5	V
Output Current	lo	-500	mA

Maximum Ratings NPN Section Tr2 @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	50	V
Collector-Emitter Voltage	V_{CEO}	50	V
Emitter-Base Voltage	V_{EBO}	5	V
Collector Current	lc	100	mA

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation	P_{D}	300*	mW
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

^{*} Not to exceed 200mW for either Tr1 or Tr2.

Electrical Characteristics PNP Section Tr1 @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Input Voltage	V _{I(off)}	-0.3	_	_	V	$V_{CC} = -5V$, $I_{O} = -100\mu A$
input voitage	V _{I(on)}	_	_	-1.5		$V_O = 0.3$, $I_O = -100$ mA
Output Voltage	V _{O(on)}	_	-0.1	-0.3	V	$I_{O} = -100 \text{mA}/-5 \text{mA}$
Input Current	l _l	_	_	-25	mA	V _I = -2V
Output Current	I _{O(off)}	_	_	-0.5	μΑ	$V_{CC} = -50V, V_{I} = 0V$
DC Current Gain	G _I	68	_	_		_
Gain-Bandwidth Product*	f _T	_	200	_	MHz	V _{CE} = -10V, I _E = -50mA, f = 100MHz

* Transistor - For Reference Only

Notes: 1. No purposefully added lead.

2. "Green" devices, Halogen and Antimony Free, Diodes Inc's "Green" Policy can be found on our website at http://www.diodes.com



Electrical Characteristics NPN Section Tr2 @TA = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV_CBO	50			V	$I_{C} = 50 \mu A$
Collector-Emitter Breakdown Voltage	BV_CEO	50		_	V	I _C = 1mA
Emitter-Base Breakdown Voltage	BV_{EBO}	5		_	V	$I_E = 50 \mu A$
Collector Cutoff Current	I _{CBO}	_		0.5	μΑ	V _{CB} = 50V
Emitter Cutoff Current	I _{EBO}	_		0.5	μΑ	V _{EB} = 4V
Collector-Emitter Saturation Voltage	V _{CE(SAT)}	_		0.3	V	I _C /I _B = 10mA / 1.0mA
DC Current Transfer Ratio	h_{FE}	100	250	600	_	$I_C = 1mA$, $V_{CE} = 5V$
Gain-Bandwidth Product (Note 3)	f _T	_	250	_	MHz	V _{CE} = 10V, I _E = -5mA, f = 100MHz

Notes: 3. Transistor - For Reference Only

Typical Curves - Tr2

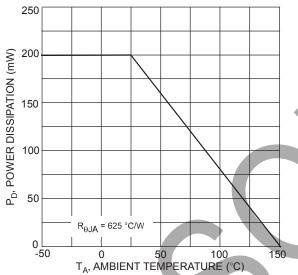


Fig. 1 Power Dissipation vs. Ambient Temperature

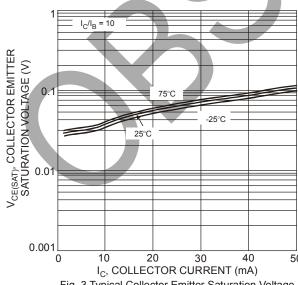


Fig. 3 Typical Collector Emitter Saturation Voltage vs. Collector Current

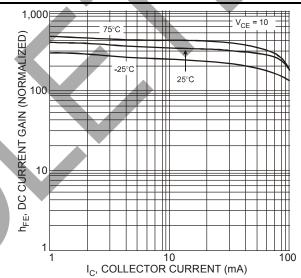


Fig. 2 Typical DC Current Gain vs. Collector Current

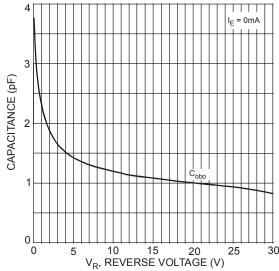
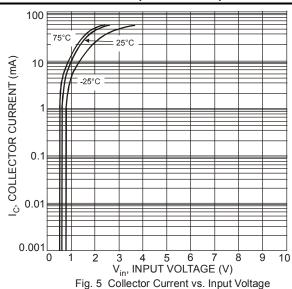
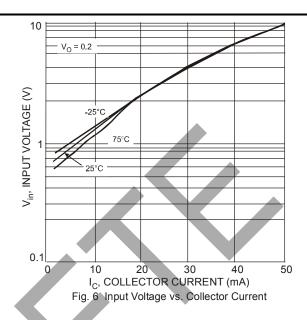


Fig. 4 Typical Capacitance Characteristics



Typical Curves - Tr2 (continued)



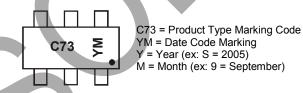


Ordering Information (Note 4)

Part Number	Case	Packaging
DIMD10A-7	SC-74R	3000/Tape & Reel

Notes: 4. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

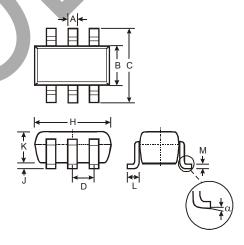
Marking Information



Date Code Key

Year	2005	2006	2007	2008	2009	2010	2	011	2012	2013	2014	2015
Code	S	1	U	V	W	X		Υ	Z	Α	В	С
										1 -	1	
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

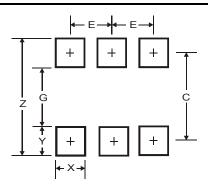
Package Outline Dimensions



SC-74R							
Dim	Min	Max	Тур				
Α	0.35	0.50	0.38				
В	1.50	1.70	1.60				
O	2.70	3.00	2.80				
D	_		0.95				
Н	2.90	3.10	3.00				
7	0.013	0.10	0.05				
K	1.00	1.30	1.10				
L	0.35	0.55	0.40				
M	0.10	0.20	0.15				
α	0°	8°					
All D	imensi	ons in	mm				



Suggested Pad Layout



Dimensions	Value (in mm)
Z	3.20
G	1.60
Х	0.55
Υ	0.80
С	2.40
E	0.95





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