

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# **TC7W02FU, TC7W02FK**

Dual 2-Input NOR Gate

#### **Features**

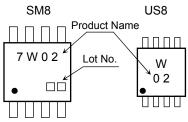
- High Speed •
- Low power dissipation
- High noise immunity
- Output drive capability
- Symmetrical Output Impedance : |I<sub>OH</sub>| = I<sub>OL</sub>= 4mA (min)
- Balanced propagation delays
- Wide operating voltage range
- : I<sub>CC</sub> = 1µA (max) at Ta = 25°C  $: V_{NIH} = V_{NIL} = 28\% V_{CC}$  (min)

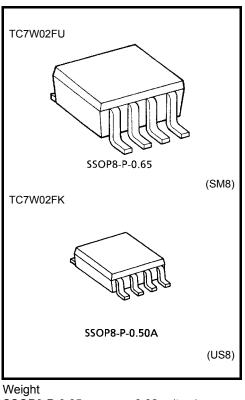
:  $t_{pd}$  = 6ns (typ.) at V<sub>CC</sub> = 5V

- : 10 LSTTL Loads

  - ∶t<sub>pLH</sub> ≒ t<sub>pHL</sub>
  - : V<sub>CC</sub> = 2 to 6V

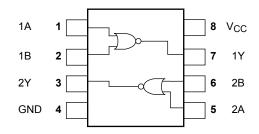






Weight	
SSOP8-P-0.65	: 0.02 g (typ.)
SSOP8-P-0.50A	: 0.01 g (typ.)

#### Pin Assignment (top view)



#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	-0.5 to 7.0	V
DC input voltage	V <sub>IN</sub>	–0.5 to V <sub>CC</sub> + 0.5	V
DC output voltage	V <sub>OUT</sub>	–0.5 to V <sub>CC</sub> + 0.5	V
Input diode current	I <sub>IK</sub>	±20	mA
Output diode current	I <sub>OK</sub>	±20	mA
DC output current	IOUT	±25	mA
DC V <sub>CC</sub> /ground current	Icc	±25	mA
Dewen dissinction		300 (SM8)	
Power dissipation	P <sub>D</sub> -	200 (US8)	mW
Storage temperature	T <sub>stg</sub>	-65 to 150	
Lead temperature (10 s)	TL	260	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

**Truth Table** 

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### IEC Logic Symbol



А	В	Y
L	L	Н
L	Н	L
Н	L	L
Н	Н	L

## **Operating Ranges**

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	2.0 to 6.0	V
Input voltage	V <sub>IN</sub>	0 to V <sub>CC</sub>	V
Output voltage	V <sub>OUT</sub>	0 to V <sub>CC</sub>	V
Operating temperature	T <sub>opr</sub>	-40 to 85	°C
		0 to 1000 ( $V_{CC} = 2.0 \text{ V}$ )	
Input rise and fall time	t <sub>r</sub> , t <sub>f</sub>	0 to 500 $(V_{CC} = 4.5 V)$	ns
		0 to 400 $(V_{CC} = 6.0 \text{ V})$	

#### **Electrical Characteristics**

#### **DC Characteristics**

Characteristics	Characteristics Symbol Test Condition			Ta = 25°C			Ta = -40	Unit		
Characteristics	Symbol	Test Condition		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
High-level input voltage VIH —		2.0	1.5		_	1.5				
		—	4.5	3.15		_	3.15			
			6.0	4.2	_		4.2	_	V	
						_	0.5	_	0.5	v
Low-level input voltage	VIL		_	4.5		_	1.35	_	1.35	
			6.0		_	1.8	_	1.8		
High-level output voltage $V_{OH}$ $V_{IN} = V_{II}$			2.0	1.9	2.0		1.9			
		$V_{IN} = V_{IL}$	I <sub>OH</sub> = –20 μA	4.5	4.4	4.5	_	4.4	_	
	V <sub>OH</sub>			6.0	5.9	6.0	_	5.9	_	
		I <sub>OH</sub> = -4 mA	4.5	4.18	4.31		4.13	_		
			I <sub>OH</sub> = -5.2 mA	6.0	5.68	5.80		5.63	_	V
				2.0		0.0	0.1		0.1	v
Low-level output voltage $V_{OL}$ $V_{IN} = V_{OL}$ or $V_{II}$	V <sub>IN</sub> = V <sub>IH</sub> or VIL	I <sub>OL</sub> = 20 μA	4.5	_	0.0	0.1	_	0.1		
			6.0		0.0	0.1	_	0.1		
		I <sub>OL</sub> = 4 mA	4.5		0.17	0.26	_	0.33		
		I <sub>OL</sub> = 5.2 mA	6.0		0.18	0.26		0.33		
Input leakage current	I <sub>IN</sub>	$V_{IN} = V_{CC}$	$V_{IN} = V_{CC}$ or GND			_	±0.1		±1.0	μA
Quiescent supply current	ICC	$V_{IN} = V_{CC}$	or GND	6.0	_		1.0	_	10.0	μA

#### AC Characteristics ( $C_L$ = 15pF, $V_{CC}$ = 5V, Ta = 25°C)

Characteristics	Symbol	Test Condition		Unit		
	Symbol	Test Condition	Min	Тур.	Max	Onit
Output Transition Time	t <sub>TLH</sub> t <sub>THL</sub>	—	_	4	8	ns
Propagation Delay Time	t <sub>pLH</sub> t <sub>pHL</sub>	_		6	12	ns

#### AC Characteristics ( $C_L$ = 50pF, Input: $t_r = t_f = 6$ ns)

Characteristics Symbol	Symbol	Dol Test Condition		Ta = 25°C			Ta = -40	Unit	
	Symbol	Test Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
Output Transition Time	t <del></del>		2.0		25	75	—	95	
	—	4.5		7	15	_	19	ns	
	THL		6.0	_	6	13	—	16	
Propagation delay time t <sub>pLH</sub>	<b>f</b>		2.0	_	25	75	—	95	
	•	_	4.5	_	9	15	—	19	ns
	чрнц		6.0	_	8	13	—	16	
Input capacitance	C <sub>IN</sub>				5	10	_	10	pF
Power dissipation capacitance	C <sub>PD</sub>		(Note 1)		21	_	_	_	pF

Note 1: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

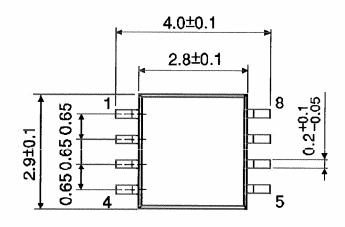
Average operating current can be obtained by the equation:  $I_{CC\ (opr)}=C_{PD}\cdot V_{CC}\cdot f_{IN}+I_{CC}/2$ 

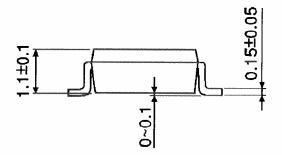
## <u>TOSHIBA</u>

## Package Dimensions

#### SSOP8-P-0.65

Unit : mm

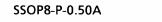




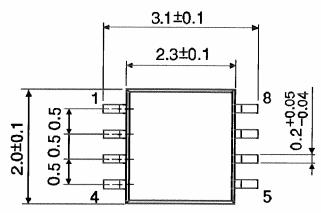
Weight: 0.02 g (typ.)

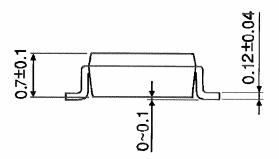
# <u>TOSHIBA</u>

## Package Dimensions



Unit : mm





Weight: 0.01 g (typ.)

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