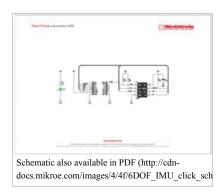
## Flash 3 click

From MikroElektonika Documentation

Flash 3 click carries an ISSI IS25LP128 IC with 128 Mbit capacity.

## **Features and usage notes**



The high-performance Flash chip operates at 50MHz at Normal and 133MHz at Fast Read speeds.

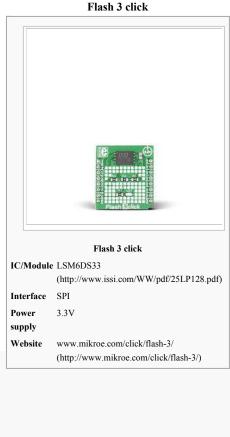
It is specified to standard 100,000 erase/program cycles with more than 20 years of data retention. The data can be erased in sectors or blocks and programmed with 1 to 256 bytes per page.

Each chip has a 128-bit unique ID for each device.

Flash 3 communicates with the target board through the mikroBUS<sup>TM</sup> SPI interface with additional functionality provided by HOLD, CE and WP pins. It is designed to use a 3.3V power supply only.

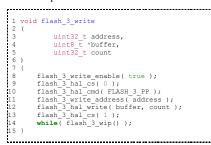
The click board also comes with a firmware library

(http://libstock.mikroe.com/projects/view/1895/flash-3-click) which is very similar to the one used for Flash 2 click, documented in this learn article (http://learn.mikroe.com/this-nand-nor-that-nand/).



## Programming

This example shows the Flash 3 click write routine.



Code examples that demonstrate the usage of Flash 3 click with MikroElektronika hardware, written for mikroC for ARM, and FT90x are available on [Libstock : http://libstock.mikroe.com/projects/view/1895/flash-3-click Libstock].

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## Resources

- Vendor's data sheet (http://www.issi.com/WW/pdf/25LP128.pdf)
- Flash 3 click Libstock example (http://libstock.mikroe.com/projects/view/1895/flash-3-click)
- mikroBUS standard specifications (http://www.mikroe.com/downloads/get/1737/mikrobus\_specification.pdf)