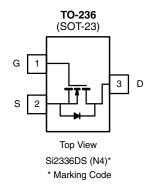




N-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A) ^a	Q _g (Typ.)			
	0.042 at V _{GS} = 4.5 V	5.2				
30	0.046 at V _{GS} = 2.5 V	4.9	5.7 nC			
	0.052 at V _{GS} = 1.8 V	4.1				



Ordering Information: Si2336DS-T1-GE3 (Lead (Pb)-free and Halogen-free)

FEATURES

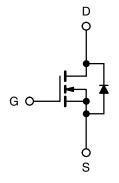
- TrenchFET® Power MOSFET
- 100 % R_q Tested
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912



HALOGEN FREE

APPLICATIONS

- DC/DC Converters
- **Boost Converters**



N-Channel MOSFET

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	30	V
Gate-Source Voltage		V _{GS}	± 8	
	T _C = 25 °C		5.2	
Continuous Drain Current (T _J = 150 °C)	T _C = 70 °C		4.1	
Continuous Diam Current (1) = 130 C)	T _A = 25 °C	I _D	4.3 ^{b, c}	
	T _A = 70 °C		3.5 ^{b, c}	A
Pulsed Drain Current		I _{DM}	20	
	T _C = 25 °C		1.5	
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	1 ^{b, c}	
	T _C = 25 °C		1.8	
Maximum Dawar Dissination	T _C = 70 °C	P _D	1.1	w
Maximum Power Dissipation	T _A = 25 °C	- D	1.25 ^{b, c}	vv
	T _A = 70 °C		0.8 ^{b, c}	
Operating Junction and Storage Temperature	T _J , T _{stg}	- 55 to 150	°C	
Soldering Recommendations (Peak Tempera		260		

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Typical	Maximum	Unit			
Maximum Junction-to-Ambient ^{b, d}	t ≤ 5 s	R _{thJA}	80	100	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	55	70	C/VV		

Notes:

- a. $T_C = 25$ °C.
- b. Surface mounted on 1" x 1" FR4 board.
- d. Maximum under steady state conditions is 130 °C/W.



SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = 250 μA		31		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_{J}$			- 2.7			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_{D} = 250 \mu A$	0.4		1	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zoro Coto Voltogo Dvoin Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V			1	μА	
Zero Gate Voltage Drain Current		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	10			Α	
		$V_{GS} = 4.5 \text{ V}, I_D = 3.8 \text{ A}$	0.034 0.04		0.042		
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 2.5 \text{ V}, I_D = 3.6 \text{ A}$		0.038	0.046		
		$V_{GS} = 1.8 \text{ V}, I_D = 2 \text{ A}$		0.041	0.052		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 15 \text{ V}, I_D = 3.8 \text{ A}$		30		S	
Dynamic ^b					•		
Input Capacitance	C _{iss}			560		pF	
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		60			
Reverse Transfer Capacitance	C _{rss}			27			
T. 10 . 0	Qg	$V_{DS} = 15 \text{ V}, V_{GS} = 8 \text{ V}, I_D = 3.4 \text{ A}$		10	15	nC	
Total Gate Charge		-		5.7	8.6		
Gate-Source Charge	Q _{gs}	V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 3.4 A		0.85			
Gate-Drain Charge	Q _{gd}			0.75			
Gate Resistance	R_{g}	f = 1 MHz	0.6	3	6	Ω	
Turn-On Delay Time	t _{d(on)}			6	12		
Rise Time	t _r	V_{DD} = 15 V, R_L = 4.3 Ω		10	20	1	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 3.5 \text{ A}, V_{GEN} = 4.5 \text{ V}, R_g = 1 \Omega$		20	40	1	
Fall Time	t _f			10	20	1	
Turn-On Delay Time	t _{d(on)}			5	10	ns	
Rise Time	t _r	V_{DD} = 15 V, R_L = 4.3 Ω		10	20		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 3.5 \text{ A}, V_{GEN} = 8 \text{ V}, R_g = 1 \Omega$		17	30		
Fall Time	t _f	-		10	20	1	
Drain-Source Body Diode Characteristic							
Continuous Source-Drain Diode Current	Is	T _C = 25 °C			1.5		
Pulse Diode Forward Current	I _{SM}				20	A	
Body Diode Voltage	V _{SD}	$I_S = 3.5 \text{ A}, V_{GS} = 0 \text{ V}$		0.8	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			15	30	ns	
Body Diode Reverse Recovery Charge	Q _{rr}			6	12	nC	
Reverse Recovery Fall Time	t _a	$I_F = 3.5 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		8		ns	
Reverse Recovery Rise Time	t _b			7	 		

Notes:

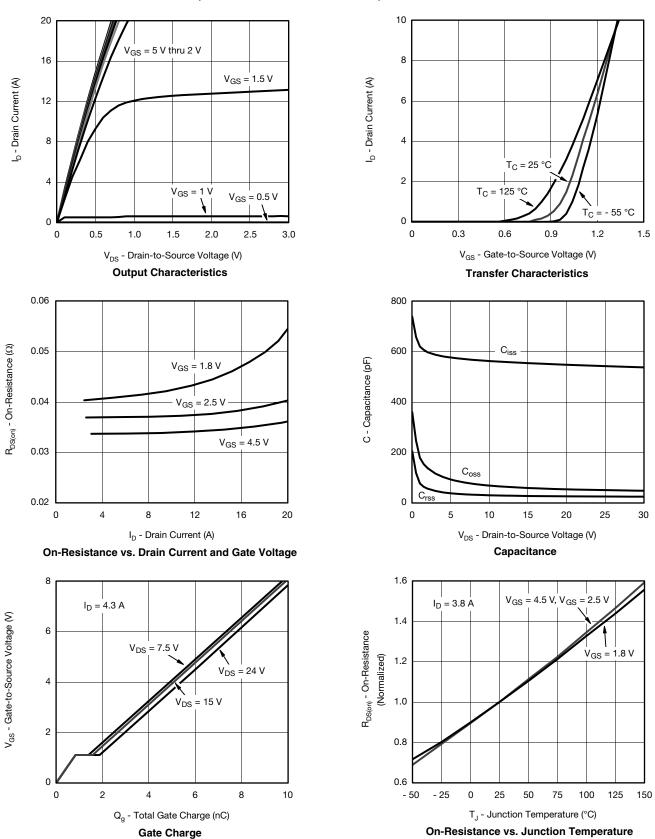
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %

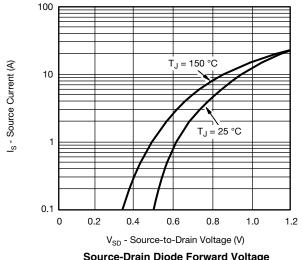
b. Guaranteed by design, not subject to production testing.

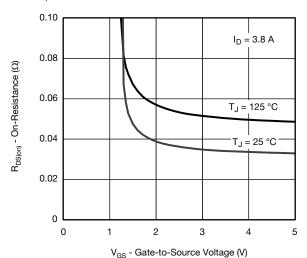


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



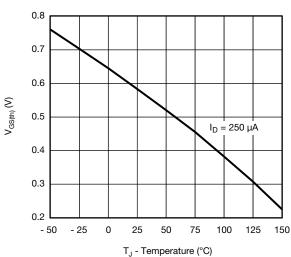
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

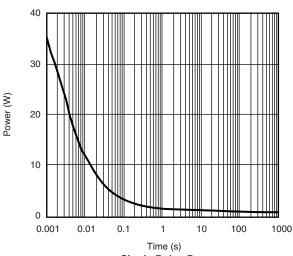




Source-Drain Diode Forward Voltage

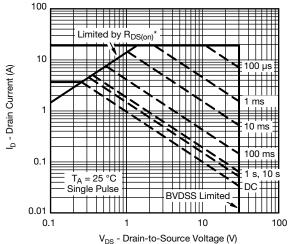
On-Resistance vs. Gate-to-Source Voltage





Threshold Voltage

Single Pulse Power



 $V_{\rm DS}$ - Drain-to-Source Voltage (V) * V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

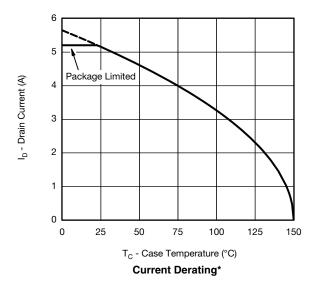
Safe Operating Area, Junction-to-Ambient

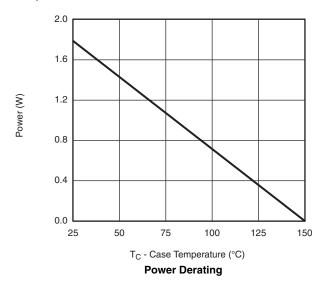






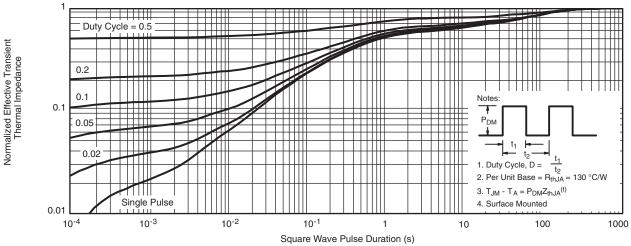
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



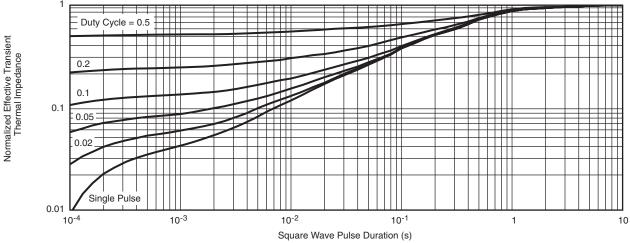


^{*} The power dissipation P_D is based on $T_{J(max.)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



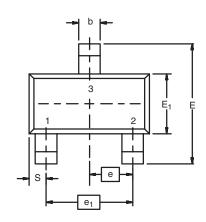
Normalized Thermal Transient Impedance, Junction-to-Ambient

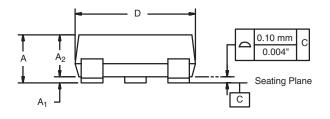


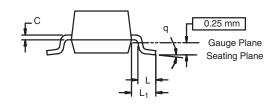
Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?71978.

SOT-23 (TO-236): 3-LEAD







Dim	MILLI	METERS	INCHES			
	Min	Max	Min	Max		
Α	0.89	1.12	0.035	0.044		
A ₁	0.01	0.10	0.0004	0.004		
A ₂	0.88	1.02	0.0346	0.040		
b	0.35	0.50	0.014	0.020		
С	0.085	0.18	0.003	0.007		
D	2.80	3.04	0.110	0.120		
Е	2.10	2.64	0.083	0.104		
E ₁	1.20	1.40	0.047	0.055		
е	0.95 BSC		0.0374 Ref			
e ₁	1.9	0.95 BSC 1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024		
L ₁	0.64 Ref		0.025 Ref			
S	0.50 Ref		0.020 Ref			
q	3°	8°	3°	8°		
FCN: S-03946-Rev K 09-	lul-01					

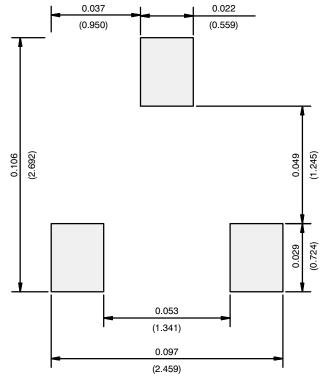
ECN: S-03946-Rev. K, 09-Jul-01

DWG: 5479

Document Number: 71196 www.vishay.com 09-Jul-01



RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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