



DMN6070SFCL

Product Summary

V _{(BR)DSS}	R _{DS(ON)} max	l _D max T _A = +25°C
001/	85 mΩ @ V _{GS} = 10V	3.0A
60V	120 mΩ @ V _{GS} = 4V	2.5A

Description

This new generation MOSFET has been designed to minimize the onstate resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Power Management Functions
- Analog Switch

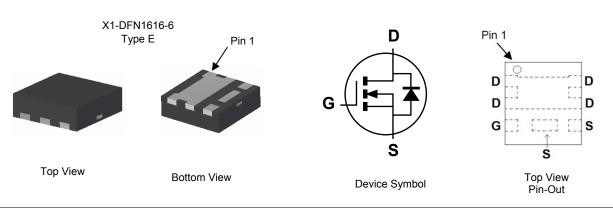
60V N-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- Typical off board profile of 0.5mm ideally suited for thin applications
- Low R_{DS(ON)} minimizes conduction losses
- PCB footprint of 2.56mm²
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 standards for High Reliability

Mechanical Data

- Case: X1-DFN1616-6 Type E
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Lead Free Plating (NiPdAu Finish over Copper leadframe)
- Terminals: Solderable per MIL-STD-202, Method 208 @
- Weight: 0.04 grams (approximate)



Ordering Information (Note 4)

Product	Reel size (inches)	Tape Width (mm)	Quantity per Reel
DMN6070SFCL-7	7	8	3,000

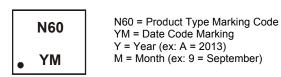
Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html

Marking Information



Date Code Key												
Year	201	1	2012		2013	20	14	2015		2016	2	2017
Code	Y		Z		А	I	3	С		D		E
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Drain-Source Voltage	V _{DSS}	60	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current (Note 6) V _{GS} = 10V	ID	3.0 2.5	A
Pulsed Drain Current (10µs pulse, Duty cycle = 1%)	I _{DM}	10	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

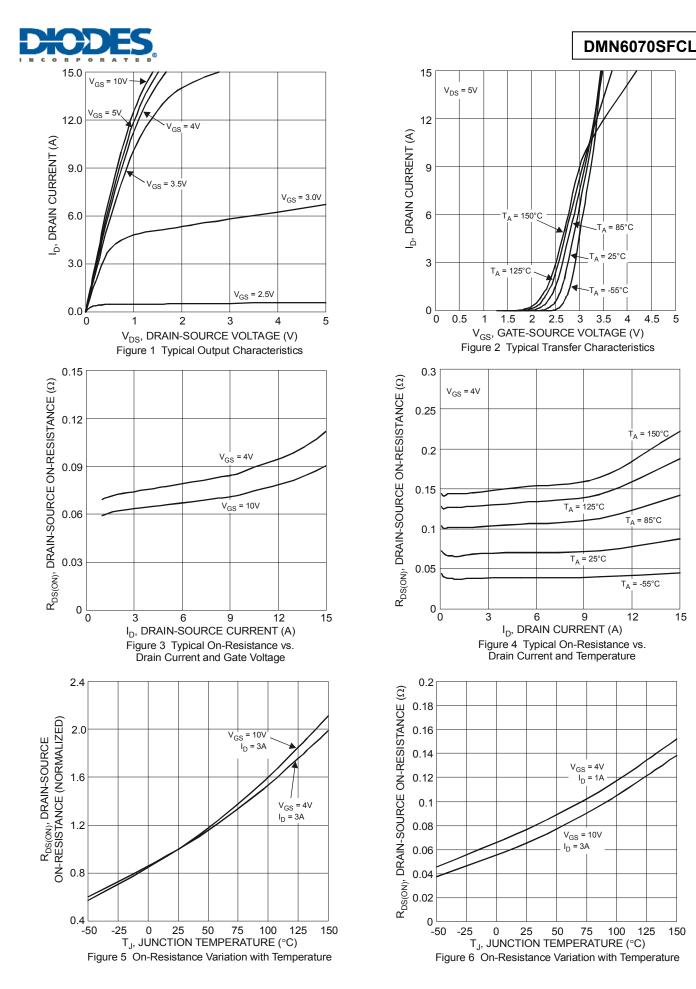
Characteristic		Symbol	Value	Units
Total Power Dissigation	(Note 5)	D	0.6	W
	(Note 6)	P _D	1.8	W
The second Decision and the stimula Analysis of	(Note 5)	P	200	°CAN
Thermal Resistance, Junction to Ambient	(Note 6)	R _{0JA}	67	°C/W
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

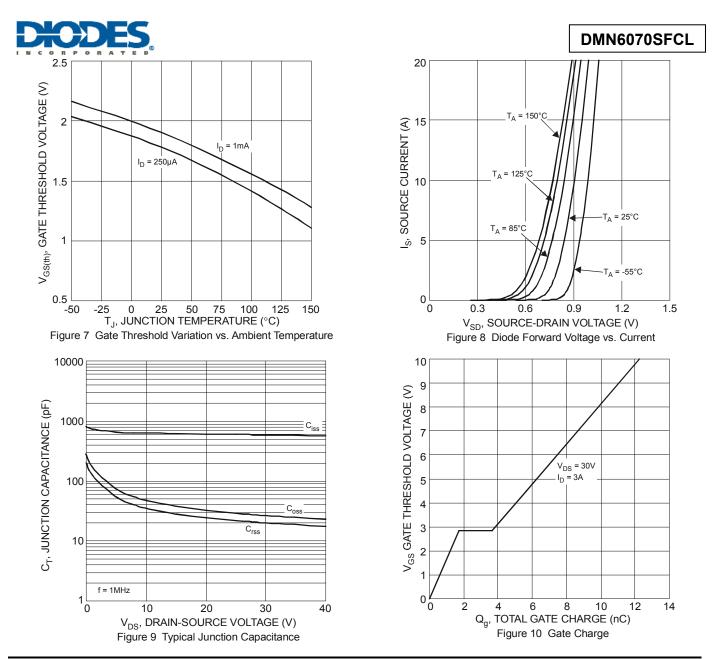
Electrical Characteristics N-CHANNEL (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Мах	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						-
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	V_{GS} = 0V, I _D = 250µA
Zero Gate Voltage Drain Current T _J = +25°C	IDSS		—	1.0	μA	V _{DS} = 60V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}		_	±100	nA	V_{GS} = ±16V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(th)}	1	—	3	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance			67	85	mΩ	V _{GS} = 10V, I _D = 1.5A
	R _{DS (ON)}		74	120	11122	V _{GS} = 4V, I _D = 0.5A
Forward Transfer Admittance	Y _{fs}		2.6		S	V _{DS} = 5V, I _D = 1.5A
Diode Forward Voltage	V _{SD}		0.7	1.2	V	V _{GS} = 0V, I _S = 3A
DYNAMIC CHARACTERISTICS (Note 8)						-
Input Capacitance	C _{iss}		606	-	pF	
Output Capacitance	Coss		32.6	-	pF	V _{DS} = 20V, V _{GS} = 0V, f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}		24.6		pF	
Gate Resistance	Rg	_	1.5	_	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz
Total Gate Charge (V _{GS} =10V)	Qg	_	12.3	_	nC	
Total Gate Charge (V _{GS} =4.5V)	Qg	_	5.6	_	nC	
Gate-Source Charge	Qgs	_	1.7	_	nC	V _{DS} = 30V, I _D = 3A
Gate-Drain Charge	Q _{gd}	_	1.9	_	nC	
Turn-On Delay Time	t _{D(on)}	_	3.5	_	ns	
Turn-On Rise Time	tr	_	4.1	_	ns	V _{GS} = 10V, V _{DS} = 30V,
Turn-Off Delay Time	t _{D(off)}	_	35	_	ns	$R_G = 20\Omega, R_L = 50\Omega$
Turn-Off Fall Time	t _f	_	11	_	ns]

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout

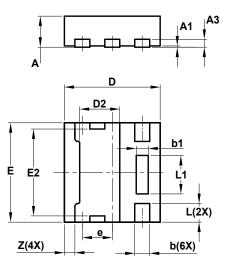
Device mounted on FR-4 substrate PC board, 202 copper, with thermal vias to bottom layer 1inch square copper plate
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.





Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.

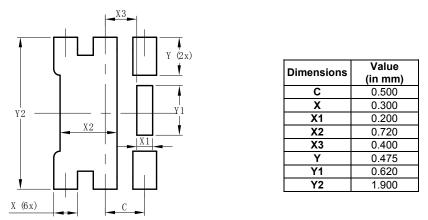


X1-DFN1616-6 Type E							
Dim	Min Max Typ						
Α	0.47	0.53	0.50				
A1	0	0.05	0.02				
A3		-	0.13				
b	0.20	0.30	0.25				
b1	0.10	0.30	0.20				
D	1.55	1.65	1.60				
D2	0.57	0.77	0.67				
ш	1.55	1.65	1.60				
E2	1.30	1.50	1.40				
e	_	_	0.50				
L	0.25	0.35	0.30				
L1	0.52	0.72	0.62				
Z	_		0.175				
All Dimensions in mm							



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for latest version.



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