

Note: Available in Tape & Reel. Append the letters "TR" to the part number. (i.e. LX8580-00CDD-TR)

LX8580

PRODUCTION DATA SHEET

ABSOLUTE MAXIMUM RATINGS (Note 1) Power Dissipation Internally Limited TAB IS OUTPUT Operating Junction Temperature CTRL OUTPUT Plastic (P Package) 150°C ADJUST Storage Temperature Range -65°C to 150°C SENSE Lead Temperature (Soldering, 10 seconds) 300°C RoHS Peak Package Solder Reflow Temp. (40 seconds max. exposure)...... 260°C (+0, -5) **P PACKAGE** Note 1. Exceeding these ratings could cause damage to the device. All voltages are with respect (Top View) to Ground. Currents are positive into, negative out of the specified terminal. TAB IS OUTPUT P PACKAGE: $\Box V_{PWR}$ 4 3 2 SENSE **DD PACKAGE:** 1 **DD PACKAGE** (Top View) RoHS 100% Matte Tin Lead Finish Junction Temperature Calculation: $T_{I} = T_{A} + (P_{D} \times \theta_{IA}).$

The $\theta_{i,j}$ numbers are guidelines for the thermal performance of the device/pc-board system. All of the above assume no ambient airflow.

> V_{CTRL} O $\stackrel{\text{Power}}{=} V_{\text{PWR}}$ Current Limiting Amplifier Thermal Overload O SENSE Voltage Regulation Amplifier $\mathsf{V}_{\mathsf{REF}}$ ADJUST



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(Unless otherwise specified, these specifications apply over the operating ambient temperatures $0^{\circ}C \le T_{A} \le 125^{\circ}C$. Low duty cycle pulse testing techniques are used which maintains junction and case temperatures equal to the ambient temperature.)

| Parameter | Symbol | Test Conditions | LX8580 | | | Units |
|--|--------------------------------------|---|--------|-------|-------|-------|
| Falalletel | Symoor | Test collutions | Min. | Тур. | Max. | Units |
| Reference Voltage | V _{REF} | $V_{CTRL} = 2.75V, V_{PWR} = 2V, I_{LOAD} = 10mA, T_1 = 25^{\circ}C$ | 1.243 | 1.250 | 1.257 | V |
| | | $V_{CTRL} = 2.7V \text{ to } 6V, V_{PWR} = 1.75V \text{ to } 5.5V, I_{OUT} = 10\text{ mA to } 4\text{ A}$ | 1.237 | 1.250 | 1.263 | V |
| | | $V_{CTRL} = 2.7V \text{ to } 6V, V_{PWR} = 2.05V \text{ to } 5.5V, I_{OUT} = 10\text{ mA to } 7\text{ A}$ | 1.237 | 1.250 | 1.263 | ۷ |
| Line Regulation | $\Delta V_{REF} (V_{IN})$ | $V_{CTRL} = 2.5V \text{ to } 12V, V_{PWR} = 1.75V \text{ to } 5.5V, I_{LOAD} = 10\text{mA}, T_{J} = 25^{\circ}\text{C}$ | | 1 | 3 | m۷ |
| | | $V_{CTRL} = 2.5V \text{ to } 12V, V_{PWR} = 1.75V \text{ to } 5.5V, I_{LOAD} = 10\text{ mA}$ | | 2 | 6 | m۷ |
| Load Regulation | ΔV_{REF} (I _{OUT}) | $V_{CTRL} = 2.75V, V_{PWR} = 2.1V, I_{LOAD} = 10mA \text{ to } 7A (V_{ADJ} = 0)$ | | 2 | 7 | m٧ |
| Thermal Regulation | ΔV_{OUT} (Pwr) | 30ms Pulse | | 0.002 | 0.020 | %/W |
| Thermal Resistance | R _{ejt} | Control Circuitry/Power Transistor | | 0.65 | 2.70 | °C/W |
| Ripple Rejection | | $V_{CTRL} = V_{PWR} = 3.75V \text{ Avg}, V_{RIPPLE} = 1V_{PP}, V_{ADJ} = 0V, I_{OUT} = 4A$ | 60 | 80 | | dB |
| Control Pin Current (Note 3) | I _{CTRL} | $V_{CTRL} = 2.75V, V_{PWR} = 2.05V, I_{LOAD} = 100mA$ | | 3 | 10 | mA |
| | | $V_{CTRL} = 2.75V, V_{PWR} = 2.05V, I_{LOAD} = 4A$ | | 30 | 60 | mA |
| | | $V_{CTRL} = 2.75V, V_{PWR} = 1.75V, I_{LOAD} = 4A$ | | 33 | 70 | mA |
| | | $V_{CTRL} = 2.75V, V_{PWR} = 2.05V, I_{LOAD} = 7A$ | | 60 | 120 | mA |
| Adjust Pin Current | I _{ADJ} | $V_{CTRL} = 2.75V, V_{PWR} = 2.05V, I_{LOAD} = 0mA (V_{ADJ} = 0)$ | | 60 | 120 | μA |
| Current Limit | I _{e(MAX)} | $V_{CTRL} = 2.75V, V_{PWR} = 2.05V, \Delta V_{OUT} = 100mV (V_{ADJ} = 0)$ | 7.1 | 9 | | Α |
| Dropout Voltage (Control Section) | V _{CTRL} - V _{OUT} | $V_{PWR} = 2.05V, I_{LOAD} = 100mA$ | | 0.90 | 1.15 | V |
| Minimum V _{CTRL} (Note 2) | | $V_{PWR} = 2.05V$, $I_{LOAD} = 1A$ | | 1.00 | 1.15 | V |
| (V _{PWR} - V _{OUT}) | | $V_{PWR} = 2.05V, I_{LOAD} = 2.75A$ | | 1.05 | 1.18 | V |
| | | $V_{PWR} = 2.05V$, $I_{LOAD} = 4A$ | | 1.06 | 1.20 | V |
| | | $V_{PWR} = 2.05V$, $I_{LOAD} = 7A$ | | 1.10 | 1.30 | V |
| Dropout Voltage (Power Section) | V _{PWR} - V _{OUT} | $V_{CTRL} = 2.75V, I_{LOAD} = 100mA$ | | 0.05 | 0.17 | V |
| Minimum V _{PWR} (Note 2) | | $V_{CTRL} = 2.75V, I_{LOAD} = 1A$ | | 0.10 | 0.22 | V |
| (V _{PWR} - V _{OUT}) | | $V_{CTRL} = 2.75V, I_{LOAD} = 2.75A$ | | 0.20 | 0.38 | V |
| $(V_{ADJ} = 0)$ | | $V_{CTRL} = 2.75V, I_{LOAD} = 4A, T_{J} = 25^{\circ}C$ | | 0.30 | 0.40 | ۷ |
| | | $V_{\text{CTRL}} = 2.75 \text{V}, I_{\text{LOAD}} = 4 \text{A}$ | | 0.30 | 0.50 | ۷ |
| | | $V_{CTRL} = 2.75V, I_{LOAD} = 7A, T_{J} = 25^{\circ}C$ | | 0.52 | 0.62 | V |
| | | $V_{\text{CTRL}} = 2.75 \text{V}, I_{\text{LOAD}} = 7 \text{A}$ | | 0.56 | 0.80 | ۷ |
| Minimum Load Current | | $V_{CTRL} = 5V, V_{PWR} = 3.3V, V_{ADJ} = 0V$ | | 5 | 10 | mA |

Note 2. Dropout is caused by either minimum control voltage (V_{CTRL}) or minimum power voltage (V_{PWR}) . Both parameters are specified with respect to the output voltage. The specifications represent the minimum input/output voltage required to maintain 1% regulation.

Note 3. The control pin current is the drive current required for the output transistor. This current will track output current with roughly a 1:100 ratio. The minimum value is equal to the quiescent current of the device.

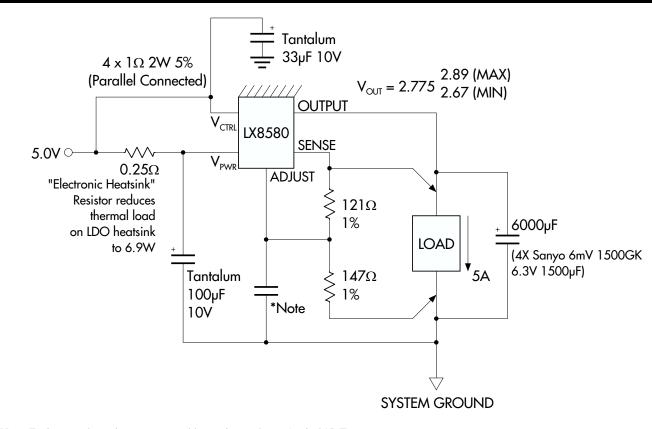


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| SENSE | 1 | This pin is the positive side of the reference voltage for the device. With this pin it is possible to Kelvin-sense the output voltage at load. | | | | | | |
|------------------|---|--|--|--|--|--|--|--|
| ADJUST | 2 | This pin is the negative side of the reference voltage for the device. Ripple rejection can be improved by adding a small bypass capacitor from the Adjust pin to ground. The capacitor value should be selected so that $C_R \le 1/2\pi F_R R_A$, where F_R is the ripple frequency and R_A is the value of the resistor between the output and sense leads of the LX8580. | | | | | | |
| OUTPUT | 3 | This is the power output of the device. | | | | | | |
| $V_{\rm CTRL}$ | 4 | This pin is the voltage supply pin for the control circuitry of the device. The current flow into this pin will be about 1% of the output current. For the device to regulate, the voltage at this pin must be between 1.0V and 1.3V greater than the output voltage. | | | | | | |
| V _{PWR} | 5 | This is the collector of the power section of the LX8580. The output load current is supplied through this pin. For the device to regulate, the voltage at this pin must be between 0.1V and 0.8V greater than the output voltage (higher output currents require higher voltages between V_{PWR} and OUTPUT). | | | | | | |



*Note: For improved transient response, add capacitor as shown (typical $15\mu F$).

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