150 mA CMOS LDO Regulator

Description

The CAT6217 is a 150 mA CMOS low dropout regulator that provides fast response time during load current and line voltage changes.

The quick-start feature allows the use of an external bypass capacitor to reduce the overall output noise without affecting the turn-on time of just $150 \ \mu s$.

With zero shutdown current and low ground current of $55 \,\mu A$ typical, the CAT6217 is ideal for battery–operated devices with supply voltages from 2.3 V to 5.5 V. An internal under voltage lockout circuit disables the output at supply voltages under 2.1 V typical.

The CAT6217 offers 1% initial accuracy and low dropout voltage, 90 mV typical at 150 mA. Stable operation is provided with a 1 μ F ceramic capacitor, reducing required board space and component cost.

Other features include output short-circuit current limit and thermal protection.

The device is available in the low profile (1 mm max height) 5-lead TSOT-23 package.

Features

- Guaranteed 150 mA Output Current
- Low Dropout Voltage of 90 mV Typical at 150 mA
- Stable with 1 µF Ceramic Output Capacitor
- External 10 nF Bypass Capacitor for Low Noise
- Quick-start Feature
- No-load Ground Current of 55 µA Typical
- Full-load Ground Current of 80 µA Typical
- $\pm 1.0\%$ Initial Accuracy (V_{OUT} ≥ 2.0 V)
- $\pm 2.0\%$ Accuracy Over Temperature (V_{OUT} ≥ 2.0 V)
- "Zero" Current Shutdown Mode
- Current Limit and Under Voltage Lockout
- Thermal Protection
- 5-lead TSOT-23 Package
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

Applications

- Cellular Phones
- Battery-powered Devices
- Consumer Electronics

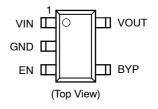


ON Semiconductor®

http://onsemi.com







MARKING DIAGRAM



RT = CAT6217 Device Code

Y = Production Year (last digit)

M = Production Month: 1 - 9, A, B, C

PIN FUNCTION

Pin #	Name	Function			
1	VIN	Supply voltage input.			
2	GND	Ground reference.			
3	EN	Enable input (active high); a 2.5 M pull-down resistor is provided.			
4	BYP	Optional bypass capacitor connec- tion for noise reduction and PSRR enhancing.			
5	VOUT	LDO Output Voltage.			

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 9 of this data sheet.

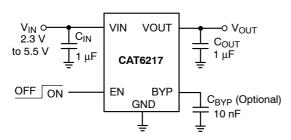


Figure 1. Typical Application Circuit

Pin Function

VIN is the supply pin for the LDO. A small 1 μ F ceramic bypass capacitor is required between the V_{IN} pin and ground near the device. When using longer connections to the power supply, C_{IN} value can be increased without limit. The operating input voltage range is from 2.3 V to 5.5 V.

EN is the enable control logic (active high) for the regulator output. It has a 2.5 M Ω pull-down resistor, which assures that if EN pin is left open, the circuit is disabled.

VOUT is the LDO regulator output. A small 1 μ F ceramic bypass capacitor is required between the V_{OUT} pin and ground for stability. For better transient response, its value can be increased to 4.7 μ F.

Table 1. ABSOLUTE MAXIMUM RATINGS

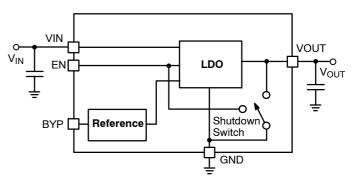


Figure 2. CAT6217 Functional Block Diagram

The capacitor should be located near the device. ESR domain is 5 m Ω to 500 m Ω . V_{OUT} can deliver a maximum guaranteed current of 150 mA. A 250 Ω internal shutdown switch discharges the output capacitor in the no–load condition.

GND is the ground reference for the LDO. The pin must be connected to the ground plane on the PCB.

BYP is the reference bypass pin. An optional $0.01 \,\mu\text{F}$ capacitor can be connected between BYP pin and GND to reduce the output noise and enhance the PSRR at high frequency.

Parameter	Rating	Unit
V _{IN}	0 to 6.5	V
V _{EN} , V _{OUT}	–0.3 to V _{IN} + 0.3	V
Junction Temperature, T _J	+150	°C
Power Dissipation, P _D	Internally Limited (Note 1)	mW
Storage Temperature Range, T _S	-65 to +150	°C
Lead Temperature (soldering, 5 sec.)	260	°C
ESD Rating (Human Body Model)	3	kV

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Table 2. RECOMMENDED OPERATING CONDITIONS (Note 2)

Parameter	Range	Unit
V _{IN}	2.3 to 5.5	V
V _{EN}	0 to V _{IN}	V
Junction Temperature Range, T _J	-40 to +125	°C
Package Thermal Resistance (SOT23–5), θ_{JA}	235	°C/W

NOTE: Typical application circuit with external components is shown above.

The maximum allowable power dissipation at any T_A (ambient temperature) is P_{Dmax} = (T_{Jmax} - T_A)/θ_{JA}. Exceeding the maximum allowable power dissipation will result in excessive die temperature, and the regulator will go into thermal shutdown.

2. The device is not guaranteed to work outside its operating rating.

Table 3. ELECTRICAL OPERATING CHARACTERISTICS (Note 3)

 $(V_{IN} = V_{OUT} + 1.0 \text{ V}, V_{EN} = \text{High}, I_{OUT} = 100 \ \mu\text{A}, C_{IN} = C_{OUT} = 1 \ \mu\text{F}$, ambient temperature of 25°C (over recommended operating conditions unless specified otherwise). **Bold numbers** apply for the entire junction temperature range.)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{OUT-ACC}	Output Voltage Accuracy	Initial accuracy for $V_{OUT} \ge 2.0 V$	-1.0		+1.0	%
		(Note 6)	-2.0		+2.0	
TC _{OUT}	Output Voltage Temp. Coefficient			40		ppm/°C
V_{R-LINE}	Line Regulation	V _{IN} = V _{OUT} + 1.0 V to 5.5 V	-0.2	±0.1	+0.2	%/V
			-0.4		+0.4	
V _{R-LOAD}	Load Regulation	I _{OUT} = 100 μA to 150 mA		0.6	1.0	%
					1.3	
V _{DROP}	Dropout Voltage (Note 4)	I _{OUT} = 150 mA		90	125	mV
					150	
I _{GND}	Ground Current	l _{OUT} = 0 μA		55	75	μA
					90	
		I _{OUT} = 150 mA		80		
I _{GND-SD} Shutdo	hutdown Ground Current	V _{EN} < 0.4 V			1	μA
					2	
PSRR Power Supply Rejection	Power Supply Rejection Ratio	$f = 1 \text{ kHz}, C_{BYP} = 10 \text{ nF}$		64		dB
		f = 20 kHz, C _{BYP} = 10 nF		54		
I _{SC}	Output short circuit current limit	V _{OUT} = 0 V		350		mA
T _{ON}	Turn-On Time	C _{BYP} = 10 nF		150		μs
e _N	Output Noise Voltage (Note 5)	BW = 10 Hz to 100 kHz		45		μVrms
R _{OUT-SH}	Shutdown Switch Resistance			250		Ω
R _{EN}	Enable pull-down resistor			2.5		MΩ
V _{UVLO}	Under-voltage lock out (UVLO) threshold			2.1		V
ESR	C _{OUT} equivalent series resistance		5		500	mΩ

ENABLE INPUT

V _{HI}	Logic High Level	V _{IN} = 2.3 to 5.5 V	1.8			V
		V _{IN} = 2.3 to 5.5 V, 0°C to +125°C junction temperature	1.6			
V _{LO}	Logic Low Level	V _{IN} = 2.3 to 5.5 V			0.4	V
I _{EN}	Enable Input Current	V _{EN} = 0.4 V		0.15	1	μΑ
		V _{EN} = V _{IN}		1.5	4	

THERMAL PROTECTION

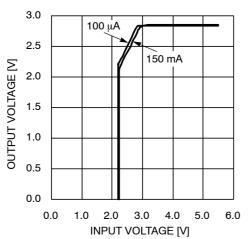
T _{SD}	Thermal Shutdown		160	°C
T _{HYS}	Thermal Hysteresis		10	°C

 Specification for 2.80 V output version unless specified otherwise.
Dropout voltage is defined as the input-to-output differential at which the output voltage drops 2% below its nominal value. During test, the input voltage stays always above the minimum 2.3 V.

5. Specification for 1.8 V output version.

6. For V_{OUT} < 2.0 V, the initial accuracy is ±2% and across temperature ±3%.







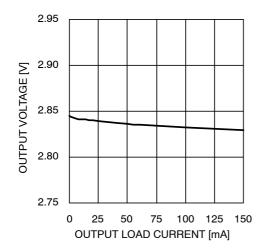


Figure 5. Load Regulation

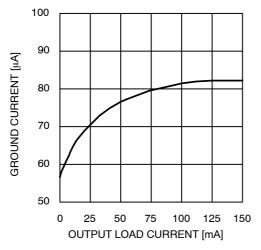


Figure 7. Ground Current vs. Load Current

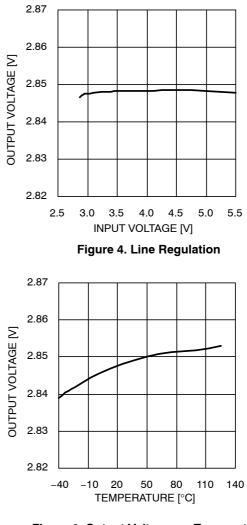


Figure 6. Output Voltage vs. Temperature

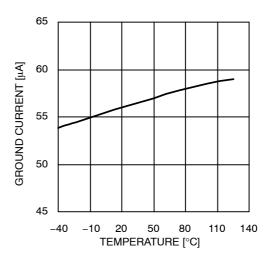


Figure 8. Ground Current vs. Temperature

TYPICAL CHARACTERISTICS (shown for 2.80 V output option)

(V_{IN} = 3.85 V, I_{OUT} = 100 μ A, C_{IN} = C_{OUT} = 1 μ F, C_{BYP} = 10 nF, T_A = 25°C unless otherwise specified.)

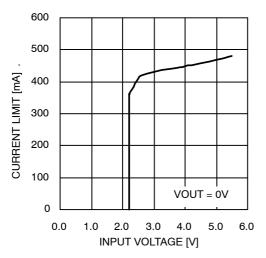


Figure 9. Output Short-circuit Current Limit

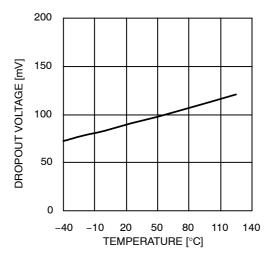


Figure 11. Dropout vs. Temperature (150 mA Load)

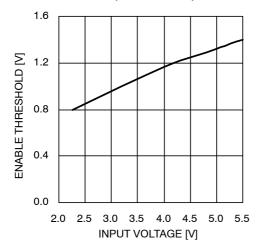


Figure 13. Enable Threshold vs. Input Voltage

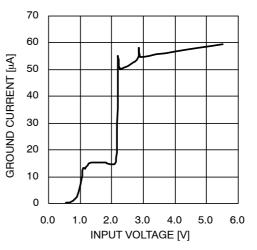


Figure 10. Ground Current vs. Input Voltage

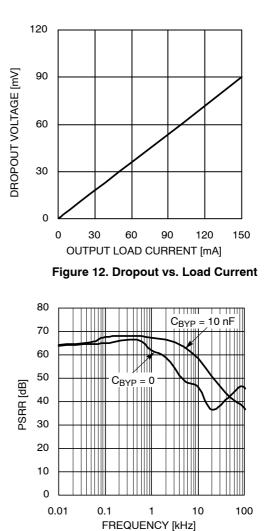


Figure 14. PSRR vs. Frequency (10 mA Load)



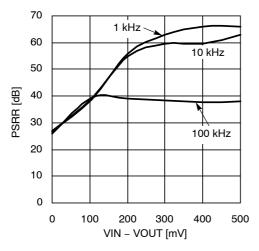


Figure 15. PSRR (30 mA Load)

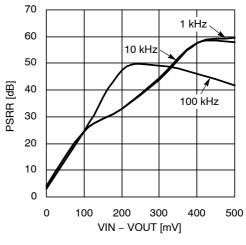


Figure 16. PSRR (150 mA Load)



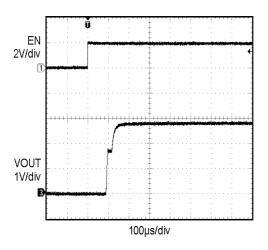


Figure 17. Enable Turn-on (100 µA Load)

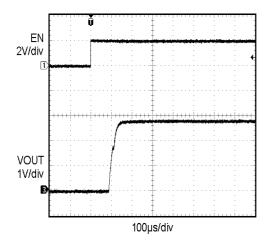
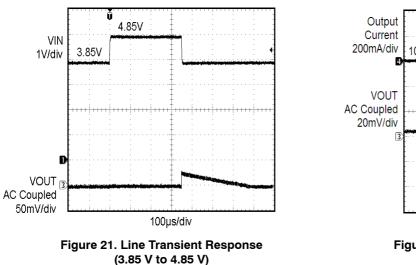


Figure 19. Enable Turn-on (150 mA Load)



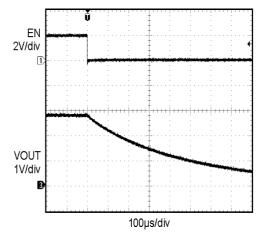


Figure 18. Enable Turn-off (100 µA Load)

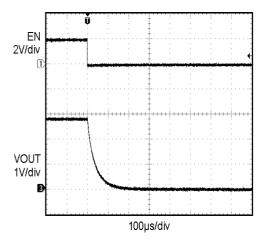
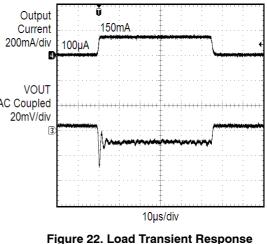
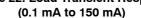


Figure 20. Enable Turn-off (150 mA Load)

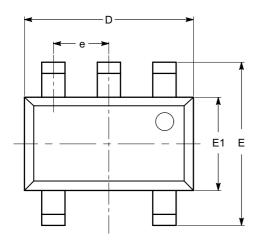




Note: All transient characteristics are generated using the evaluation board CAT621XEVAL1.

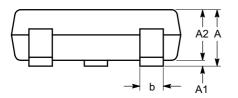
PACKAGE DIMENSIONS

TSOT-23, 5 LEAD CASE 419AE-01 ISSUE O



SYMBOL MIN NOM MAX А 1.00 A1 0.01 0.05 0.10 A2 0.80 0.87 0.90 0.45 b 0.30 с 0.12 0.15 0.20 2.90 BSC D Е 2.80 BSC E1 1.60 BSC 0.95 TYP е L 0.30 0.40 0.50 L1 0.60 REF L2 0.25 BSC 0° 8° θ

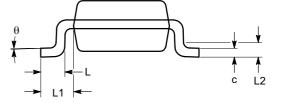
TOP VIEW



SIDE VIEW

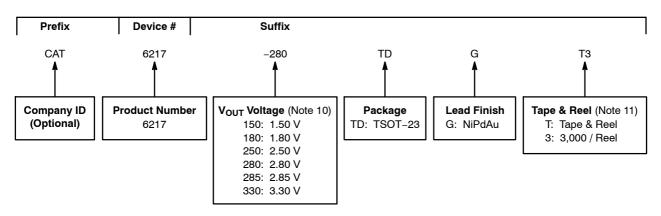
Notes:

- (1) All dimensions are in millimeters. Angles in degrees.
- (2) Complies with JEDEC MO-193.



END VIEW

Example of Ordering Information (Note 9)



ORDERING INFORMATION

Orderable Part Number	V _{OUT} Voltage	Package	Shipping
CAT6217-150TDGT3	1.50 V	TSOT-23	3,000 / Tape & Reel
CAT6217-180TDGT3	1.80 V	TSOT-23	3,000 / Tape & Reel
CAT6217-250TDGT3	2.50 V	TSOT-23	3,000 / Tape & Reel
CAT6217-280TDGT3	2.80 V	TSOT-23	3,000 / Tape & Reel
CAT6217-285TDGT3 (Note 10)	2.85 V	TSOT-23	3,000 / Tape & Reel
CAT6217-330TDGT3	3.30 V	TSOT-23	3,000 / Tape & Reel

7. All packages are RoHS-compliant (Lead-free, Halogen-free).

8. The standard finish is NiPdAu.

9. The device used in the above example is a CAT6217-280TDGT3 (V_{OUT} = 2.80 V, in a TSOT-23 package, NiPdAu, Tape & Reel, 3,000/Reel).

10. For other voltage options, please contact your nearest ON Semiconductor Sales office.

11. For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

ON Semiconductor and with a registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer applications by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other application in which the failure of the SCILLC product create a stuation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death agosciated with such unintended or unauthorized use parts SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunit//Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303–675–2175 or 800–344–3860 Toll Free USA/Canada Fax: 303–675–2176 or 800–344–3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800–282–9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81–3–5773–3850

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative