

**OUTPUT: CMOS** 





Product Number (please contact us) SG2016CAN: X1G004801xxxx00 SG-210STF: X1G004171xxxx00 SG3225CAN: X1G005961xxxx15 SG5032CAN: X1G004451xxxx00 SG7050CAN: X1G004481xxxx00

## SG2016 / 3225 / 5032 / 7050CAN SG-210STF

Frequency
 Supply voltage
 Function
 Operating temperature
 20 standard frequencies
 1.8 V to 3.3 V Typ.
 Standby(¬¬)
 40 °C to +105 °C











SG2016CAN SG-210STF SG3 (2.0 x 1.6 mm) (2.5 x 2.0 mm) (3.2 x

SG3225CAN (3.2 x 2.5 mm)

SG5032CAN SG7050CAN (5.0 x 3.2 mm) (7.0 x 5.0 mm)

### Specifications (characteristics)

Item	Symbol	Specifications				Conditions / Remarks				
Output frequency	fo	14.7456 MHz 16 25 MHz 26	MHz MHz	20 MHz 27 MHz	12 MHz 24 MHz 32 MHz 50 MHz	12.288 MHz 24.576 MHz 33.33 MHz 72 MHz				
	Vcc	1.60 V to 3.63 V					4 MHz ≤ fo ≤ 50 MHz, T_use = +105 °C Max.			
Supply voltage		1.71 V to 3.63 V				fo = 72 MHz, T_use = +85 °C Max.			Refer to Figure 1	
		2.25 V to 3.63 V					fo = 72 MHz, T_use = +105 °C Max.			- Iguic i
0, ,	<b>T</b> (	-55 °C to +125 °C				SG2016CAN, SG3225CAN				
Storage temperature	T_stg	-40 °C to +125 °C				All others	6			
Operating temperature	T_use	-20 °C to +70 °C, -40 °C to +85 °C, -40 °C to +105 °C				+105 °C	See of figure *1			
		±25 × 10 <sup>-6</sup>				-20 °C to +70 °C				
Frequency tolerance	f_tol	±50 × 10 <sup>-6</sup>					-40 °C to +85 °C, -40 °C to +105 °C			
		V <sub>CC</sub> = 1.8 V ± 10 %	V <sub>CC</sub> =	2.5 V ± 10 9	% Vcc	= 3.3 V ± 10 %				
	Icc	1.5 mA Max.	1.6	mA Max.	1	.8 mA Max.	No load condition, 4 MHz ≤ fo ≤ 20 MHz			
Current consumption		1.8 mA Max.	2.0	mA Max.	2	.2 mA Max.	No load condition, 20 MHz < fo ≤ 40 MHz			
		2.1 mA Max. 2.4 mA Max. 2.6 mA Max. No load condition, 40 MHz < fo ≤ 50			Hz < fo ≤ 50 M	Hz				
		2.4 mA Max.	2.8	mA Max.	3	.0 mA Max.	No load condition, fo = 72 MHz			
Stand-by current	I_std	2.1 µA Max. 2.5 µA Max. 2.7 µA Max. $\overline{\text{ST}}$ =GND								
Symmetry	SYM	45 % to 55 %				50 % Vcd	e level, L_CMO	S ≤ 15 pF		
	V <sub>OH</sub>	90 % V <sub>CC</sub> Min.				1.8 V ± 10 %	2.5 V ± 10 %	3.3 V ± 10 %		
	V <sub>OL</sub>	10 % V <sub>CC</sub> Max.				I <sub>OH</sub>	-1.5 mA 1.5 mA	-3 mA 3 mA	-4 mA 4 mA	
Output voltage	V <sub>OH-2</sub>	V <sub>CC</sub> - 0.4 V Min.					1.8 V±10 %	2.5 V±10 %	3.3 V±10 %	
	V <sub>OL-2</sub>	0.4 V Max.					I <sub>OH</sub>	-3 mA 3 mA	-4 mA 4 mA	-6 mA 6 mA
Output load condition (CMOS)	L_CMOS	15 pF Max.				100				
	V <sub>IH</sub>	80 % V <sub>CC</sub> Min.				ST terminal				
Input voltage	VIL	20 % V <sub>CC</sub> Max.								
Rise time and Fall time	tr / tf	3 ns Max. 3.5 ns Max. (@1.8 V±10 %)				20 % V <sub>CC</sub> to 80 % V <sub>CC</sub> level, L_CMOS = 15 pF				
Start-up time	t_str	3 ms Max.				T = 0 at 90 % V <sub>CC</sub>				
Frequency aging	f_age	±3 × 10 <sup>-6</sup> / year Max.			+25 °C, First year					

[Model: SG2016/3225/5032/7050CAN]

- ①Model ②Output(C: CMOS) ③Frequency ④Supply voltage
- ⑤Frequency tolerance ⑥Operating temperature range
- ⑦Internal identification code("A" is default)

(4)Su	pply voltage	*See Figure 1
T	1.8 V to 3.3	
K	2.5 V to 3.3	V Typ.

⑤Frequency tolerance / ⑥Operating temperature range				
DB*	±25 × 10 <sup>-6</sup> / -20 °C to +70 °C			
JG	±50 × 10 <sup>-6</sup> / -40 °C to +85 °C			
JH	±50 × 10 <sup>-6</sup> / -40 °C to +105 °C			

<sup>\*</sup> Please refer to Product number list on Full Data Sheet for available frequencies

[Model: SG-210STF]

4 Frequency 5 Frequency tolerance

③Sι	ipply voltage	*See Figure 1	
Т	1.8 V to 3.3		

⑤Frequency tolerance				
S*	±25 × 10 <sup>-6</sup> / -20 °C to +70 °C			
L	±50 × 10 <sup>-6</sup> / -40 °C to +85 °C			
Υ	±50 × 10 <sup>-6</sup> / -40 °C to +105 °C			

<sup>\*</sup> Please refer to Product number list on Full Data Sheet for available frequencies

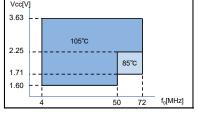


Figure 1 : The upper limit of Operating temperature and the related conditions

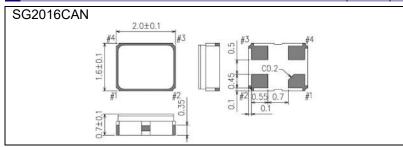
Please note that Supply voltage range ( $V_{\rm CC}$ ) depends on Output frequency (fo) and upper limit of Operationg temperature (T\_use Max.).

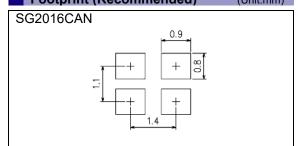
## **External dimensions**

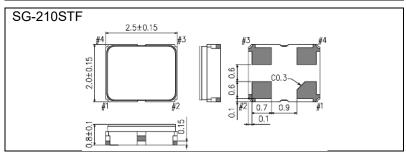
(Unit:mm)

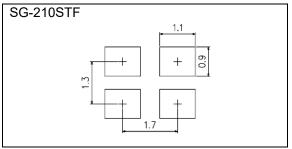
## Footprint (Recommended)

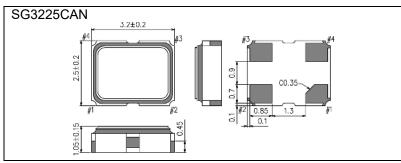
(Unit:mm)

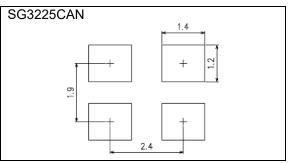


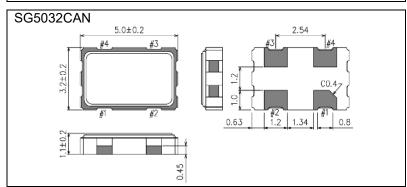


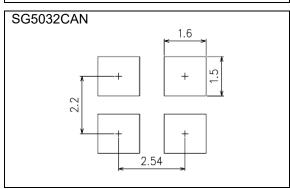


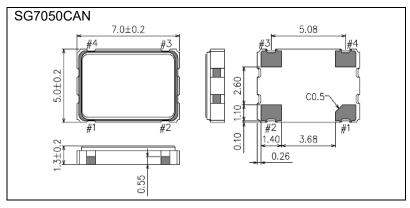


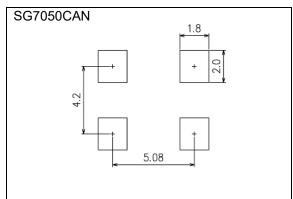












### Pin Map

Pin	Connection	Function					
		ST tern	ninal				
4	1 ST		ST function	Oscillator circuit	Output		
'		31		HIGH or "open"	Oscillation	Specified frequency: Enable	
			LOW	Oscillation stop	High impedance: Disable		
2	GND	Ground					
3	OUT	Clock o	utput				
4	V <sub>cc</sub>	Power s	supply				

■Notes: To maintain stable operation, provide a 0.01uF to 0.1uF by-pass capacitor at a location as near as possible to the power source terminal of the crystal product (between Vcc - GND).

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► Complies with EU RoHS directive.

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