

Description

The PAM8904Q is a piezo sounder driver designed for driving a ceramic/piezo sounder or speaker with $18V_{PP}$ from a 3V power supply. A programmable charge pump boost converter allows the PAM8904Q V_{OUT} to operate in 1x, 2x and 3x mode.

The boost converter operates at a fixed frequency of 1.0MHz and provides a 9V output with a minimum number of external components. PAMs unique drive technology ensures minimal inrush current, low EMI and high efficiency.

To ensure maximum efficiency and battery life, the PAM8904Q includes an auto standby feature which puts the device in sleep mode while there is no valid input signal. A forced shutdown is also available to enable the IC only when required.

To help protect the IC and system, the PAM8904Q includes overtemperature shutdown, overcurrent and short circuit protection.

The PAM8904Q is available in a 16 pin U-QFN3030-16 (Type B) package.

The PAM8904Q is qualified to AEC-Q100 Grade 2 and is Automotive Compliant supporting PPAPs.

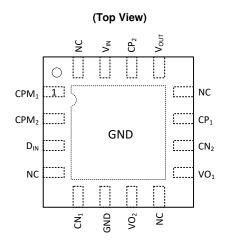
Features

Notes:

- Supply Voltage Range from 2.3V to 5V
- 18VPP Output from a 3V Supply
- Integrated Boost Converter Generates 9V Supply
- Wide Input Signal Capability 20Hz to 300kHz
- Low Current Consumption
- Automatic Standby and Wake-up Control
- Overtemperature Shutdown and Short-Circuit Protection
- Available in Space Saving Packages 16 pin U-QFN3030-16 (Type B) Package with Green Molding
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- The PAM8904Q is suitable for automotive applications requiring specific change control; this part is AEC-Q100 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.

https://www.diodes.com/quality/product-definitions/

Pin Assignments



U-QFN3030-16 (Type B)

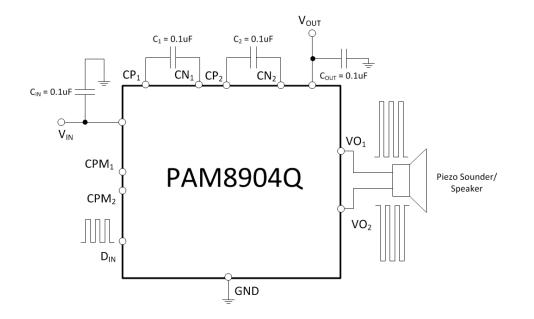
Applications

- Automotive Alarms
- Dash Board Audio
- GPS
- Electric Vehicle Approach Warning
- Automotive Security Device

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 - 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



Typical Applications Circuit



Pin Descriptions

Pin Number	Pin Name	I/O/P	Function
1	CPM ₁	I	Charge pump mode select 1
2	CPM ₂	I	Charge pump mode select 2
3	DIN	I	Signal Input
4	NC	_	No Connection
5	CN1	Ι	Capacitor 1 Negative Terminal
6	GND	Р	Ground
7	VO ₂	0	Positive Output
8	NC		No Connection
9	VO1	0	Negative Output
10	CN ₂	Ι	Capacitor 2 Negative Terminal
11	CP1	Ι	Capacitor 1 Positive Terminal
12	NC	_	No Connection
13	Vout	0	Boost Output
14	CP ₂	Ι	Capacitor 2 Positive Terminal
15	V _{IN}	Р	Power Supply
16	NC		No Connect
PAD	GND	Р	Recommended to connect to GND or No Connection



Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Symbol	Characteristics	Value	Unit
V _{IN}	Supply Voltage	-0.3 to +5.5	V
VI	CPM ₁ , CPM ₂	GND -0.3 to V _{IN} +0.3	V
θја	Thermal Resistance (Junction to Ambient)	35	°C/W
θ _{JC}	Thermal Resistance (Junction to Case)	14	°C/W
TJ	Operating Junction Temperature Range	-40 to +150	°C
Tstg	Storage Temperature Range	-65 to +150	°C

ESD Susceptibility

ESD Susceptionity				
HBM	Human Body Model	8	kV	
MM	Machine Model	600	V	
CDM	Charged Device Model	1	kV	

Caution: Stresses greater than the 'Absolute Maximum Ratings' specified above, may cause permanent damage to the device. These are stress ratings only; functional operation of the device at conditions between maximum recommended operating conditions and absolute maximum ratings is not implied. Device reliability may be affected by exposure to absolute maximum rating conditions for extended periods of time.

(Semiconductor devices are ESD sensitive and may be damaged by exposure to ESD events. Suitable ESD precautions should be taken when handling and transporting these devices.)

Recommended Operating Conditions (@TA = +25°C, unless otherwise specified.)

Symbol	Characteristics		Min	Мах	Unit
VIN	Supply Voltage		2.3	5.0	V
VIH	High-Level Input Voltage	CPM ₁ , CPM ₂	1.2	V _{IN} +0.3	V
VIL	Low-Level Input Voltage	CPM ₁ , CPM ₂	-0.3	+0.4	V
TA	Operating Free-Air Temperature		-40	+105	°C



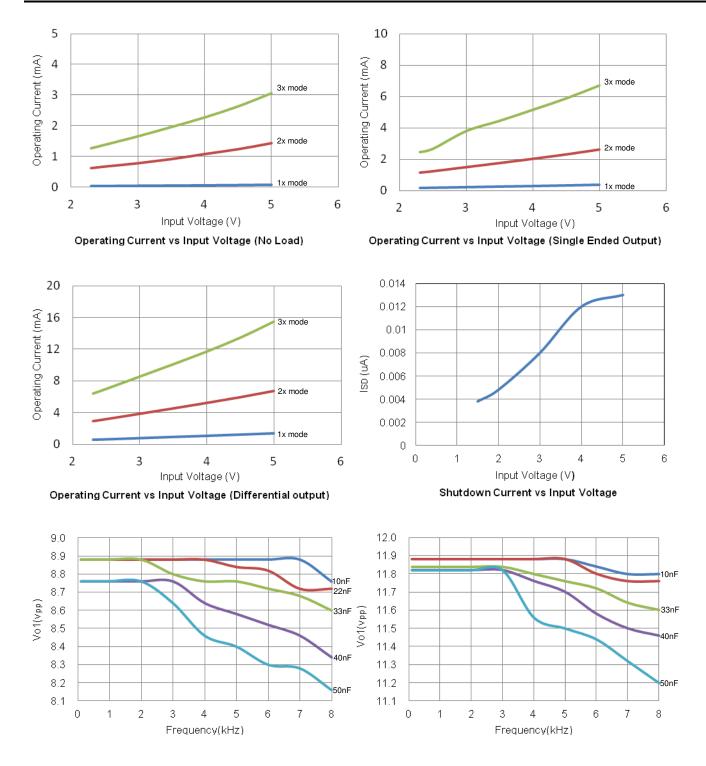
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Vout	Output Voltage Range	Note 4	2.1	_	12	V
Vout1		1x Mode	2.8	—	3	V
Vout2	Output Voltage	2x Mode	5.2	—	6	V
Vout3		3x Mode	7.2	_	9	V
IDD11		1x Mode, CPIEZO = No Load	_	50	_	μA
IDD12	Operating Current 1	2x Mode, CPIEZO = No Load	_	720	_	μA
IDD13		3x Mode, CPIEZO = No Load	—	1,700	_	μA
IDD21		1x Mode, Single-ended application	—	0.3	_	mA
IDD22	Operating Current 2	2x Mode, Single-ended application	_	1.4	_	mA
IDD23	7	3x Mode, Single-ended application	_	3.9	_	mA
IDD31		1x Mode, Differential application	_	0.9		mA
I _{DD32}	Operating Current 3	2x Mode, Differential application	_	3.8		mA
I _{DD33}		3x Mode, Differential application	_	8.5		mA
I _{SD}	Shutdown Current	D _{IN} = 0V	_	_	1	μA
fın	Input Frequency	Rectangular pulse on DIN	0.02	_	300	kHz
fosc	Oscillating Frequency	—	_	1		MHz
ton1		1x Mode, From DIN signal High to 90% Vout steady state	_	270	_	μs
t _{ON2}	V _{OUT} Start Delay Time	2x Mode, From DIN signal High to 90% $V_{\mbox{OUT}}$ steady state	_	320	_	μs
tons		$3x$ Mode, From D_{IN} signal High to 90% V_{OUT} steady state	_	350	_	μs
toff	Shutdown Delay Time	DIN = H- >L	—	42	_	ms
lsc	Output Short-Circuit Current	—	—	40	_	mA
OTP	Thermal Shutdown	Junction temperature	—	+150	—	°C
OTP _{HYS}	Thermal Shutdown Hysteresis	—	—	+37	_	°C
ViH	Control Terminal Voltage H	CPM1, CPM2, DIN pins	0.8 x V _{IN}	_	Vin	V
VIL	Control Terminal Voltage L	CPM1, CPM2, DIN pins	0	_	0.2 x V _{IN}	V
I _{IH1}	Control Terminal Current 1	$D_{IN} = 3V$	—	—	1	μA
I _{IH2}	Control Terminal Current 2	$VCPM_1$, $VCPM_2 = 3V$, $D_{IN} = 3V$	_	_	1	μA
Іінз	Control Terminal Current 3	$VCPM_1$, $VCPM_2 = 3V$, $D_{IN} = 0V$	_	_	1	μA
CL	Capacitive Load	—		15	50	nF

Note: 4. It is possible to drive V_{OUT}, VO₁ and VO₂ to 3x V_{DD}. A supply voltage of 4V of more should not be used in 3x mode as this will exceed the maximum output voltage rating.

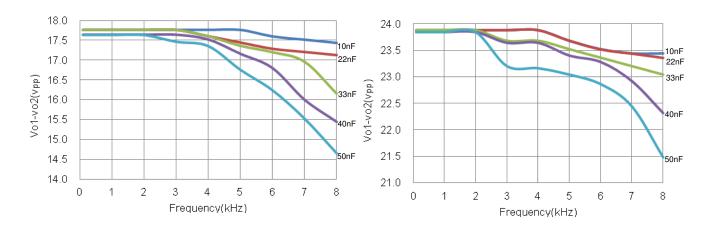


Performance Characteristics (@T_A = +25°C, V_{IN} = 3.0V, C_{Piezo} = 15nF, f_{DIN} = 4kHz, unless otherwise specified.)





Performance Characteristics (continued. @TA = +25°C, VIN = 3.0V, CPiezo = 15nF, fDIN = 4kHz, unless otherwise specified.)





Application Information

Charge Pump Mode Setting

The Charge Pump Mode (CPM) pins CPM₁ and CPM₂ are used to set the charge pump into mode $1 \times V_{DD}$, $2 \times V_{DD}$, $3 \times V_{DD}$ or they can be used to put the PAM8904Q in to a forced low current shutdown mode.

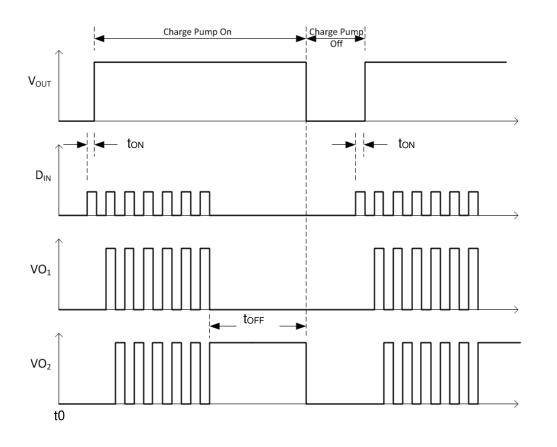
DIN	CPM ₁	CPM ₂	MODE
0	_	_	Shutdown Mode
1	0	0	Shutdown Mode
1	0	1	1x Mode
1	1	0	2x Mode
1	1	1	3x Mode

Care must be taken when using the 3x mode with a V_{DD} supply of 4V or more as this will force the V_{OUT} to exceed its maximum specification. The table shows the safe operating range of the PAM8904Q.

VDD Range	Mode
2.3V to 3.9V	1x, 2x and 3x
4.0V to 5.0V	1x and 2x only

Timing Chart and Device Operation

When one or both of the CPM pins are pulled high, the device enters normal operation mode, refer to the above table for the mode selection. Once the PAM8904Q senses a valid signal on the D_{IN} pin, the charge pump will start and provide the desired voltage on the V_{OUT} pin and the output drive VO_1 and VO_2 start to function after time t_{ON} which is typically between 270µs and 350µs depending on the mode chosen. Once the input signal on D_{IN} is removed, the PAM8904Q senses this and waits typically 42ms to ensure the signal has been removed. If there is no further valid signal within the time period t_{OFF} , the PAM8904Q enters into a low current standby mode.



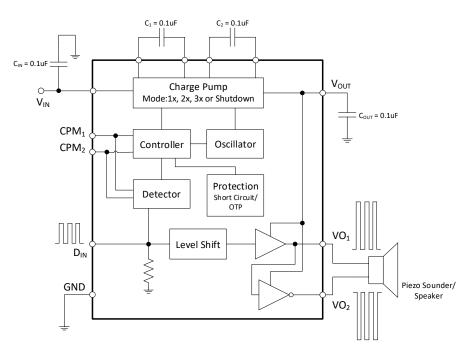


Application Information (continued)

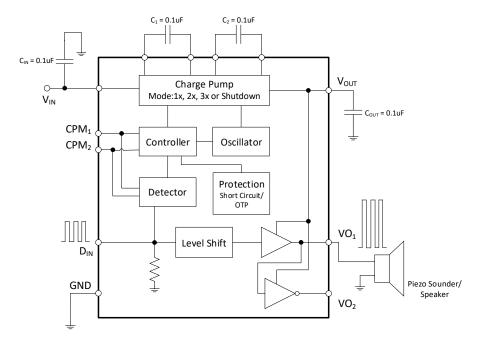
Output Configuration

The PAM8904Q is able to be configured either in a differential or a single ended configuration.

Differential Output Typical Application Circuit



Single Ended Output Typical Application Circuit



When using the PAM8904Q with a single ended output, the VO₂ pin should be left open circuit.



Application Information (continued)

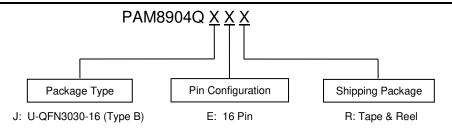
Short-Circuit Protection

The PAM8904Q has short circuit protection circuitry on the outputs to prevent damage. Once a short circuit is detected on the outputs the chip will limit the total current to protect the output device. This is not a latched fault; once the short is removed the normal operation is restored.

Thermal Protection

If the junction temperature of the PAM904Q exceeds +150°C the device will enter overtemperature shutdown. The outputs and the charge pump will be switched off. Once the junction temperature cools down to its normal operating condition, the IC will re-start automatically.

Ordering Information



David Niewskiew	Package	Package Type	7" Tape and Reel		Qualification	
Part Number	Code (Notes 5 & 6)		Quantity	Part Number Suffix	(Note 7)	
PAM8904QJER	J	U-QFN3030-16 (Type B)	3,000/Tape & Reel	R	Automotive Compliant	

Notes: 5. Pad layout shown at http://www.diodes.com/package-outlines.html.

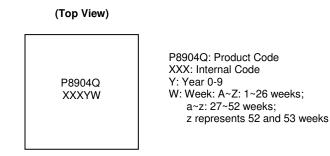
For Tape and reel information see https://www.diodes.com/assets/Packaging-Support-Docs/ap02007.pdf.
PAM8904Q has been qualified to AEC-Q100 grade 2 and is classified as "Automotive Compliant" supporting PPAP documentation. Automotive, AEC-Q100 and standard products are electrically and thermally the same, except where specified.

For more information, please refer to https://www.diodes.com/quality/product-definitions/.

See PAM8904 datasheet for commercial qualified versions.

Marking Information

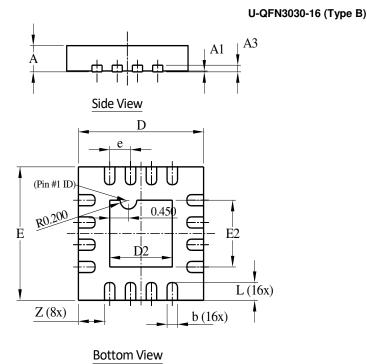
U-QFN3030-16 (Type B)





Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

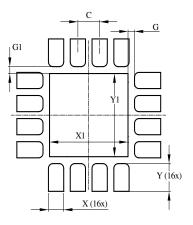


U-QFN3030-16							
	(Type B)						
Dim	Min	Max	Тур				
Α	0.55	0.65	0.60				
A1	0	0.05	0.02				
A3			0.15				
b	0.18	0.28	0.23				
D	2.95	3.05	3.00				
D2	1.40	1.60	1.50				
Е	2.95	3.05	3.00				
E2	1.40	1.60	1.50				
е		_	0.50				
L	0.35	0.45	0.40				
Z			0.625				
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-QFN3030-16 (Type B)



Dimensions	Value	
Dimensions	(in mm)	
С	0.500	
G	0.150	
G1	0.150	
Х	0.350	
X1	1.800	
Y	0.600	
Y1	1.800	



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