

integrated_™ **MAX6325/MAX6341/MAX6350**

1ppm/°C, Low-Noise, +2.5V/+4.096V/+5V Voltage References

General Description

The MAX6325/MAX6341/MAX6350 are low-noise, precision voltage references with extremely low, 0.5ppm/°C typical temperature coefficients and excellent, ±0.02% initial accuracy. These devices feature buried-zener technology for lowest noise performance. Load-regulation specifications are guaranteed for source and sink currents up to 15mA. Excellent line and load regulation and low output impedance at high frequencies make them ideal for high-resolution data-conversion systems up to 16 bits.

The MAX6325 is set for a 2.5V output, the MAX6341 is set for a 4.096V output, and the MAX6350 is set for a 5V output. All three provide for the option of external trimming and noise reduction.

Features

- ♦ Ultra Low, 1ppm/°C Max Tempco
- ♦ Very Low, 1.5µVp-p Noise (0.1Hz to 10Hz) (MAX6325)
- ♦ ±0.02% Initial Accuracy (MAX6350)
- ♦ ±15mA Output Source and Sink Current
- **♦ Low, 18mW Power Consumption (MAX6325)**
- ♦ Industry-Standard Pinout
- ♦ Optional Noise Reduction and Voltage Trim
- **♦ Excellent Transient Response**
- ♦ 8-Pin SO Package Available
- ♦ Low, 30ppm/1000hr Long-Term Stability
- ♦ Stable for All Capacitive Loads

19-1203; Rev 1; 1/01

_Applications

High-Resolution Analog-to-Digital and Digital-to-Analog Converters

High-Accuracy Reference Standard

High-Accuracy Industrial and Process Control

Digital Voltmeters

ATE Equipment

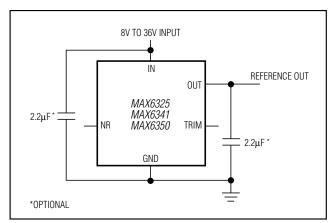
Precision Current Sources

Ordering Information

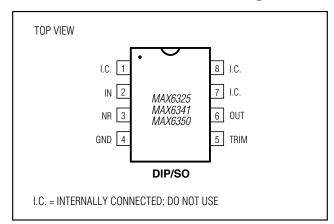
PART	TEMP. RANGE	PIN- PACKAGE	MAX TEMPCO (ppm/°C)
MAX6325CPA	0°C to +70°C	8 Plastic DIP	1.0
MAX6325CSA	0°C to +70°C	8 SO	1.0
MAX6325EPA	-40°C to +85°C	8 Plastic DIP	1.5
MAX6325ESA	-40°C to +85°C	8 SO	1.5
MAX6325MJA	-55°C to +125°C	8 CERDIP	2.5

Ordering Information continued at end of data sheet.

Typical Operating Circuit



Pin Configuration



For pricing, delivery, and ordering information, please contact Maxim Direct at 1-888-629-4642, or visit Maxim's website at www.maximintegrated.com.

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ABSOLUTE MAXIMUM RATINGS

(Voltages Referenced to GND)
IN0.3V to 40V
OUT, TRIM0.3V to 12V
NR0.3V to 6V
OUT Short-Circuit to GND Duration (V _{IN} ≤ 12V)Continuous
OUT Short-Circuit to GND Duration (V _{IN} ≤ 40V)5s
OUT Short-Circuit to IN Duration (V _{IN} ≤ 12V)Continuous
Continuous Power Dissipation ($T_A = +70^{\circ}C$)
8-Pin Plastic DIP (derate 9.09mW/°C above +70°C)727mW

8-Pin SO (derate 5.88mW/°C above	re +70°C)471mW
8-Pin CERDIP (derate 8.00mW/°C	above +70°C)640mW
Operating Temperature Ranges	
MAX63 C_ A	0°C to +70°C
MAX63 E_ A	40°C to +85°C
MAX63 MJA	55°C to +125°C
Storage Temperature Range	
Lead Temperature (soldering, 10s)	+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS—MAX6325

(VIN = +10V, IOUT = 0mA, TA = TMIN to TMAX, unless otherwise noted. Typical values are at TA = +25°C.)

PARAMETER	SYMBOL	CONDITIONS	TA	MIN	TYP	MAX	UNITS
Input Voltage Range	V _{IN}		C, E, M	8		36	V
Output Voltage	Vout	MAX6325	+25°C	2.499	2.500	2.501	V
		MAX6325C_A	С		0.5	1.0	ppm/°C
Output Voltage Temperature Coefficient (Note 1)	TCV _{OUT}	MAX6325E_A	Е		0.75	1.5	
Coemoioni (Note 1)		MAX6325MJA	M		1.0	2.5	1
			+25°C		10	18	
		9\\ < \\n\ < 10\\	С			30	1
		$8V \le V_{IN} \le 10V$	Е			35	1
Line Regulation (Note 2)	$\Delta V_{ ext{OUT}} /$		M			45	ppm/V
Line negulation (Note 2)	ΔV_{IN}	10V ≤ V _{IN} ≤ 36V	+25°C		2	5	- ppm/v
			С			7	
			Е			8	
			M			10	
	ΔVout/ Δlout	Sourcing: 0mA ≤ I _{OUT} ≤ 15mA	С		1	6	ppm/mA
			Е		1	7	
Load Regulation (Note 2)			M		3	15	
Load Regulation (Note 2)		Sinking: -15mA ≤ I _{OUT} ≤ 0mA	С		1	6	
			E		1	7	
			M		10	30	
Supply Current	IIN		+25°C		1.8	2.7	mA
Supply Sulferit	IIIV		C, E, M			3.0] ""^
Trim-Adjustment Range	$\Delta V_{ ext{OUT}}$	(Figure 1)	C, E, M	±15	±25		mV
Turn-On Settling Time	ton	To ±0.01% of final value	+25°C		5		μs
Output Noise Voltage (Note 3)		0.1Hz ≤ f ≤ 10Hz	+25°C		1.5		µVр-р
	en	10Hz ≤ f ≤ 1kHz	+25°C		1.3	2.8	μV _{RMS}
Temperature Hysteresis		(Note 4)	+25°C		20		ppm
Long-Term Stability	ΔV _{OUT} /t		+25°C		30		ppm/ 1000hr

1ppm/°C, Low-Noise, +2.5V/+4.096V/+5V Voltage References

ELECTRICAL CHARACTERISTICS—MAX6341

 $(V_{IN} = +10V, I_{OUT} = 0mA, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted. Typical values are at } T_A = +25^{\circ}C.)$

PARAMETER	SYMBOL	CONDITIONS	TA	MIN	TYP	MAX	UNITS
Input Voltage Range	VIN		C, E, M	8		36	V
Output Voltage	Vout	MAX6341	+25°C	4.095	4.096	4.097	V
G		MAX6341C_A	С		0.5	1.0	
Output Voltage Temperature Coefficient (Note 1)	TCV _{OUT}	MAX6341E_A	Е		0.75	1.5	ppm/°C
Coemoletii (Note 1)		MAX6341MJA	М		1.0	2.5	1
			+25°C		10	18	
		8V ≤ V _{IN} ≤ 10V	С			30	
		0	Е			35	
Line Regulation (Note 2)	ΔV _{OUT} /		M			45	nnm//
Line negulation (Note 2)	ΔVIN	10V ≤ V _{IN} ≤ 36V	+25°C		2	5	- ppm/V
			С			7	
			Е			8	
			M			10	
	ΔV _{OUT} / Δl _{OUT}	Sourcing: 0mA ≤ I _{OUT} ≤ 15mA	С		1	6	-ppm/mA
			Е		1	7	
Load Regulation (Note 2)			M		3	9	
Load negulation (Note 2)		Sinking: -15mA ≤ I _{OUT} ≤ 0mA	С		1	6	
			Е		1	7	
			M		7	18	
Cupply Current	Local		+25°C		1.9	2.9	mA
Supply Current	I _{IN}		C, E, M			3.2	IIIA
Trim-Adjustment Range	ΔV _{OUT}	(Figure 1)	C, E, M	±24	±40		mV
Turn-On Settling Time	ton	To ±0.01% of final value	+25°C		8		μs
Output Naiss Valtage (Nate 2)	_	0.1Hz ≤ f ≤ 10Hz	+25°C		2.4		μVр-р
Output Noise Voltage (Note 3)	en	10Hz ≤ f ≤ 1kHz	+25°C		2.0	4.0	μV _{RMS}
Temperature Hysteresis		(Note 4)	+25°C		20		ppm
Long-Term Stability	ΔV _{OUT} /t		+25°C		30		ppm/ 1000hr

1ppm/°C, Low-Noise, +2.5V/+4.096V/+5V Voltage References

ELECTRICAL CHARACTERISTICS—MAX6350

 $(V_{IN} = +10V, I_{OUT} = 0mA, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted. Typical values are at } T_A = +25^{\circ}C.)$

PARAMETER	SYMBOL	CONDITIONS	TA	MIN	TYP	MAX	UNITS
Input Voltage Range	VIN		C, E, M	8		36	V
Output Voltage	Vout	MAX6350	+25°C	4.999	5.000	5.001	V
		MAX6350C_A	С		0.5	1.0	
Output Voltage Temperature Coefficient (Note 1)	TCV _{OUT}	MAX6350E_A	Е		0.75	1.5	ppm/°C
Coefficient (Note 1)		MAX6350MJA	М		1.0	2.5	
			+25°C		10	18	
		01/ < 1/01/ 101/	С			30	
		$8V \le V_{IN} \le 10V$	E			35	
Line Regulation (Note 2)	ΔV _{OUT} /		М			45	nnm//
Line Regulation (Note 2)	ΔVIN		+25°C		2	5	ppm/V
		10V ≤ V _{IN} ≤ 36V	С			7	
			Е			8	1
			М			10	1
	ΔV _{OUT} / Δl _{OUT}	Sourcing: 0mA ≤ I _{OUT} ≤ 15mA	С		1	6	-ppm/mA
			Е		1	7	
Load Regulation (Note 2)			М		2	9	
Load Regulation (Note 2)		Sinking: -15mA ≤ I _{OUT} ≤ 0mA	С		1	6	
			Е		1	7	
			М		6	15	
Supply Current	lac		+25°C		2.0	3.0	mA
Supply Current	I _{IN}		C, E, M			3.3	1 1114
Trim-Adjustment Range	ΔV _{OUT}	(Figure 1)	C, E, M	±30	±50		mV
Turn-On Settling Time	ton	To ±0.01% of final value	+25°C		10		μs
Output Naise Valtage (Nate 2)		0.1Hz ≤ f ≤ 10Hz	+25°C		3.0		µVр-р
Output Noise Voltage (Note 3)	en	10Hz ≤ f ≤ 1kHz	+25°C		2.5	5.0	μV _{RMS}
Temperature Hysteresis		(Note 4)	+25°C		20		ppm
Long-Term Stability	ΔV _{OUT} /t		+25°C		30		ppm/ 1000hr

Note 1: Temperature coefficient is measured by the box method; i.e., the maximum ΔV_{OUT} is divided by $\Delta T \times V_{OUT}$.

Note 2: Line regulation (ΔV_{OUT} / (V_{OUT} x Δ_{VIN})) and load regulation (ΔV_{OUT} / (V_{OUT} x ΔI_{OUT})) are measured with pulses and do not include output voltage changes due to die-temperature changes.

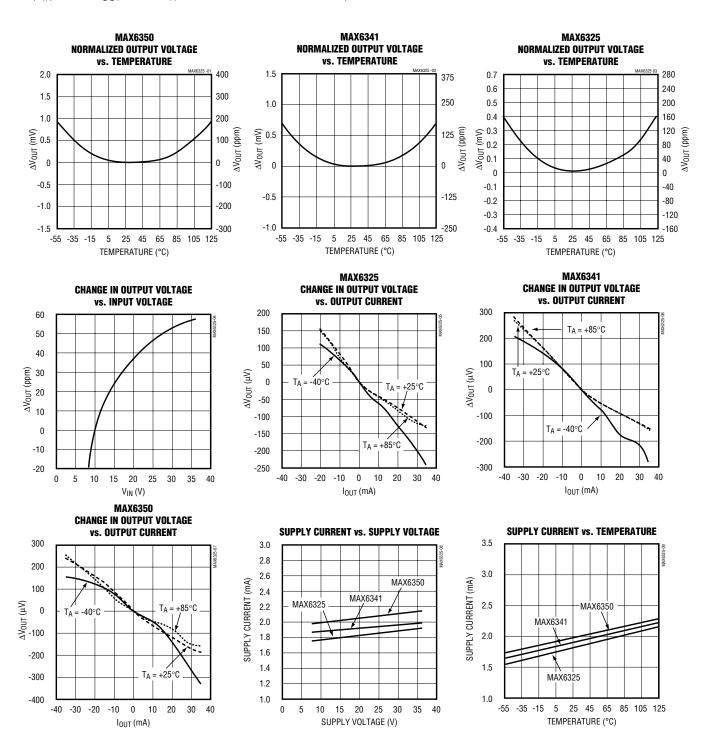
Note 3: Noise specifications are guaranteed by design.

Note 4: Temperature hysteresis is specified at $T_A = +25$ °C by measuring V_{OUT} before and after changing temperature by +25°C, using the plastic DIP package.

1ppm/°C, Low-Noise, +2.5V/+4.096V/+5V Voltage References

Typical Operating Characteristics

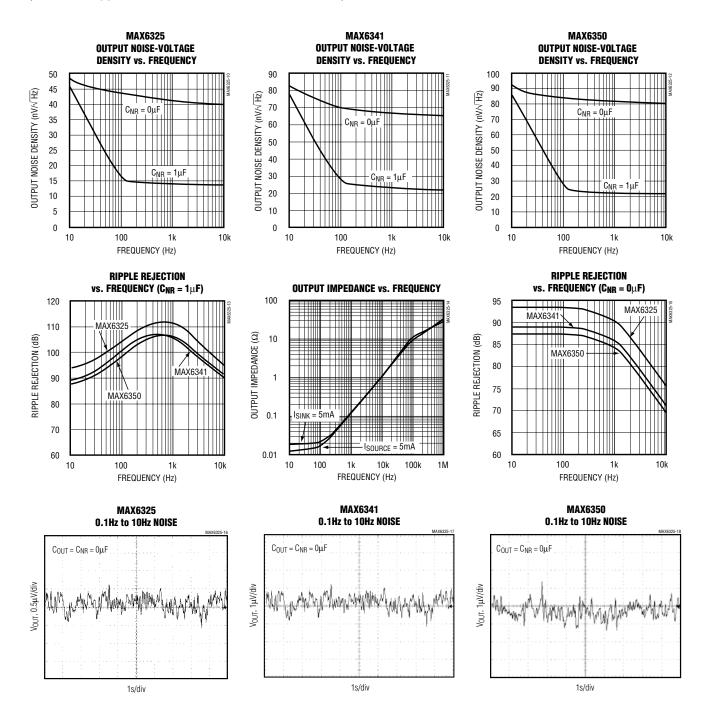
 $(V_{IN} = +10V, I_{OUT} = 0mA, T_A = +25$ °C, unless otherwise noted.)



1ppm/°C, Low-Noise, +2.5V/+4.096V/+5V Voltage References

Typical Operating Characteristics (continued)

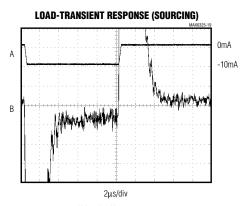
 $(V_{IN} = +10V, I_{OUT} = 0mA, T_A = +25$ °C, unless otherwise noted.)



1ppm/°C, Low-Noise, +2.5V/+4.096V/+5V Voltage References

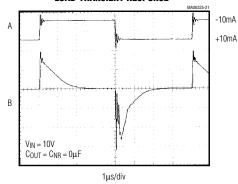
Typical Operating Characteristics (continued)

(VIN = +10V, IOUT = 0mA, TA = +25°C, unless otherwise noted.)



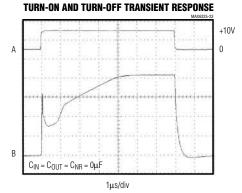
A: I_{OUT} , 10mA/div (SOURCING) B: V_{OUT} , 500 μ V/div

LOAD-TRANSIENT RESPONSE



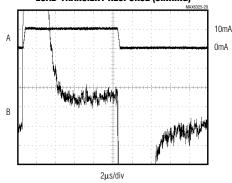
A: I_{OUT} (± 10 mA SOURCE AND SINK), 20mA/div, AC COUPLED B: V_{OUT} , 20mV/div, AC COUPLED

MAX6341



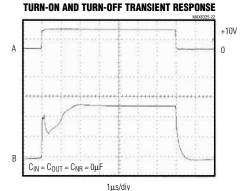
A: V_{IN}, 10V/div B: V_{OUT}, 1V/div

LOAD-TRANSIENT RESPONSE (SINKING)



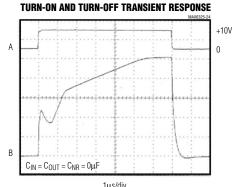
A: I_{OUT}, 10mA/div (SINKING) B: V_{OUT}, 500μV/div

MAX6325



A: V_{IN}, 10V/div B: V_{OUT}, 1V/div

MAX6350



A: V_{IN}, 10V/div B: V_{OUT}, 1V/div

1ppm/°C, Low-Noise, +2.5V/+4.096V/+5V Voltage References

Pin Description

PIN	NAME	FUNCTION
1, 7, 8	I.C.	Internally Connected. Do not use .
2	IN	Positive Power-Supply Input
3	NR	Noise Reduction. Optional capacitor connection for wideband noise reduction. Leave open if not used (Figure 2).
4	GND	Ground
5	TRIM	External Trim Input. Allows ±1% output adjustment (Figure 1). Leave open if not used.
6	OUT	Voltage Reference Output

Detailed Description

Temperature Stability

The MAX6325/MAX6341/MAX6350 are highly stable, low-noise voltage references that use a low-power temperature-compensation scheme to achieve laboratory-standard temperature stability. This produces a nearly flat temperature curve, yet does not require the power associated with heated references.

The output voltage can be trimmed a minimum of 0.6% by connecting a $10 k\Omega$ potentiometer between OUT and GND, and connecting its tap to the TRIM pin, as shown in Figure 1. The external trimming does not affect temperature stability.

8V TO 36V INPUT IN MAX6325 MAX6341 NR MAX6350 GND TRIM 10kΩ

Figure 1. Output Voltage Adjustment

Noise Reduction

To augment wideband noise reduction, add a 1µF capacitor to the NR pin (Figure 2). Larger values do not improve noise appreciably (see *Typical Operating Characteristics*).

Noise in the power-supply input can affect output noise, but can be reduced by adding an optional bypass capacitor to the IN pin and GND.

Bypassing

The MAX6325/MAX6341/MAX6350 are stable with capacitive load values from 0µF to 100µF, for all values of load current. Adding an output bypass capacitor can help reduce noise and output glitching caused by load transients.

Applications Information

Negative Regulator

Figure 3 shows how both a +5V and -5V precision reference can be obtained from a single, unregulated +5V supply. A MAX865 generates approximately ±9V to operate the MAX6350 reference and MAX400 inverting amplifier. The +5V is inverted by the ultra-low offset MAX400 op amp. Resistor R1 is optional, and may be used to trim the ±5V references. R2 and R4 should be matched, both in absolute resistance and temperature coefficient. R3 is optional, and is adjusted to set the -5V reference.

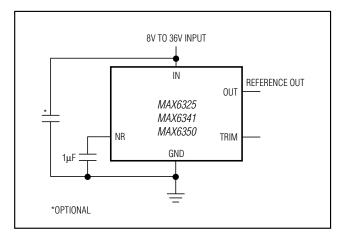


Figure 2. Noise-Reduction Capacitor

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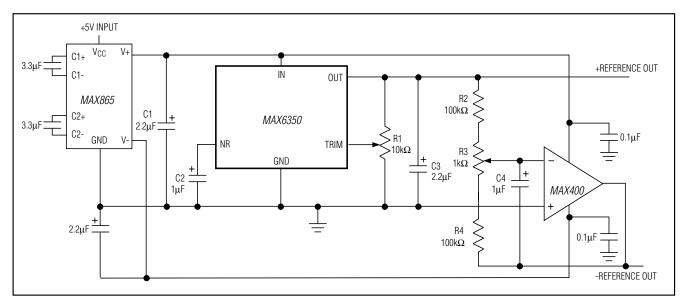


Figure 3. +5V and -5V References from a Single +5V Supply

Ordering Information (continued)

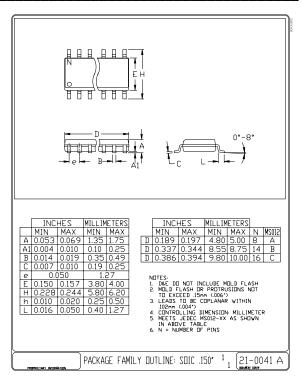
PART	TEMP. RANGE	PIN- PACKAGE	MAX. TEMPCO (ppm/°C)
MAX6341CPA	0°C to +70°C	8 Plastic DIP	1.0
MAX6341CSA	0°C to +70°C	8 SO	1.0
MAX6341EPA	-40°C to +85°C	8 Plastic DIP	1.5
MAX6341ESA	-40°C to +85°C	8 SO	1.5
MAX6341MJA	-55°C to +125°C	8 CERDIP	2.5
MAX6350CPA	0°C to +70°C	8 Plastic DIP	1.0
MAX6350CSA	0°C to +70°C	8 SO	1.0
MAX6350EPA	-40°C to +85°C	8 Plastic DIP	1.5
MAX6350ESA	-40°C to +85°C	8 SO	1.5
MAX6350MJA	-55°C to +125°C	8 CERDIP	2.5

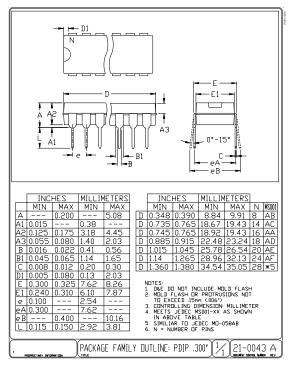
Chip Information

TRANSISTOR COUNT: 435

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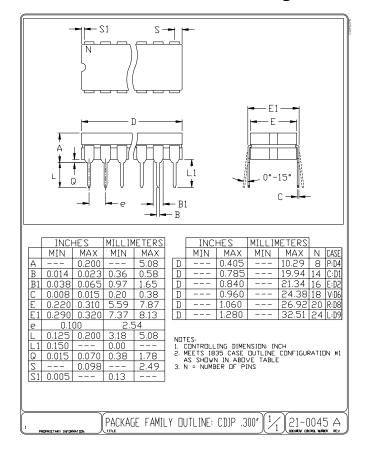
Package Information





1ppm/°C, Low-Noise, +2.5V/+4.096V/+5V Voltage References

Package Information (continued)





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