TOSHIBA Field-Effect Transistor Silicon N-Channel MOS Type

SSM3K309T

- O Power Management Switch Applications
- High-Current Switching Applications

• 1.8V drive

• Low on-resistance : $R_{on} = 47m\Omega$ (max) (@V_{GS} = 1.8V)

: $R_{on} = 35m\Omega \text{ (max) } (@V_{GS} = 2.5V)$

: $R_{on} = 31m\Omega \text{ (max) } (@V_{GS} = 4.0V)$

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Drain-source voltage		V_{DS}	20	$\mathcal{M}($	
Gate-source voltage		V_{GSS}	±12	V	
Drain current	DC	I _D	4.7		
	Pulse	I _{DP}	9.4		
Drain power dissipation		P _D (Note 1)	700	mW	
Channel temperature		T _{ch}	150	°C	
Storage temperature range	;	T _{stg}	-55 to 150	> ∘c	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Mounted on an FR4 board.

 $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 1.6 \text{ t}, \text{ Cu Pad: } 645 \text{ mm}^2)$

2.8:02 1.6:02 1.6:02 1.6:02 1. GATE 2. SOURCE 3. DRAIN JEDEC JEITA TOSHIBA 2.8:02 2.0760 1. GATE 2. SOURCE 3. DRAIN

Weight: 10 mg (typ.)

Electrical Characteristics (Ta = 25°C)

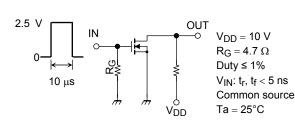
Chara	octeristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain, gourge breekdown voltage	V (BR) DSS	$I_D = 1 \text{ mA}, V_{GS} = 0$	20	_	_	V	
Drain–source breakdown voltage		V (BR) DSX	$I_D = 1 \text{ mA}, V_{GS} = -12 \text{ V}$	12	_		_
Drain cutoff current	√ >	I _{DSS}	V _{DS} =20 V, V _{GS} = 0	_	_	1	μА
Gate leakage curré	nt	/igss	$V_{GS} = \pm 12 \text{ V}, V_{DS} = 0$	_	_	±1	μА
Gate threshold volt	age	V _{th}	$V_{DS} = 3 \text{ V}, I_D = 1 \text{ mA}$	0.35	_	1.0	V
Forward transfer ad	dmittance	Y _{fs}	$V_{DS} = 3 \text{ V}, I_D = 4.0 \text{A}$ (Note2)	13	25	_	S
Drain-source ON-resistance		RDS (ON)	I _D = 4.0 A, V _{GS} = 4.0 V (Note2)	_	22	31	mΩ
			I _D = 3.0 A, V _{GS} = 2.5 V (Note2)	_	25	35	
			I _D = 1.0 A, V _{GS} = 1.8 V (Note2)	_	30	47	
Input capacitance		C _{iss}	V _{DS} = 10 V, V _{GS} = 0, f = 1 MHz	_	1020	_	pF
Output capacitance	:	Coss	V _{DS} = 10 V, V _{GS} = 0, f = 1 MHz	_	175	_	pF
Reverse transfer ca	apacitance	C _{rss}	V _{DS} = 10 V, V _{GS} = 0, f = 1 MHz	_	160		pF
Switching time	Turn-on time	t _{on}	V_{DD} = 10 V, I_D = 2A V_{GS} = 0 to 2.5 V, R_G = 4.7 Ω	_	23		- ns
	Turn-off time	t _{off}		_	34	_	
Drain-source forwa	ard voltage	V _{DSF}	$I_D = -4.7 \text{ A}, V_{GS} = 0$ (Note2)	_	-0.85	-1.2	V

Note2: Pulse test

Start of commercial production 2006-08

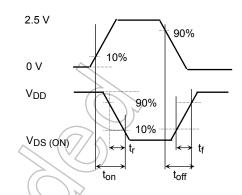
Switching Time Test Circuit

(a) Test Circuit

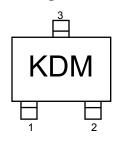


(b) V_{IN}

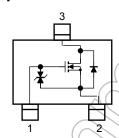
(c) V_{OUT}



Marking



Equivalent Circuit (top view)



Precaution

 V_{th} can be expressed as the voltage between gate and source when the low operating current value is I_D = 1 mA for this product. For normal switching operation, V_{GS} (on) requires a higher voltage than V_{th} and V_{GS} (off) requires a lower voltage than V_{th} .

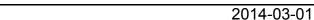
(The relationship can be established as follows: VGS (off) < Vth < VGS (on).)

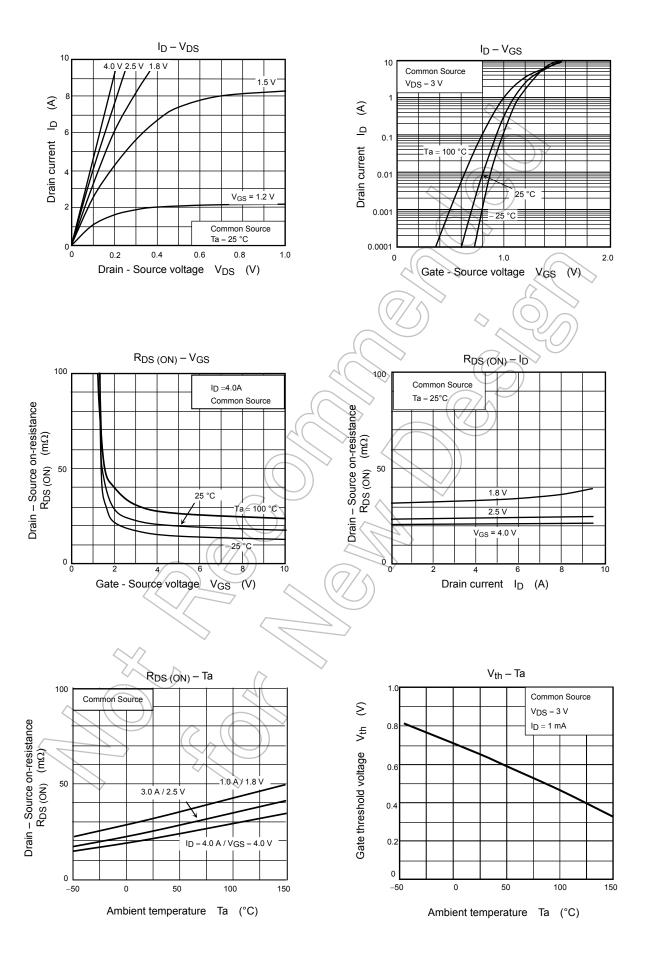
Take this into consideration when using the device.

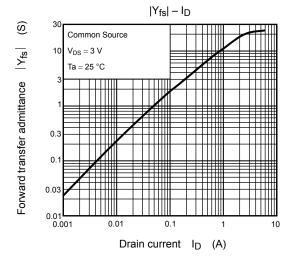
Handling Precaution

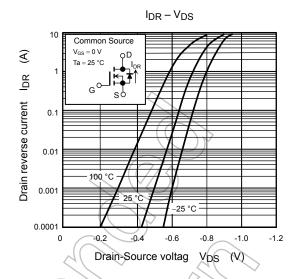
When handling individual devices that are not yet mounted on a circuit board, make sure that the environment is protected against electrostatic discharge. Operators should wear antistatic clothing, and containers and other objects that come into direct contact with devices should be made of antistatic materials.

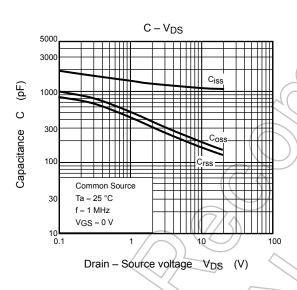
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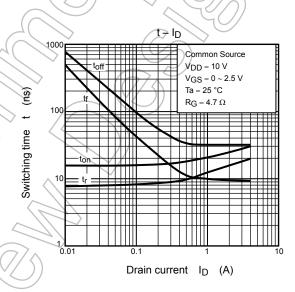


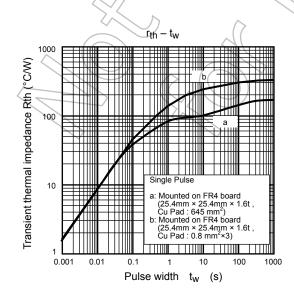


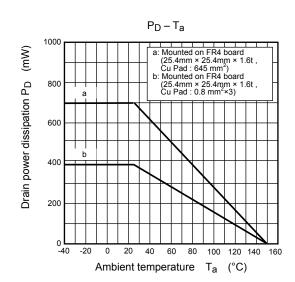












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