MAXIM

µP Power-Supply Monitor with Reset

MAX7705

_____General Description

In applications with microprocessors (µPs) that have a $\overline{RESET}$ input, the MAX7705 is functionally and socket compatible with the TL7705, but requires **no external components.** It provides power-supply glitch immunity and a guaranteed power-up reset delay, while typically consuming 1/27th the power from a +5V supply.

The MAX7705 monitors the power supply in µP and digital systems. The $\overline{RESET}$ output is valid for V_{CC} down to 1V. The device offers excellent circuit reliability and low cost by eliminating external components and adjustments.
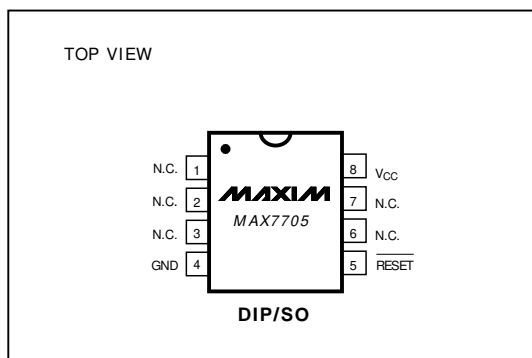
A system reset is provided during power-up, power-down, and brownout conditions. When V_{CC} is below the reset threshold, $\overline{RESET}$ is low and holds the µP in reset. $\overline{RESET}$ will go high 280ms after V_{CC} rises above the reset threshold. The MAX7705 is available in 8-pin DIP and SO packages.

FEATURE	MAX7705	TL7705
No. of External Components Required	**0**	3
Operating Supply Current +5V	**65µA**	1.8mA
Operating Supply Current +3V	**35µA**	1.8mA
Power-Supply Glitch Immunity	**Yes**	No
Guaranteed Minimum Reset Delay	**Yes**	No

_____Features

♦ **No External Components**

♦ **Low Cost**

♦ **Precise Reset Threshold**

♦ **280ms Power-On Reset Delay**

♦ **8-Pin DIP and SO Packages**

_____Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
MAX7705CPA	0°C to +70°C	8 Plastic DIP
MAX7705CSA	0°C to +70°C	8 SO
MAX7705C/D	0°C to +70°C	Dice*
MAX7705EPA	-40°C to +85°C	8 Plastic DIP
MAX7705ESA	-40°C to +85°C	8 SO

* Contact factory for dice specifications.

_____Applications

Minimum Component Count, Low-Cost Processor Systems

_____Pin Configuration

TOP VIEW

N.C.	1	8	V_{CC}
N.C.	2	7	N.C.
N.C.	3	6	N.C.
GND	4	5	$\overline{RESET}$

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DIP/SO

_____Typical Operating Circuit

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1	N.C.	V_{CC}	8
2	N.C.	N.C.	7
3	N.C.	N.C.	6
4	GND	$\overline{RESET}$	5

V_{CC}

µP

RESET

GND

* THE MAX7705 ELIMINATES THREE EXTERNAL COMPONENTS REQUIRED BY THE TL7705.

Call toll free 1-800-998-8800 for free samples or literature.
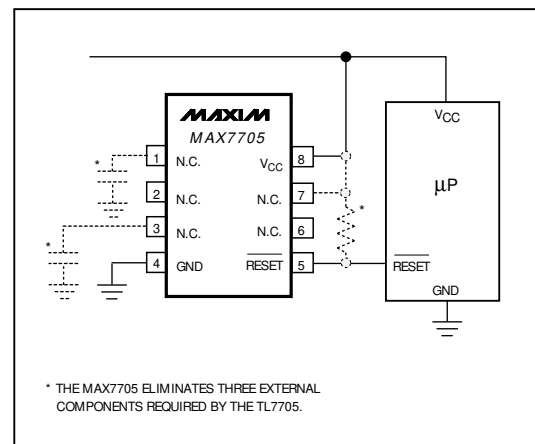
µP Power-Supply Monitor with Reset

ABSOLUTE MAXIMUM RATINGS

Terminal Voltage (with respect to GND)
V_{CC} ....-0.3V to 6.0V
$\overline{RESET}$...-0.3V to (V$_{CC}$ + 0.3V)
Input Current, V$_{CC}$ 20mA
Output Current, $\overline{RESET}$ 20mA
Rate-of-Rise, V$_{CC}$ 100V/µs
Continuous Power Disipation (T$_A$ = +70°C)
Plastic DIP (derate 9.09mW/°C above +70°C) 727mW
SO (derate 5.88mW/°C above +70°C)........................471mW

Operating Temperature Ranges:
MAX7705C_ _ ....0°C to +70°C
MAX7705E_ _ ....-40°C to +85°C
Storage Temperature Range ....-65°C to +160°C
Lead Temperature (soldering, 10sec) ....+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

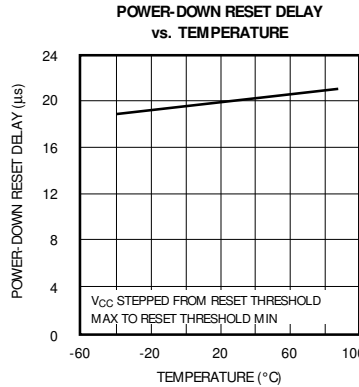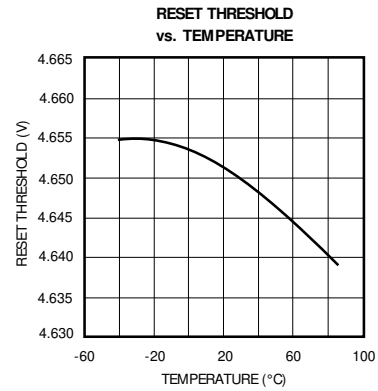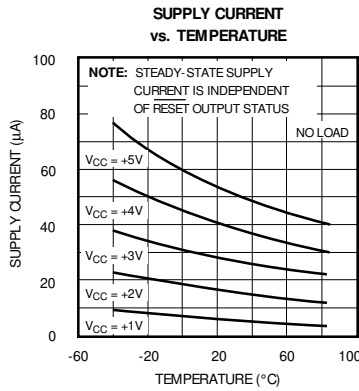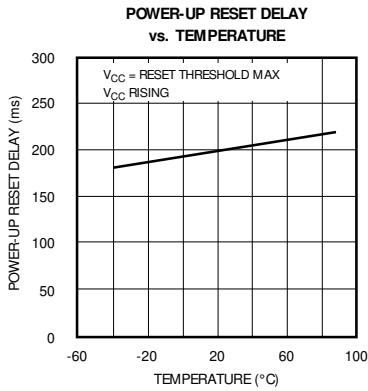(V$_{CC}$ = full range, T$_A$ = T$_{MIN}$ to T$_{MAX}$, unless otherwise noted.)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
V$_{CC}$ Range	MAX7705C	1.0		5.5	V
	MAX7705E	1.2		5.5	
Supply Current	MAX7705C, V$_{CC}$ < 5.5V		65	150	µA
	MAX7705E, V$_{CC}$ < 5.5V		65	200	
$\overline{RESET}$ Threshold		4.50	4.65	4.75	V
V$_{CC}$ to $\overline{RESET}$ Delay	V$_{CC}$ = reset threshold max to reset threshold min		20		µs
Reset Active Timeout Period	V$_{CC}$ = reset threshold max, V$_{CC}$ rising	140	280	560	ms
$\overline{RESET}$ Output Voltage	I$_{SINK}$ = 3.2mA, V$_{CC}$ = reset threshold min			0.4	V
	I$_{SINK}$ = 50µA, V$_{CC}$ ≥ 1.0V, MAX7705C			0.3	
	I$_{SINK}$ = 100µA, V$_{CC}$ ≥ 1.2V, MAX7705E			0.4	
	I$_{SOURCE}$ = 800µA, V$_{CC}$ ≥ reset threshold max	V$_{CC}$ - 1.5			

/VI/IXI/VI

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_____Typical Operating Characteristics

(T$_A$ = +25°C, unless otherwise noted.)

**POWER-UP RESET DELAY
vs. TEMPERATURE**

V$_{CC}$ = RESET THRESHOLD MAX
V$_{CC}$ RISING

**SUPPLY CURRENT
vs. TEMPERATURE**

NOTE: STEADY-STATE SUPPLY
CURRENT IS INDEPENDENT
OF RESET OUTPUT STATUS

NO LOAD

V$_{CC}$ = +5V
V$_{CC}$ = +4V
V$_{CC}$ = +3V
V$_{CC}$ = +2V
V$_{CC}$ = +1V

**RESET THRESHOLD
vs. TEMPERATURE**

**POWER-DOWN RESET DELAY
vs. TEMPERATURE**

V$_{CC}$ STEPPED FROM RESET THRESHOLD
MAX TO RESET THRESHOLD MIN

_____Pin Description

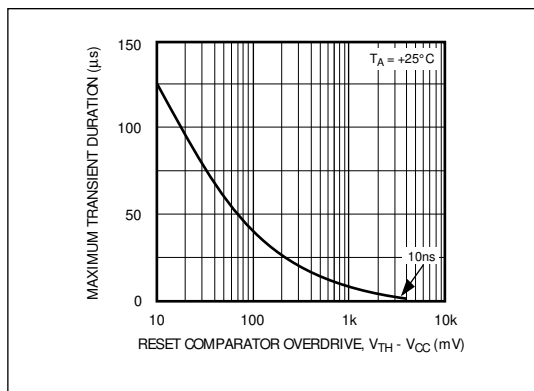| PIN	NAME	FUNCTION
1,2,3,6,7	N.C.	No Connection. There is no internal connection to these pins.
4	GND	Ground
5	$\overline{\text{RESET}}$	Reset Output remains low while V$_{CC}$ is below the reset threshold, and for 280ms after V$_{CC}$ rises above the reset threshold.
8	V$_{CC}$	+5V Supply Voltage

Figure 1. Maximum Transient Duration without Causing a Reset Pulse vs. Reset Comparator Overdrive

_____Applications Information

Replacing the TL7705 with a MAX7705 in an Existing Design

The MAX7705 has only three active pins: V_{CC}, GND, and $\overline{RESET}$. When using a TL7705 with a µP that has a $\overline{RESET}$ input, simply plug the MAX7705 into the same socket and omit the $\overline{RESET}$ pull-up resistor, reset timing capacitor, and reference bypass capacitor (see the _Typical Operating Circuit_).

The MAX7705 monitors the V_{CC} voltage and asserts reset whenever V_{CC} falls below the reset threshold. The reset power-up delay is created by an internal fixed oscillator. This delay is 100% tested and guaranteed over the full temperature range. The $\overline{RESET}$ output both sources and sinks current (see $\overline{RESET}$ Output Voltage in the _Electrical Characteristics_).

Negative-Going V_{CC} Transients

The MAX7705 asserts $\overline{RESET}$ during power-up, power-down, and brownout conditions. However, it is relatively immune to short-duration negative-going V_{CC} transients (glitches).

Figure 1 shows typical transient duration vs. reset comparator overdrive for which the MAX7705 does **not** generate a reset pulse. The graph was generated using a fast-edge, negative-going pulse applied to V_{CC}, starting 1.5V above the actual reset threshold and ending below the reset threshold by the magnitude indicated (reset comparator overdrive). It indicates the typical maxi-

mum pulse width a negative-going V_{CC} transient may have without causing a reset pulse to be issued. As the magnitude of the transient increases (goes farther below the reset threshold), the maximum allowable pulse width decreases. Typically, a V_{CC} transient that goes 100mV below the reset threshold and lasts 40µs or less will not cause a reset pulse to be issued.

A 0.1µF bypass capacitor mounted as close as possible to pin 2 (V_{CC}) provides additional transient immunity.

Ensuring a Valid $\overline{RESET}$ Output Down to V_{CC} = 0V

When V_{CC} falls below 1V, the MAX7705 $\overline{RESET}$ output no longer sinks current; it becomes high impedance. Therefore, high-impedance CMOS logic inputs connected to the $\overline{RESET}$ output can drift to indeterminate voltages. In most applications this presents no problem, as µP and other circuitry is generally inoperative with V_{CC} below 1V. In applications where the $\overline{RESET}$ output must be valid down to 0V, adding a pull-down resistor to the $\overline{RESET}$ pin (as shown in Figure 2) will cause any stray leakage currents to flow to ground, holding $\overline{RESET}$ low. The resistance value of R1 is not critical. It should be about 100kΩ, which is large enough not to load $\overline{RESET}$ and small enough to pull $\overline{RESET}$ to ground.
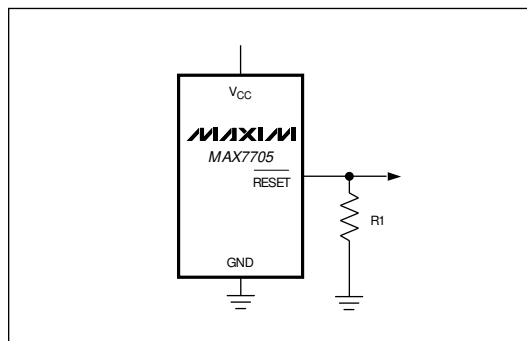

Figure 2. $\overline{RESET}$ Valid to V_{CC} = Ground Circuit

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Interfacing to µPs with Bidirectional Reset Inputs

µPs with bidirectional reset pins, such as Motorola's 68HC11 series, can contend with the MAX7705 reset output. If, for example, the MAX7705 $\overline{RESET}$ output is asserted high and the µP wants to pull it low, an indeterminate logic level may result. To correct this, connect a 4.7kΩ resistor between the MAX7705 $\overline{RESET}$ output and the µP reset I/O, as in Figure 3. Buffer the $\overline{RESET}$ signal to other system components.
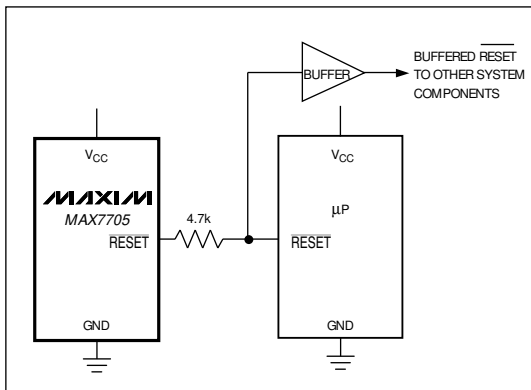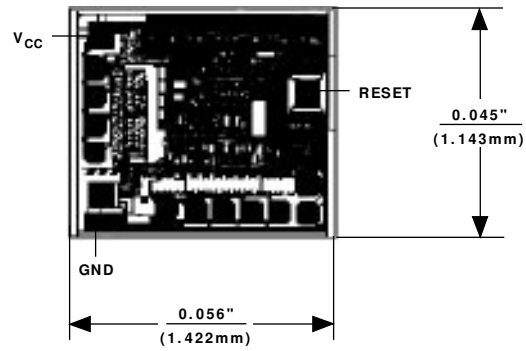
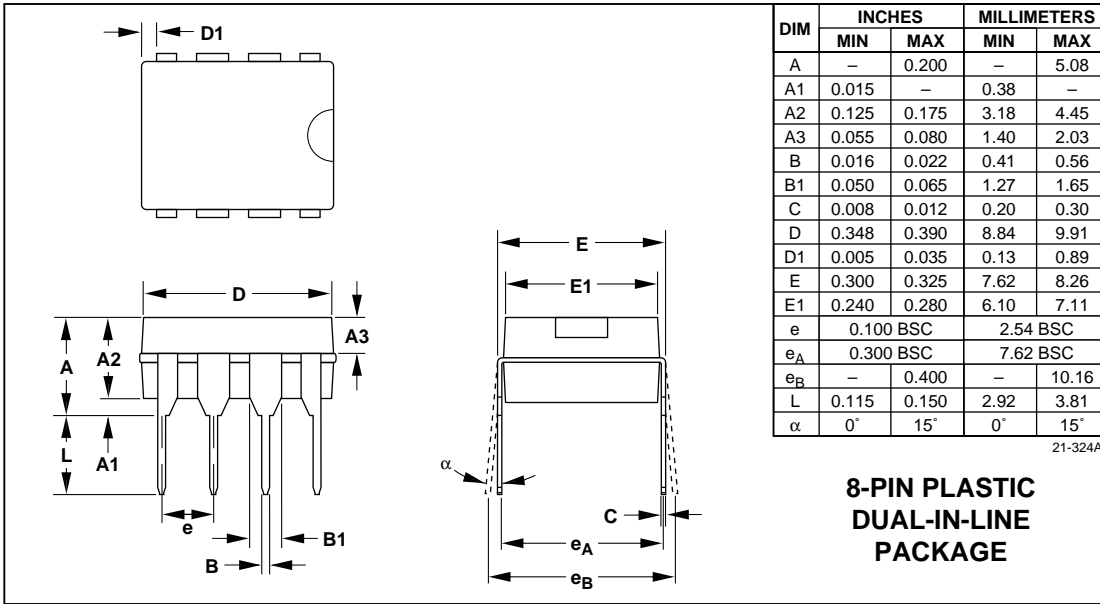Figure 3. Interfacing to µPs with Bidirectional Reset I/O

_____Chip Topography

TRANSISTOR COUNT = 380;
SUBSTRATE CONNECTED TO V_{CC}.
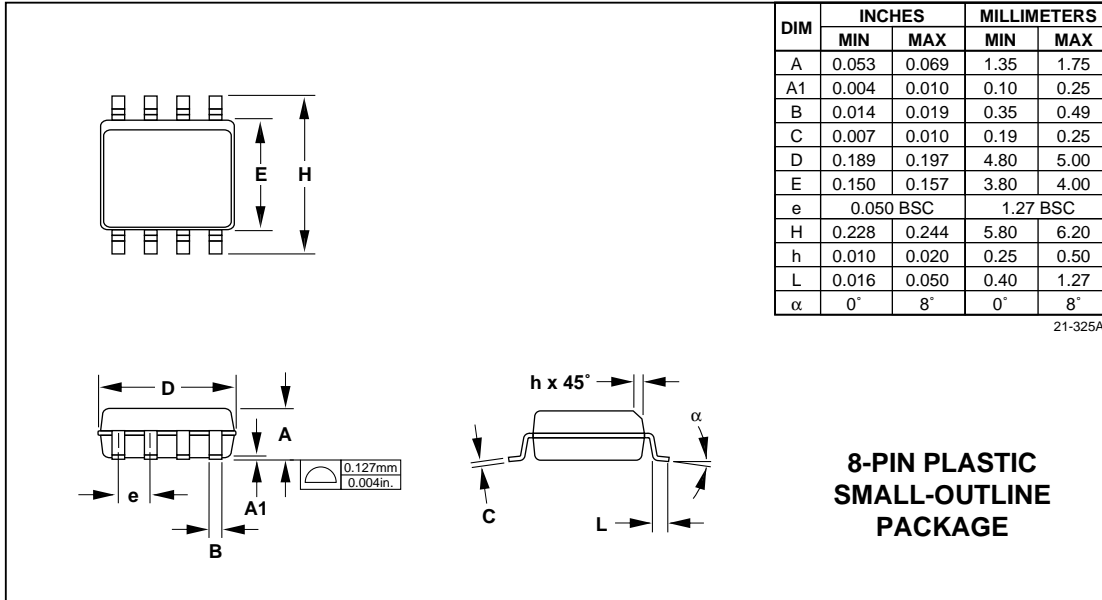
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DIM	INCHES		MILLIMETERS	
	MIN	**MAX**	**MIN**	**MAX**
A	–	0.200	–	5.08
A1	0.015	–	0.38	–
A2	0.125	0.175	3.18	4.45
A3	0.055	0.080	1.40	2.03
B	0.016	0.022	0.41	0.56
B1	0.050	0.065	1.27	1.65
C	0.008	0.012	0.20	0.30
D	0.348	0.390	8.84	9.91
D1	0.005	0.035	0.13	0.89
E	0.300	0.325	7.62	8.26
E1	0.240	0.280	6.10	7.11
e	0.100 BSC		2.54 BSC	
e_A	0.300 BSC		7.62 BSC	
e_B	–	0.400	–	10.16
L	0.115	0.150	2.92	3.81
α	0°	15°	0°	15°

21-324A

**8-PIN PLASTIC
DUAL-IN-LINE
PACKAGE**

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MAX7705

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.053	0.069	1.35	1.75
A1	0.004	0.010	0.10	0.25
B	0.014	0.019	0.35	0.49
C	0.007	0.010	0.19	0.25
D	0.189	0.197	4.80	5.00
E	0.150	0.157	3.80	4.00
e	0.050 BSC		1.27 BSC	
H	0.228	0.244	5.80	6.20
h	0.010	0.020	0.25	0.50
L	0.016	0.050	0.40	1.27
α	0°	8°	0°	8°

21-325A

**8-PIN PLASTIC
SMALL-OUTLINE
PACKAGE**

/MAXI/M

µP Power-Supply Monitor with Reset

µP Supervisory Circuits

Part Number	Nominal Reset Threshold (V)	Minimum Reset Pulse Width (ms)	Nominal Watchdog Timeout Period (sec)	Backup-Battery Switch	$\overline{CE}$-Write Protect	Power-Fail Comparator	Manual-Reset Input	Watchdog Output	Low-Line Output	Active-High Reset	Battery-On Output
MAX690A/692A	4.65/4.40	140	1.6	✔		✔					
MAX691A/693A	4.65/4.40	140/adj.	1.6/adj.	✔	✔/10ns	✔		✔	✔	✔	✔
MAX696	Adj.	35/adj.	1.6/adj.	✔		✔		✔	✔	✔	✔
MAX697	Adj.	35/adj.	1.6/adj.		✔	✔		✔	✔	✔	
MAX700	4.65/adj.	200	-				✔			✔	
MAX703/704	4.65/4.40	140	-	✔		✔	✔				
MAX705/706	4.65/4.40	140	1.6			✔	✔	✔			
MAX706P	2.63	140	1.6			✔	✔	✔		✔	
MAX706R/S/T	2.63/2.93/3.08	140	1.6			✔	✔	✔			
MAX707/708	4.65/4.40	140	-			✔	✔			✔	
MAX708R/S/T	2.63/2.93/3.08	140	-			✔	✔			✔	
MAX709L/M/R/S/T	4.65/4.40/2.63/2.93/3.08	140	-								
MAX791	4.65	140	1	✔	✔/10ns	✔	✔	✔	✔	✔	✔
MAX792L/M/R/S/T	4.65/4.40/2.63/2.93/3.08	140	1		✔/10ns	✔	✔	✔	✔	✔	
MAX800L/M	4.60/4.40	140	1.6/adj.	✔	✔/10ns	✔/±2%		✔	✔	✔	✔
MAX802L/M	4.60/4.40	140	1.6	✔		✔/±2%					
MAX805L	4.65	140	1.6	✔		✔				✔	
MAX813L	4.65	140	1.6			✔	✔	✔		✔	
MAX820L/M/R/S/T	4.65/4.40/2.63/2.93/3.08	140	1		✔/10ns	✔/±2%	✔	✔	✔	✔	
MAX1232	4.37/4.62	250	0.15/0.60/1.2			✔					
MAX1259	-	-	-	✔		✔					

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MAX7705

Part Number Table

Notes:

1. See the MAX7705 QuickView Data Sheet for further information on this product family or download the MAX7705 full data sheet (PDF, 70kB).
2. Other options and links for purchasing parts are listed at: http://www.maxim-ic.com/sales.
3. Didn't Find What You Need? Ask our applications engineers. Expert assistance in finding parts, usually within one business day.
4. Part number suffixes: T or T&R = tape and reel; + = RoHS/lead-free; # = RoHS/lead-exempt. More: See full data sheet or Part Naming Conventions.
5. * Some packages have variations, listed on the drawing. "PkgCode/Variation" tells which variation the product uses.

Part Number	Free Sample	Buy Direct	Package: TYPE PINS SIZE DRAWING CODE/VAR *	Temp	RoHS/Lead-Free? Materials Analysis
MAX7705EPA+				-40C to +85C	RoHS/Lead-Free: Yes
MAX7705EPA					RoHS/Lead-Free: No
MAX7705CWA				0C to +70C	RoHS/Lead-Free: No
MAX7705EWA				-40C to +85C	RoHS/Lead-Free: No
MAX7705CPA			PDIP;8 pin;.300" Dwg: 21-0043D (PDF) Use pkgcode/variation: P8-1*	0C to +70C	RoHS/Lead-Free: No Materials Analysis
MAX7705CPA+			PDIP;8 pin;.300" Dwg: 21-0043D (PDF) Use pkgcode/variation: P8+1*	0C to +70C	RoHS/Lead-Free: Yes Materials Analysis

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